

CONTROL DATA° FIELD TEST UNIT TB304

GENERAL DESCRIPTION
OPERATION
THEORY OF OPERATION
MAINTENANCE
DIAGRAMS
WIRE LISTS
PARTS DATA

	REVISION RECORD		
REVISION	DESCRIPTION		
01	Preliminary kelease		
(6-1-76)			
Α	Manual Released. Includes ECO's 48212, 48217, 48225, 48238.		
(9-15-76)			
В	Add ECO PE48224 for 50-pin I/O cables, make miscellaneous editorial corrections		
(11-29-76)	as indicated by "B" REV column of List of Effective Pages (page iii).		
C	Include models B and C, each with MMD capability. Provide ruggedized packag-		
(4-15-77)	ing. ECOs incorporated: PE48435, PE48474, PE48476, PE54002, PE50059, PE54009.		
	This edition obsoletes all previous editions.		
D	Incorporate ECOs PE54015, 54018, 54023, 54029. Add part numbers for		
(6-2-77)	FTU models. Add clarification to certain operating/maintenance procedures.		
	Make miscellaneous editiorial changes, including consistency in nomenclature		
	on logic diagrams.		
	KØR 0639		

REVISION LETTERS I,O,Q AND X ARE NOT USED

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or use Comment Sheet in the back of this manual.

LIST OF EFFECTIVE PAGES

Sheet_1_ of 2_

New features, as well as changes, deletions, and additions to information in this manual are indicated by bars in the margins or by a dot near the page number if the entire page is affected. A bar by the page number indicates pagination rather than content has changed.

Cover	PAGE	REV
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PAGE	REV
3-8 3-9 3-10 3-11 3-12 3-13 3-14 3-15 3-16 3-17 3-18 3-19 3-20 3-21 3-22 3-22 3-22 3-22 3-22 3-22 3-23 3-24 3-25 3-27 3-28 3-29 3-30 3-31 3-32 3-34 3-35 3-37 3-38 3-37 3-38 3-40 3-41 3-42 3-44 3-45 5-4 Div Blank 4-1 4-2 4-3 Blank S-5 Div S-0	0 0 0 0 0 0 0 0 0 0

5-1 5-2 Div Blank 5-3 5-4 5-5 5-6 5-7 5-8 5-9 5-10 5-12 5-13 5-14 5-15 5-16 5-17 5-18 5-19 5-20 5-21 5-22 5-23 5-24 5-25 5-27 5-28 5-29 5-20 5-21 5-22 5-23 5-24 5-25 5-27 5-28 5-29 5-30 5-31 5-32 5-31 5-32 5-34 5-35 5-36 5-37 5-38 5-39 5-40 5-41 5-45 D
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PAGE	REV
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5-91 Blank S-6 Div Blank 6-1 Blank Div Blank 6-3 6-4 6-5 6-6 6-7 6-8 6-9 6-10 6-12 6-13 6-14 6-15 6-16 6-17 6-18 6-19 6-20 6-21 6-22 6-23 6-24 6-25 6-26 6-27 6-28 6-29 6-30 6-31 6-32 6-33 6-34 DBlank 6-35 6-37	0 0 0 0 0 0 0 0 0 0

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LIST OF EFFECTIVE PAGES

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PAGE	REV
6-38 6-39 6-40 6-41 6-42 6-43 6-44 6-45 6-47 6-48 6-50 6-51 6-52 6-53 6-55 6-57 6-58 6-57 6-60 6-61 6-62 6-63 6-66 6-67 8-7 nk 7-1 7-23 7-11 Blank Sh Rtn Env	

PAGE	REV
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PAGE	REV	
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PREFACE

This manual has been prepared for customer engineers and other technical personnel who will be using the TB304 Field Test Unit (FTU) to assist in troubleshooting the Storage Module Drive (SMD) and the Mini-Module Drive (MMD). The FTU is available in three versions:

- TB304A Exercise/test SMDs only, plus head alignment capability.
- TB304B Exercise/test both SMDs and MMDs, plus head alignment capability for SMDs.
- TB304C Exercise/test both SMDs and MMDs; no head alignment capability.

All B and C versions of the TB304, and all A versions with serial numbers 201 and above, are ruggedized to minimize damage during transportation and handling.

Personnel using this manual should already be familiar with the computer system, drive controller and drive logic, as well as system programming techniques for executing I/O operations, including the proper sequencing of I/O commands and signals between the drive and its controller.

The manual is divided into seven sections as follows:

Section 1 - General Description. Contains the physical description and functional specifications for the FTU.

- Section 2 Operation. Provides installation information and procedures for operating the FTU in all modes, as well as steps to take when error lights appear on the FTU control panel.
- Section 3 Theory of Operation. Makes extensive use of flowcharts to show the FTU logic sequencing during the various operating modes.
- Section 4 Maintenance. Contains procedures for adjusting the ±5 V dc supplies and describes how to use sections 2, 3, and 5 to troubleshoot the FTU.
- Section 5 Diagrams. Contains logic diagrams, schematic diagrams of the power supplies and "locator" drawings showing the physical lay-out of the parts in each electrical assembly.
- Section 6 Wire List. Shows point-topoint wiring of the ICs on the logic board.
- Section 7 Parts Data. Provides an exploded view of the TB304 and a breakdown of field replaceable parts.

CONTENTS

1. GENERAL DESCRIPTION		Procedure 6: Check Data-Error	
Introduction	1-1	Logic	2-14
Specifications	1-1	Procedure 7: Trouble Analysis	2-15
Functional Description	1-1	Procedure 8: Using the Head Alignment Card	2-15
Access Modes	1-1	3. THEORY OF OPERATION	
Direct Seek	1-1	Introduction	3-1
Continuous Seek	1-1	I/O Lines	3-1
Sequential Forward Seek	1-1	Salient Logic Elements	3-5
Sequential Reverse Seek	1-1	Access (Seek) Modes	3-8
Random Seek	1-1	General	3-8
Read/Write Modes	1-1	Sequential Reverse (Seq Rev) Seek	3-9
Write Format	1-1	Sequential Forward (Seq Fwd) Seek	3-12
Write	1-2	Continuous (Cont) Seek	3-14
Read	1-2	Direct Seek	3-17
Write Then Read	1-2	Random (Rand) Seek	3-19
2. OPERATION		Read/Write Operations	3-21
Introduction	2-1	Write Format Sequence	3-21
Installation	2-1	Read Sequence	3-21
Operating Voltage	2-1	Write Sequence	3-34
I/O Cables	2-1	Offset Mode/Delayed On Cylinder	3-41
Head Alignment Cable	2-1	FTU Error Logic	3-43
Controls and Indicators	2-4	Address Error/Data Error Logic	
Operation	2-11	Address Errors	3-43 3-43
General	2-11	Address Mark Error	3-43
Preliminary Set-up	2-11	Sync Bit Error	3-43
Operating Procedures	2-12	Address Word Error	3-43
Procedure 1: Continuous Seek,	2 12	Data Errors	
No R/W	2-12		3-43
Procedure 2: Random Seek, No R/W	2-12	Read Clock Error	3-43
Procedure 3: Write Format	2-13	Data Word Error	3-43
Procedure 4: Read	2-13	Servo Clock Error Logic	3-43
Procedure 5: Write Flag	2-13	Sector Mark Error Logic	3-44

33319600 c

Normal Sequence	3-44	Word Cntr, End of Adrs Fld, R/W Reset Control	5~15
Abnormal Sequence	3-44	Word Mux	5-16
Unit Select/Release Logic	3-44	Pattern Register	5-17
Fixed-Head Operations	3-45	Read Data Compare	5-18
Head Alignment Card (HFSV)	3-45	Servo Clk, Addrs, Data Error Detection	5-19
4. MAINTENANCE		Sector Counting and Error Detection	5-20
Power Supply Voltage Checks	4-1	Character Cntr, Servo Clk, FTU Wrt Clk	5-21
Overvoltage Adjustment Procedure	4-1	Display Mux	5-22
Voltage Adjustment Procedure	4-1	Bus Out Mux	5-23
Troubleshooting the FTU Logic	4-2	Receivers I	5-24
General	4-2	Receivers II	5-25
Using the Testpoints	4-2	Transmitters I	5-26
Cylinder Select	4-2	Transmitters II	5-27
Head Select	4-2	Logic Board IC Placement	5-28
Address Mark	4-2	Logic Brd to Control Panel Cabling	5-29
Write Clock/Data	4-3	Brd to Panel Enabling, JC31 to J10,	J-29
Read Clock/Data	4-3	11, 12	5-30
Read Gate/Write Gate	4-3	Brd to Panel Cabling, JD31 to J7,8,9	5-31
5. DIAGRAMS		Brd to Panel Cabling, JE31 to J4,5,6	5-32
Key to Logic Symbology and Logic		Brd to Panel Cabling, JF31 to J1,2,3	5-33
Interconnections	5-1	Control Panel	5-34
Key to Logic Flat-cable and IC Connector Pin Arrangement	5-2	Control Panel Indicators (LEDs)	5-35
TB304 B/C DIAGRAMS		Control Panel Switches I	5-36
Logic Board	5-3	Control Panel Switches II	5-37
Start Control, RTZ	5-4	Control Panel Switches III	5-38
Access Control, Index Detection	5 -5	Control Panel Test Points	5-39
Access Timing, Increment Cyl Adrs Reg	5-6	I/O Cabling	5-40
Cylinder Address Register	5-7	A-Cable, Connector J3	5-41
Cylinder Address Mux	5-8	B-Cable, Connector J2	5-42
Head Address Register	5-9	I/O Bypass Cable	5-43
Offset Control, On Cyl Detection	5-10	Power Supply Layout	5-44
Rd/Wrt Operation Start Control	5-11	Power Supply; Wiring Diagram	5-45
Rd/Wrt Format, Control I	5-12	Schematic Diagrams; ±5 V Power Supplies	5-46
Rd/Wrt Format, Control II	5-13	Head Alignment Card (HFSV), Sheet 1	5-47
Bit Cntr, Wrt Sync/Found Sync, Addr Mk Logic	5-14		

viii

TB304 A DIAGRAMS		Bus Out Mux	5-71
Logic Board	5-51	Receivers I	5-72
Start Control, RTZ	5-52	Receivers II	5-73
Access Control, Index Detection	5-53	Transmitters I	5-74
Access Timing, Increment Cyl Adrs Reg	5-54	Transmitters II	5-75
Cylinder Address Register	5-55	Logic Board IC Placement Logic Brd to Control Panel Cabling	5-76 5-77
Cylinder Address Mux	5-56	Brd to Panel Cabling, JC31 to J10,	
Head Address Register	5-57	11, 12	5-78
Offset Control, On Cyl Detection	5-58	Brd to Panel Cabling, JD31 to J7,8,9	5-79
Rd/Wrt Operation Start Control	5-59	Brd to Panel Cabling, JE31 to J4,5,6	5-80
Rd/Wrt Format, Control I	5-60	Brd to Panel Cabling, JF31 to J1,2,3	5-81
Rd/Wrt Format, Controll II	5-61	Control Panel	5-82
Bit Cntr, Wrt Sync/Found Sync, Addr	5-62	Control Panel Indicators (LEDs)	5-83
MK Logic	5-62	Control Panel Switches I	5-84
Word Cntr, End of Adrs Fld, R/W Reset Control	5-63	Control Panel Switches II	5-85
Word Mux	5-64	Control Panel Switches III	5-86
Pattern Register	5-65	Control Panel Test Points	5-87
Read Data Compare	5-66	I/O Cabling	5-88
Servo Clk, Adrs, Data Error Detection	5-67	A-Cable, Connector J3	5-89
Sector Counting and Error Detection	5-68	B-Cable, Connector J2	5-90
Character Cntr, Servo Clk, FTU Wrt Clk	5-69	I/O Bypass Cable	5-91
Display Mux	5-70		

83319600 D ix

6. W	IRE LIST		Logic	Board Wire Wrap List, TB304 A	6-37			
Introduction 6-1		7. PARTS DATA						
Logic	Board Wire Wrap List, TB304 B/C	6-3	Introduction					
	FIGURES							
2-1	TB304 and Associated Hardware	2-2	3-8	Write Format Track Pattern	3-23			
2-2	TB304 Control Panel	2-5	3-9	Write Format Flowchart	3-24			
2-3	Track Format	2-14	3-10	Read Track Pattern	3-30			
			3-11	Read Flowchart	3-31			
2-4	Head Alignment Card	2-20	3-12	Write Track Pattern	3-35			
3-1	TB304 Block Diagram	3-4	3-13	Write Flowchart	3-36			
3-2	Sequential Reverse Seek Flow-chart	3-10	3-14	Offset Mode/Pseudo ON CYLINDER Signal Flowchart	3-42			
3-3	Sequential Forward Seek Flow- chart	3-13	7-1	TB304 Final Assembly, Standard Package	7-2			
3-4	Continuous (Alternate) Seek Flowchart	3-15	7-2	Control Panel, Standard Package	7-2			
3-5	Direct Seek Flowchart	3-18	7-3	TB304 Final Assembly, Ruggedized Package	7-3			
3-6	Random Seek Flowchart	3-20	7-4	Control Panel, Ruggedized Package	7-8			
3-7	Track Format and Basic R/W Timing	3-22	7-5	Component Assembly, 5VKN	7-10			
		TABL	ES					
1-1	TB304 Specifications	1-2	2-4	Trouble Analysis (Decision Logic Table)	2-17			
2-1	Connecting I/O Bypass Cable to Drive	2-1	3-1	I/O Lines	3-1			
2-2	Head Alignment	2-4	3-2	Salient Logic Elements	3-5			
2-3	Setting Sector Switches	2-11	3-3	Access Timing Functions	3-8			

x 83319600 D

SECTION 1

GENERAL DESCRIPTION

INTRODUCTION

The CONTROL DATA® TB304 Field Test Unit (FTU) is a portable, self-contained tester for exercising and/or simulating on-line operations in Storage Module Drives (SMDs), series BJ4, BJ5, BJ7, BK4, BK5, BK6 and BK7, as well as Mini-Module Drives (MMDs) in the BZ3 or BZ4 series.

The FTU is housed in a suitcase-type carrying case that provides a control panel, a logic board, and an integral power supply. The case contains space for storing the power cable, all necessary I/O cables, and a head alignment card.* The head alignment card plugs into the logic chassis of the SMD and permits individual head alignment, using a null meter mounted in the FTU control panel. In addition, a special I/O bypass cable is provided that enables head alignment and servo maintenance to be performed without disconnecting the I/O cables between the drive and its controller.

SPECIFICATIONS

Specifications for the TB304 are given in table 1-1.

FUNCTIONAL DESCRIPTION

The TB304 provides five access (seek) modes, four read/write modes, and two modes for determining head selection.

ACCESS MODES

Direct Seek

The drive under test will perform a single seek to the track number set in the CYLINDER ADDRESS switches on the FTU control panel.

Continuous Seek

The drive under test will perform repetitive seeks between any two tracks selected by the operator. Operation will continue until the START/STOP switch on the FTU control panel is moved to STOP (STOP switch actuated).

Sequential Forward Seek

The drive under test will perform singletrack incremental seeks until the last track is reached, then seek to track 00 and continue

* Head alignment card not supplied with TB304C.

incremental seeks in the same manner until the STOP switch is actuated.

Sequential Reverse Seek

The drive under test will perform single-track decremental seeks, starting with the track nominated by the CYLINDER ADDRESS switches, until track 00 is reached. The drive will then return to the nominated track and repeat the operation until the STOP switch is actuated.

Random Seek

The drive under test will perform seeks to random tracks as selected by a free-running counter within the FTU. Operation will continue until the STOP switch is actuated.

READ/WRITE MODES

The TB304 generates serial NRZ write data at a rate determined by the servo clock signals transmitted from the unit under test. When a repeated access mode is selected (that is, any mode except Direct), a read/write operation will be completed at the selected cylinder, after which a seek will be initiated to the next cylinder address (as determined by the access mode) and the read/write operation repeated. This seek-read/write sequencing will continue until the STOP switch is actuated or an error occurs. For Direct seeks, the R/W operation will continue at the selected track or cylinder (depending upon the head select mode) until the STOP switch is actuated.

The number of tracks read or written during each R/W operation is controlled by the mode of head selection chosen. For manual head selection, only the track under the head selected by the HEAD ADDRESS switches will be read or written. For sequential head selection, the heads will be sequenced so as to read or write each track in the cylinder. When the highest-numbered head (or cylinder track) has been exercised, the drive will seek to another cylinder (except in Direct Seek) and the R/W operation will be repeated at that new cylinder, starting with head 00.

Write Format

When the FTU WRT-RD SELECT switch is set to WRT FORMAT, the FTU will write each selected

TABLE 1-1. TB304 SPECIFICATIONS

Characteristic	Condition	Specification
Size	LxWxH	$20.5 \times 16.0 \times 8.0$ inches (52.0 x 40.6 x 20.3 cm)
Weight		43 lbs (19.5 kg)
Temperature	Operating	$+60^{\circ}$ F to $+90^{\circ}$ F (15.5°C to $+32^{\circ}$ C)
	Gradient (rise per hour)	+12 ^o F (+6.6 ^o C)
	Non-operating	+30°F to +150°F (-34°C to +66°C)
Relative Humidity	Operating	20% to 80%
(no condensation)	Non-operating	5% to 95%
Altitude	Operating	-1000 ft to +10,000 ft (-306 m to +3048 m)
	Non-operating	-1000 ft to +35,000 ft (-306 m to +10.7 km)
Input Power	50/60 Hz, single phase	120 (+8, -18) V ac @ 1.5 A, max. 240 (+17, -27) V ac @ 0.8 A, max. (conversion is via terminal board in power supply)
Minimum Input	120 V ac	90 V ac (100 V ac nominal, ±10%)
Voltage	240 V ac	180 V ac (200 nominal, ±10%)

track with the appropriate track address and a repetitive 8-bit data pattern that has been set in the DATA PATTERN switches on the FTU control panel. The FTU also provides a means for indicating a defective track when using the Write Format mode.

Write

When the FTU WRT-RD SELECT switch is set to WRT, the FTU will write the repetitive 8-bit data pattern on the selected track, after having first read and verified the track address.

Read

When the FTU WRT-RD SELECT switch is set to RD, the FTU will read the data from the

selected track, after having first read and verified the track address.

Write Then Read

When the FTU WRT-RD SELECT switch is set to WRT•RD, the FTU will verify the track address and write the 8-bit data pattern on the selected track during one revolution of the drive, then verify the track address and read back the data during the second revolution.

A fifth position (OFF) of the WRT-RD SELECT switch is available for "access only" operations. This position is also used during the head alignment procedure.

SECTION 2

OPERATION

INTRODUCTION

This section provides installation information for the TB304, including the purpose and use of the various cables supplied with the tester, and gives detailed operating instructions for the many test procedures that are possible with the FTU. A general view of the FTU and its associated hardware is shown in figure 2-1.

INSTALLATION

OPERATING VOLTAGE

The FTU is connected at the factory for use with a 120-V ac 50/60-Hz power source. To reconnect for 240-V ac operation, proceed as follows.

- Open the FTU cover. Unhinge cover and set aside if desired.
- 2. Raise the control panel.
- Remove head alignment card from its compartment on top of the power supply box.
- Remove four screws securing cover plate of power supply; remove the cover plate.
- Remove jumper straps connecting terminals 1 and 2, and terminals 3 and 4 of AlTB1.
- Install both jumpers so as to connect terminals 2 and 3.
- Replace and secure the power supply cover plate.
- Replace the head alignment card in its compartment.
- 9. Procure a 120-to-240 V ac conversion plug from a local supply house and attach to the FTU power cord plug. Alternatively, remove the molded 3-prong plug attached to the FTU line cord and install one suitable for 240-V ac receptacles.

The FTU is now ready for 240 V ac operation.

I/O CABLES

Five I/O cables are provided. The standard A-cable has a 75-pin block-type connector on each end. The standard B-cable is equipped with two 34-pin block-type connectors. For

sites that use any of the BK series SMD's, two adapter cables are provided for converting the block connectors on the standard I/O cables to the flat connectors on the BK-series machines.

The fifth cable is the I/O Bypass cable that contains a 28-pin female connector on one end, the other end fanning out to two 14-pin male connectors. These male connectors plug into locations F01 (P1) and F06 (P2) on the FTU logic board. Pin 1 of the 14-pin connectors P1 and P2 should be aligned with pin 2 of the 16-pin locations on the logic board. The 28-pin connector (P3) connects to the drive logic chassis according to the type of drive under test. Table 2-1 provides the information needed. (See also cross-reference numbers 400 and 403 (c.r. 400, 403) in the Diagrams section.)

HEAD ALIGNMENT CABLE

SMD head alignment using the TB304A/B requires installing the special head alignment card in the proper position of the drive logic

TABLE 2-1. CONNECTING I/O BYPASS CABLE TO DRIVE

SMD Series	Connect 28-pin connector P3 to
BJ4xx	JA84
BJ5xx	JA3 *
вЈ7хх	JA3 *
BK4xx	A02 **
BK5xx	A02 **
вкбхх	JA84
BK7xx	JA84

- * Plugs into card side of logic chassis. Others slip over W/W pins on back (wire-wrap) side of logic chassis at locations shown.
- ** Pin 1 of P3 corresponds to pin 16 of A02

NOTE

The I/O Bypass cable is useful when doing head alignment and for exercising the drive access mechanism. Read/write operations, however, cannot be performed using the Bypass cable.

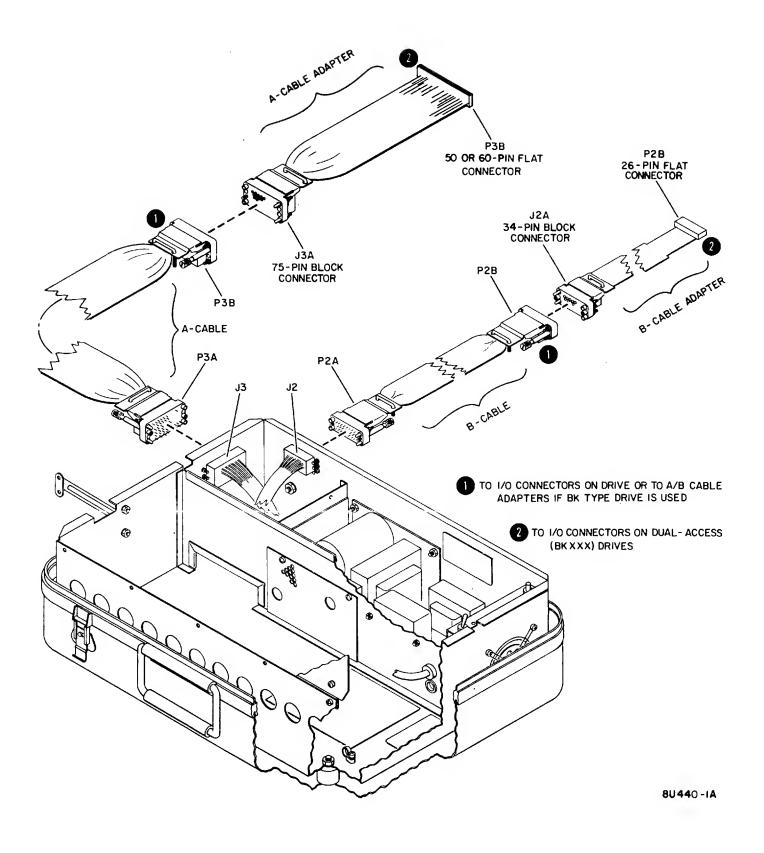


Figure 2-1. TB304 and Associated Hardware (Sheet 1)

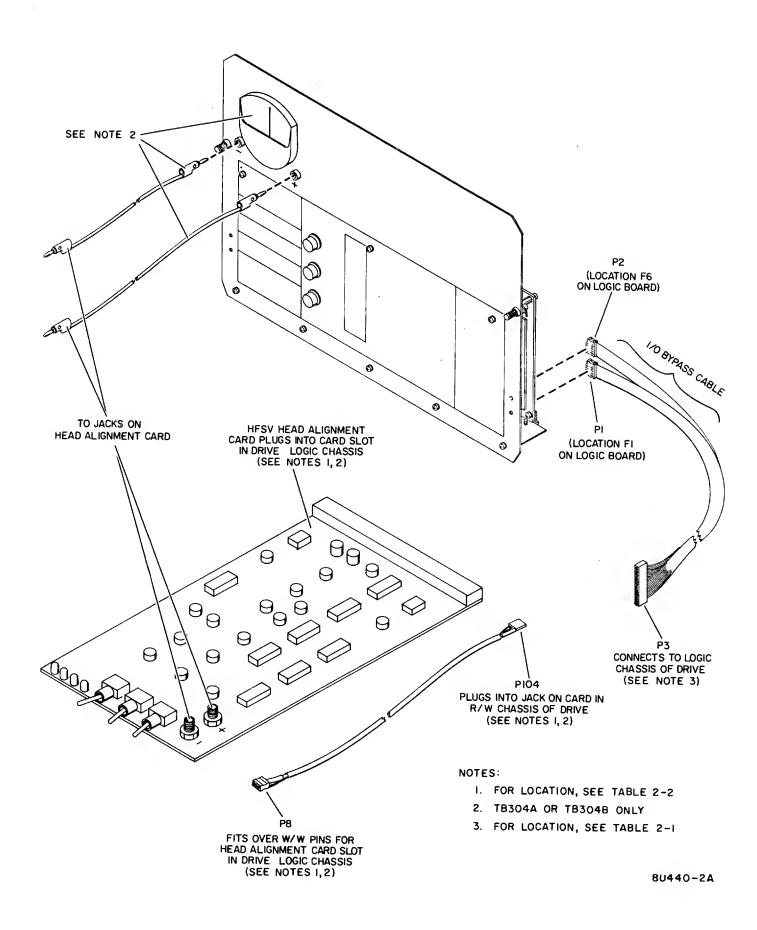


Figure 2-1. TB304 and Associated Hardware (Sheet 2)

chassis, and connecting the 4-wire head alignment cable between the logic chassis and the appropriate jack in the drive's R/W assembly. Table 2-2 gives the needed information for

the various SMD series. A pair of test leads. (provided) is then installed between the banana jacks on the head alignment card and the null meter on the FTU control panel.

TABLE 2-2. HEAD ALIGNMENT CONNECTIONS (SMD ONLY)

	Head	Head Alignme	ent Cable		
SMD Series	Alignment Card Location	P104 000 0	P8 0000		
BJ5xx	A08		[0000]		
BJ7xx	A08	Plugs into J104 on Head Select/Read Amplifier			
BK4xx	A02	card in R/W chassis.	Slips over W/W pins 8A,B		
BK5xx	A02		through 11A,B at location reserved for Head Align-		
BJ4xx	A16	Plugs into Jl on Read Amplifier card in	ment card. (See column at left.)		
BK6xx	Al6	location E03 of R/W			
BK7xx	A16	onabib.			

CONTROLS AND INDICATORS

Except for the power switch and circuit breaker mounted on the power supply box, and the sector switches mounted in location A20 on the logic board, all controls and indicators for operation of the TB304 are located on the control panel (figure 2-2). The controls and indicators are described below,

moving from top left to bottom right across the panel. An asterisk following the switch or indicator name denotes that it is for use only with the standard I/O cables. That is, the function/indication is not provided when the I/O Bypass cable is used in lieu of the standard A and B cables. Sector switch settings are given in table 2-3.

+5 V, -5 V indicators	Indicate t	hat the respective power supply voltages are present.
SERVO OFFSET switch	A 3-positi	on switch with center "off": Commands the drive to offset the carriage in the positive direction (toward the spindle).
	center -	Nominal positioning (no offset). Commands the drive to offset the carriage in the negative direction (away from the spindle).
		NOTE

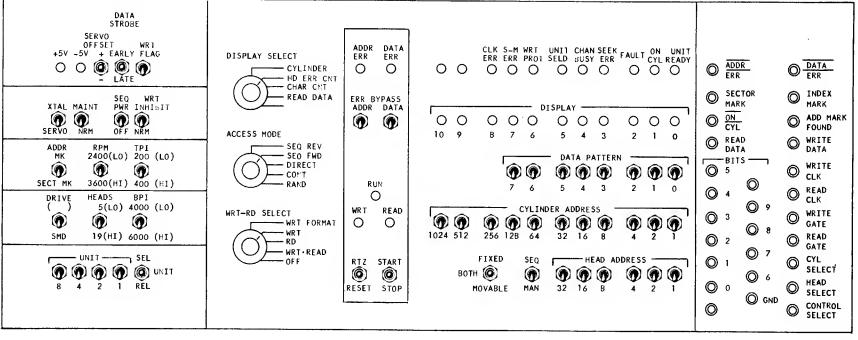
The START/STOP switch must be actuated to effect any change in offset by the drive when in Direct mode.

DATA STROBE* A 3-position switch with center "off": EARLY switch Moves the drive Read strobes from nominal to an earlier time with respect to data.

> center Drive strobes at nominal timing. LATE Moves the drive Read strobes from nominal to a later time, with respect to data.

WRT FLAG* When moved up, this switch causes a Defective Track flag bit to be inserted in Bit 6 of Address Word 1, provided that:
 a. WRT-RD SELECT switch is set to WRT FORMAT switch b. ACCESS MODE switch is set to DIRECT.

c. Head Select switch (SEQ-MAN) is set to MAN.

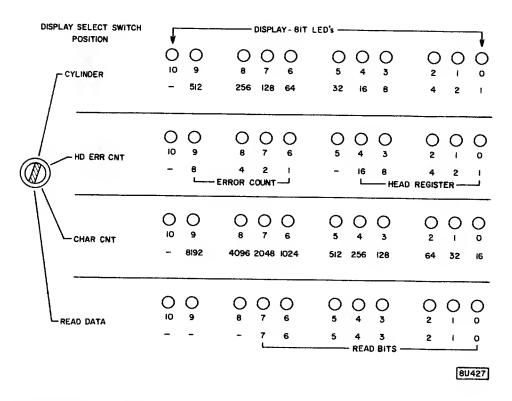


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XTAL/SERVO switch	XTAL	A crystal oscillator within the FTU provides a nominal 9.667 MHz clock signal. Used for tester maintenance.
	SERVO	Servo Clock signals from the drive provide the basic timing for the FTU.
MAINT/NRM switch	MAINT	Used to perform maintenance on the FTU without a drive connected. Provides pseudo Ready and On Cylinder signals to simulate a drive response.
	NRM	Normal testing of a drive is done in this position.
SEQ PWR/OFF* switch	SEQ PWR	<pre>In this position, the FTU commands the drive to power up, provided that: a. The drive is in the REMOTE mode. b. Primary power is available at the drive. c. The drive START switch is ON (indicator lighted).</pre>
	OFF	In this position, the FTU cannot power-up the drive.
WRT INHIBIT/NRM* switch	WRT INHIBIT	Prevents writing by the drive. Writing is inhibited even though all other FTU switches are set to perform a write operation.
	NRM	Allows a normal Write operation in the drive, provided that other FTU switches are set to perform a write operation.
ADDR MK/SECTOR MK* switch	ADDR MK	The FTU writes an Address Mark when in the Write Format mode, or reads the Address Mark in other active positions of the WRT-RD SELECT switch.
	SECTOR MK	Disables the writing or reading of Address Marks; permits reading of Sector Marks only.
RPM switch	2400 (Lo) 3600 (Hi)	Set to speed of drive under test. (Not used in TB304A.)
TPI switch	200 (Lo) 400 (Hi)	Must be set to correspond to the number of tracks per inch of the drive under test.
DRIVE switch	() SMD	Preconditions the RPM, TPI, BPI, and HEADS switches on the FTU panel to the parameters of the drive under test.
HEADS switch	5 (Lo) 19 (Hi)	Must be set to correspond to the number of heads present in the drive under test.
BPI switch	4000 6000	Must be set to correspond to the bits-per-inch rating of the drive under test.
UNIT* switches	8 4 2 1	Used to indicate the binary value of the four Unit Select lines to the drive. This code must match that of the logic plug in the drive under test.
UNIT SEL/REL* switch	A 3-positi "down" pos	on, center-off switch with locking "up" end momentary itions:
	SEL	(locking) Sends a Unit Select Tag to the drive under test.
	Center	Drops the Unit Select Tag to the drive under test.
	REL	(momentary) Sends a Release signal for dual-access drives. Has no function for single-access drives.

DISPLAY SELECT switch

A 4-position rotary switch that controls the eleven DISPLAY lamps on the FTU control panel. The interpretation of the display is shown below. When the switch is set on READ DATA, the cylinder address will be displayed as long as the FTU is running. The data pattern read will be displayed if the FTU has stopped because of a data error, Otherwise the display will be zeros.



ACCESS MODE switch

A 5-position rotary switch that controls the Seek (access) mode of the drive under Test:

SEQ REV (Sequential Reverse) The drive seeks to the address in the FTU Cylinder Address switches, sequences down to zero, and then repeats.

SEQ FWD (Sequential Forward) The drive performs a series of incremental seeks, starting with the address that is in the Cylinder Address register at the start of the operation. When maximum cylinder address is reached, the cycle begins again at address zero.

DIRECT The drive seeks to the address in the Cylinder Address switches. Seeking to another address requires changing the address in the switches and manually initiating another Seek operation by actuating the START switch.

CONT (Continuous) The drive seeks alternately between the address in the Cylinder Address switches and that in the Cylinder Address register (CAR). The contents of CAR does not change during this operation.

RAND (Random) The drive seeks to random addresses generated by increasing the count in the CAR during the time that the drive is not "on cylinder".

WRT-RD SELECT*
switch

A 5-position rotary switch that determines the manner in which data will be exchanged between the FTU and the drive under test:

WRT FORMAT (Write Format) Writes an Address Mark (if selected), the correct Track Address (HA and CA), and a pre-selected 8-bit Data Pattern field on each selected track. The format is as shown in figure 2-3.

WRT (Write) Writes a pre-selected pattern in the Data field of each selected track, after first verifying the Track Address.

RD (Read) Verifies the Track Address, then reads the selected track.

WRT•READ (Write Then Read) Verifies the Track Address, writes the Data field on the selected track, then reads the track. (Operation requires two revolutions.)

OFF Disables all Read/Write functions; restricts the drive under test to Seek operations only.

ADDR ERROR* indicator

Indicates that the address information received from the drive differs from the address requested, or that an Address Mark is missing when reading in the Address Mark mode, or that the Address sync bit was not received from the drive.

DATA ERROR* indicator

Indicates that the 8-bit data pattern received from the drive differs from the pattern set in the DATA PATTERN switches.

ERROR BYPASS* switches

ADDR (Address) If this switch is in the "up" position, it allows the FTU to continue operating when an Address or S-M error occurs. In the "down" position, an Address error will stop the FTU.

DATA If this switch is in the "up" position, it allows the FTU to continue operating when a Data error occurs. In the "down" position, a Data error will stop the FTU.

RUN indicator Indicates that the FTU is running or that the RTZ switch (Return to Zero) is being actuated. Five conditions will turn off the indicator.

- a. Returning the RTZ switch to neutral (but provided that RUN was not lit before the RTZ operation).
- b. A valid (unbypassed) error.
- c. Actuating the STOP switch.
- d. Actuating the RESET switch
- e. If the Ready signal from the drive goes low.

WRT indicator

Indicates that the FTU is writing.

RD indicator

Indicates that the FTU is reading.

RTZ/RESET switch

A 3-position momentray switch with center "off":

RTZ Clears HAR and CAR; clears the drive's Fault register and causes the drive to perform a Return-to-Zero seek.

The RUN light on the FTU control panel will be lit as long as this switch is actuated to the RTZ position.

center Neutral position.

RESET Clears the Error FF's in the FTU and the drive. This switch must be actuated after a valid error has occurred in order to be able to restart the FTU.

START/STOP switch

A 3-position momentary switch with center "off":

START Generates a pulse that starts the FTU; turns on the RUN indicator. An existing Error condition must be cleared by actuating the RESET switch before START will have any effect.

center Neutral position.

STOP Stops the FTU; extinguishes the RUN indicator.

CLK ERROR Indicates indicator servo cloc

Indicates that a period of 200 nanoseconds has expired without a servo clock pulse from the drive under test.

indicator
WRT PROTECT*

S M ERROR*

Indicates that an incorrect number of Sector Marks was received from the drive under test between successive Index Marks.

indicator

Indicates the presence of a Write Protect signal from the drive under test. $% \left(1\right) =\left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(1\right) \left(1\right) \left(1\right) \left(1\right) +\left(1\right) \left(1\right)$

UNIT SELD* indicator

Indicates the presence of a Unit Selected signal from the drive under test.

CHAN BUSY*
indicator

Indicates the presence of a Channel Busy signal from the drive under test.

SEEK ERROR*
indicator

Indicates the presence of a Seek Error signal from the drive under test.

FAULT*
indicator

Indicates the presence of a Fault signal from the drive under test.

ON CYL indicator

Indicates the presence of an On Cylinder signal from the drive under test, or a pseudo On Cylinder signal generated by the FTU if in Maintenance mode.

UNIT READY indicator

Indicates the presence of a Ready signal. This signal comes from the drive if the A and B I/O cables are connected between the drive and the FTU. If the I/O Bypass cable is connected, this signal is present continuously.

DISPLAY indicators

Refer to diagram shown for the DISPLAY SELECT switch (page 2-7).

DATA PATTERN switches

These switches permit setting the repetitive 8-bit Data Pattern that is written in the Data field of the selected track during a Write operation. For Read operations, the data read from the track is compared with these switches to check for the presence of a Read error. A l is indicated when a switch is in the "up" position, a 0 when the switch is "down".

CYLINDER ADDRESS switches

These switches enter the binary value of the cylinder address to which a Seek is desired. Used in conjunction with the ACCESS MODE switch described on page 2-7. A 1 is indicated when a switch is in the "up" position, a 0 when the switch is "down".

FIXED/BOTH MOVABLE switch

83319600 C

This switch is functional only when the DRIVE switch on the FTU panel is in the "up" () position.

FIXED (1) The FTU will access only fixed heads in the Sequential access mode. When in Random access mode, the cylinder address may not be random because of inconsistencies in clock frequency and Off Cylinder times.

BOTH (1) The FTU will access both movable and fixed heads when in Sequential or Random access modes.

MOVABLE The FTU will access only the movable heads when operating in Sequential or Random access modes.

 To access all fixed heads, the SEQ/MAN head switch must be in SEQ.

2-9

SEQ/MAN switch	This switch determines the manner in which the head address is selected.
	SEQ (Sequential) After performing a Write or Read operation the FTU increases the count in the Head Address Register by 1 for each Index Mark or, when in Random, for each access. When the count is maximum (5 or 19), Head zero is the next head selected, and the incrementing continues.
	MAN (Manual) The FTU will select the head address set in the Head Address switches.
ADDR ERROR* test point	This test point goes to a logical 0 when an Address Error occurs.
DATA ERROR* test point	This test point goes to a logical 0 when a Data Error occurs.
SECTOR MARK* test point	This test point goes to a logical 1 when the FTU receives a Sector Mark signal from the drive under test.
INDEX MARK test point	This test point goes to a logical 1 when the FTU receives an Index Mark signal from the drive under test.
ON CYL test point	This test point will be a logical 0 when the drive under test is On Cylinder.
ADD MARK FOUND test point	This test point goes to a logical 1 when the FTU receives an Address Mark Found signal from the drive under test.
READ DATA* test point	This test point reflects the binary value of the serial data bits being received from the drive under test during a Read operation. The test point is inoperative during Maintenance mode or when the I/O Bypass cable is installed.
WRITE DATA test point	This test point reflects the binary value of the serial data bits being processed by the FTU during a Write operation ("0"=0, "1"=1). The indication is valid for any Write operation, even though the I/O Bypass cable may be connected or the FTU is in the Maintenance mode. The test point remains a logical 0 during Read operations.
BITS 0-9 test points	These ten test points reflect the logical value of the Bus Out Bits (DOB) delivered to the drive under test via the A cable. Logically, these bits are present in the FTU's Bus Out multiplexer, and are available at the test point panel regardless of the operating mode (including Maintenance mode or I/O Bypass cable connected) of the TB304. For an interpretation of the specific Bus Out Bits, see table 3-1.
GND test point	This test point is a common ground point between the FTU logic board and the control panel.
WRITE CLK test point	This test point reflects the logic level of the 9.667 MHz Write Clock signal that the FTU sends to the drive under test during any Write operation.
READ CLK* test point	This test point reflects the logic level of the 9.667 MHz Read Clock signal received from the drive under test during any Read operation. A Read Error forces the test point to logical zero.
WRITE GATE test point	A logical l at this test point indicates the presence of a Write Gate signal generated by the FTU during any Write operation.

CYL SELECT This test point goes to a logical 1 when the FTU sends a Cylinder test point Select signal (Tag 1) to the drive.

A logical 1 at this test point indicates the presence of a Read Gate signal generated by the FTU during any Read operation.

HEAD SELECT This test point goes to a logical 1 when the FTU sends a Head test point Select signal (Tag 2) to the drive.

CONTROL SELECT This test point goes to a logical 1 when the FTU sends a Control test point Select signal (Tag 3) to the drive.

READ GATE

test point

OPERATION

GENERAL

The TB304 is used to pin-point a problem in the drive, once the nature of that problem has been defined. Suppose, for example, that a drive is suspected of intermittent Read errors. That's the nature of the problem. The TB304 can be set up to repeatedly write and read back any chosen data pattern on a given track, or on the entire pack, or on any portion of the pack between two selected cylinders, stopping when an error occurs.

By making several such error-stop passes, and by using the DISPLAY SELECT switch to identify the cylinder, head, character count, and the data pattern read for each error stop, the CE can create a record of error parameters that will provide a failure pattern for pinpointing the problem.

The character-count display provides an indication of how far from Index the error occurred. If you suspect a bad spot on the disk because errors consistently occur for the same head (track) and cylinder, the character count can confirm it. If the character count is random for that situation, suspect intermittent data failures on the selected head, rather than a bad spot on the disk.

That is to say, the degree of flexing to which the head lead is subject at a particular head/arm location, plus machine vibration, might cause head/write errors; but they would most logically be random errors, not occurring at the same spot for every revolution of the disk. Discriminations between random errors and honest-to-goodness bad spots are important when writing the track format on a new scratch pack, as described under Operating Procedures.

Data and address errors are the most prevalent causes for failure. The TB304 provides Bypass switches for each of these errors. This allows the FTU to alternately write and read a suspected track (or cylinder or pack area) for an extended period without stopping when an error of this type appears. After the test, the Error Count display will show the number of errors that occurred during the test period, up to a maximum of 15.

PRELIMINARY SET-UP

- Determine which of the following conditions will be required of the drive to be tested:
 - a. The drive is to be tested for Read/ Write, as well as Seek functions.
 - b. The drive is to be powered up from the FTU.

- c. Only the access (Seek) functions of the drive are to be tested.
- d. The drive need not be powered up from the FTU.
- e. Head alignment (but no R/W) of the drive is to be performed.

For conditions a or b, the A and B I/O cables from the controller must be disconnected from the drive and the I/O connections made between the drive and the FTU, via the A and B cables provided with the tester.

For conditions c, d, or e, the I/O Bypass cable connection between the FTU and the drive to be tested will be sufficient. Of course, any of the five conditions may be realized by connecting as shown for conditions a or b.

- Take steps to ensure that the system will not attempt to select the drive while that drive is being exercised by the FTU.
- Remove ac power from the drive and make the I/O cable connections as determined in step 1. Do not reapply ac power to the drive.
- 4. Set the Sector switches on the Field Test Unit according to table 2-3. The switches are located on a dual in-line package located in position A20 on the FTU logic board.
- 5. Set the following drive-oriented switches to the drive specifications:

RPM HEADS TPI BPI

TABLE 2-3. SETTING SECTOR SWITCHES

No. of		Sector Switches						
Sectors	1	2	3	4	5	6	7	8
2	ON							
4	ON	ON						
8	ON	ON	ON					
16	ON	ON	ON	ON			ĺ	
32	ON	ON	ON	ON	ON			
64	ON	ON	ON	ON	ON	ON		
128	ON	ON	ON	ON	ON	ON	ON	
256	ON	ON	ON	ON	ON	ON	ON	ON
OFF POSITION ON POSITION								

Set the following switches as shown for normal testing:

Position Switch Servo Offset center (off) Data Strobe center (normal) Wrt Flag down (off) SERVO Xtal/Servo Maint/Nrm NRM Wrt Inhibit/Nrm NRM Addr Mk/Sect Mk ADDR MK to logical address Unit (4 switches) of the drive Addr Error/Bypass

Addr Error/Bypass Data Error Bypass (2 switches)

both down (off)

- Install a scratch pack on the drive to be tested.
- 8. Turn on the FTU.
- 9. Apply ac power to the drive.
- 10. Select the drive by placing the SEL/REL switch to SEL. The UNIT SELD indicators should light up, as will the WRT PROT indicator if the drive under test has the Write Protect Feature.
- 11. Power up the drive. The WRT PROT lamp, if on, will go out when the drive is up to speed. The UNIT READY lamp will light up when the heads are loaded.
- 12. Actuate the RTZ switch, then the RESET switch on the FTU panel.

The drive is now ready for exercising. It has performed a Seek to cylinder zero and has selected head zero. The remaining switches on the FTU panel may now be set for the desired function and operating modes, as illustrated in Operating Procedures.

OPERATING PROCEDURES

The first five of the procedures described below embody every access, read/write, and head-select mode provided by the TB304. Procedure 6 checks the data error logic and Procedure 7 describes the use of the head alignment card.

Procedure 1: Continuous Seek, no R/W

(Perform alternate Seeks between the cylinder address in the CAR and the address set in the CYLINDER ADDRESS switches.)

 Assure that the FTU switches are positioned as described in the Preliminary Set-up procedure. In addition, position the following switches as shown: <u>Switch</u> <u>Position</u>
Display Select CYLINDER

 Set the CYLINDER ADDRESS switches to the value of one of the cylinder address to which the drive will seek. (For illustrations, choose CA 8.)

OFF

- 3. Set the ACCESS MODE switch to DIRECT.
- 4. Momentarily actuate the START switch. When the ON CYL indicator is lit, move the switch momentarily to STOP. Address 8 is now in the CAR, and will be displayed on the panel.
- Set the CYLINDER ADDRESS switches to the value of the second cylinder address. (For illustration, choose CA16.)
- 6. Set the ACCESS MODE switch to CONT.
- 7. Actuate START.

Wrt-Rd Select

The drive will perform alternate Seeks between addresses 8 and 16. The ON CYL indicator will blink rapidly as the heads move on and off cylinder. The Cylinder Address display lights will alternate between 8 and 16.

- 8. Stop the FTU.
- 9. Actuate RTZ.
- 10. Start the FTU.

The tone of the drive will change as it now seeks between cylinders 0 and 16. Observe the display.

11. To stop the operation, actuate either STOP or RESET.

Procedure 2: Rondom Seek, no R/W

1. Maintain the Preliminary Set-up switch positions. In addition, set the following switches as shown:

Switch Position

Display Select CYLINDER
Wrt-Rd Select OFF
Access Mode RAND

- 2. Actuate START.
- Assure that cylinders are being selected randomly by observing the changing pattern on the display lamps.
- 4. Stop the FTU by actuating STOP or RESET.

NOTE

Procedure 3 through 6, following, require that the A and B I/O cables be connected between the FTU and the drive under test.

Procedure 3: Write Format

This procedure is used to write a prescribed format on every track of the disk pack. The next procedure, Read, determines whether any of the tracks so written contain errors. The final procedure, Write Flag, shows how to select a single track, write a "defective track" flag bit on that track, and check to ensure that the flag bit was indeed written. The track format is shown in figure 2-3.

 Maintain the switch positions as given in the Preliminary Set-up procedure. In addition, set the following switches as shown:

Switch	Position
Display Select Access Mode Wrt-Rd Select Cylinder Address (8) Seq/Man Head Address (6) Data Pattern (8)	READ DATA SEQ FWD WRT FORMAT all down (off) SEQ all down (off) anything but "all
	zeros"

- Move RTZ/RESET switch to RESET, then to RTZ.
- 3. Actuate START.

Observe the progression of the display lights as the drive moves away from cylinder zero. (With the DISPLAY SELECT switch set to READ DATA, the cylinder address is displayed while the FTU is running.)

- 4. If an error stop occurs, remove the cause of the error by proceeding as indicated in the Trouble Analysis decision logic table on page 2-17. (Data errors will not occur during WRT FORMAT.) Then proceed as follows:
 - a. Actuate RESET to clear the error indication in the FTU.
 - b. Actuate START. This rewrites the track that was selected when the error occurred, then continues the Write Format operation.
 - c. Do not actuate RTZ. To do so would cause the operation to begin anew at cylinder zero, head zero.

5. After all tracks have been written, stop the FTU. (Writing will begin again at cylinder zero, so the actual stopping point is immaterial.)

Procedure 4: Read

This operation tests the entire data pack for errors, using the Sequential Reverse Access mode.

 Set the following switches as shown. All others should remain as given for the Write Format procedure.

<u>Switch</u>	<u>Position</u>
Display Select Access Mode	CYLINDER SEQ REV
Wrt-Rd Select	RD

- Set the CYLINDER ADDRESS switches to the maximum cylinder address of the drive under test.
- 3. Actuate START,

Reading will begin at maximum cylinder, head zero. Should an error occur, the RUN light will go out and the panel lamps will display the type of error, as well as the cylinder that was being read when the error occurred. Follow steps 4 and 5 for each separate error stop. If no errors, skip to step 6.

 Record the error parameters, moving the DISPLAY SELECT switch as needed.

NOTE

It is not necessary to record the ERROR COUNT. This will remain at count 1 for any stop-on-error operation.

- After the error parameters have been recorded, actuate RESET to clear the error indication in the FTU, then actuate START to continue reading.
- When the FTU begins reading again at the maximum cylinder address, actuate STOP to halt the FTU.

Procedure 5: Write Flag

The above Read procedure allows for reading each track of the scratch pack but one time. A single read may well produce random errors that in an on-line environment would be eliminated by the system's error-recovery program. Before flagging a track as bad -- that is, as one that continually produces errors that are

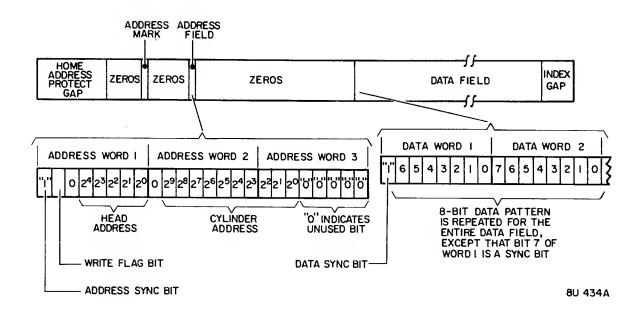


Figure 2-3. Track Format

not recoverable -- it is best to WRT•RD the track several times. If the error persists, the Trouble Analysis procedure (see Procedure 7) should be executed in an effort to recover the error. Only after both of these attempts have failed, should the track be flagged.

This procedure writes a "defective track" flag bit in Bit 6 of Address Word 1 (see figure 2-3).

 Set the following FTU switches as indicated. Other switches should remain as shown for the Preliminary Set-up procedure.

Switch	Pos	ition	
Wrt Flag	_	(on)	
Display Select	REA	D DATA	
Access Mode	DIR	ECT	
Wrt-Rd Select	WRT	FORMAT	
Data Pattern (8)	as	set when th	ie
	tra	ck was most	re-
	cen	tly written	١.
Cylinder Address (1	l) (to	select the	track
Head Address (6)	l to	be flagged	
Seq-Man	MAN		

2. Actuate RESET, then START.

Drive will seek to the selected track.

- When ON CYL light comes on, wait about 1/2 second and then actuate STOP.
- 4. Set WRT-RD SELECT switch to RD.

5. Actuate START.

The "defective track" flag bit will prohibit reading the track. The ADDRESS ERROR and DATA ERROR lights should not light up.

6. Actuate STOP.

Repeat steps 1 through 6 for each track to be flagged as defective.

NOTE

The WRT FLAG switch must be turned off before reading a track that has not been written as defective, otherwise an address error will occur.

Procedure 6: Check Data-Error Logic

This procedure assures the operator that data errors will be recognized by the FTU. It is used when the FTU is exercising a drive in any situation where data errors are expected -- an intermittent Read failure, for example -- but none occur. It assumes that the procedure in question is still running and that the scratch pack therefore has a data field written on the tracks being tested.

1. Stop the FTU.

2. Set the following FTU switches as indicated:

Switch

Position

Wrt-Rd Select Data Error Bypass Data Pattern RD
down (off)
Choose any one switch
and move it to the
opposite position.
(Move just one!)

All other switches must remain as they were at the start of the procedure being questioned.

3. Start the FTU.

The FTU should stop with the DATA ERROR indicator lit.

- Actuate RESET to clean the error indication.
- 5. Set the DATA ERROR BYPASS switch up (on).
- 6. Actuate START.

The FTU should run without stopping on an error, but the Rd/Wrt Error counter will count the errors (up to 15).

 Stop the FTU. Return all switches used during this procedure to the state they were in at the start of the procedure being questioned.

Procedure Z. Trouble Analysis

Table 2-4 is a decision logic table (DLT). It shows the procedures the operator should take to eliminate any error that might occur when using the FTU to exercise a drive. Address and Data errors will, of course, occur only during one of the Read or Write modes. Others may occur for either Read/Write or Access Only (no R/W) modes. It should be noted that a Sector Mark error is not indicated if the Address Error Bypass switch is active.

Basically, the DLT deals with attempts either to eliminate or to recover data and address errors before writing a "defective track" flag on the track in error. This involves trying various Offset and Data Strobe switch combinations.

The DLT is divided into four quadrants. Each test condition (shown in the upper left quadrant) is reduced to a Yes (Y) or No (N) result, as indicated in numbered columns of the upper right quadrant, two columns being allotted for each test condition. The two lower quadrants deal with recommended actions.

To determine what actions (if any) should be executed for a given test result, follow the selected column down to the number "1" (first recommended action) in the lower right quadrant. The specific action to be taken is then located by following across to the lower

left (Actions) quadrant. After Action 1 has been taken, repeat the test that gave rise to the error condition. If the error persists, perform Action 2, test again, and so on. An "X" in the lower right quadrant indicates a "no error" or a "problem solved" situation, and requires no further remedial action.

Columns 9 through 19 (sheets 2 and 3 of table 2-4) define the sequential tests that were alluded to in the second paragraph of this procedure, and which should be made in an attempt to recover any data error. Note that the last Action item in any of these "yes" (even-numbered) columns instructs the operator to set up the next condition, or test, for re-reading the track in error. When the last of these sequential tests has failed to recover the data, then and only then should the Write Flag procedure be carried out as indicated by Action 3 of column 19.

Procedure 8: Using the Head Alignment Card *

This procedure describes the use of the type HFSV Head Alignment card and the null meter on the FTU control panel to perform R/W- and/ or servo-head alignment on the SMD under test. The procedure may be implemented using either the A and B cables or the I/O Bypass cable. The cabling involved is seen in figure 2-1, and table 2-2 gives specific connection information. The switches and indicators on the HFSV card are shown in figure 2-4.

This procedure assumes that the I/O connections between the drive and the FTU have already been made as described in the Installation portion of this manual. The installation and cabling of the Head Alignment card, as detailed in table 2-2, must be made with ac power removed from the drive and the FTU.

- Install the proper CE pack on the drive to be tested.
- Install the HFSV card in the drive's logic chassis at the location specified in table 2-2 for the type of drive under test.
- 3. Install the Head Alignment cable between the drive's logic chassis and the jack on the card in the R/W chassis, as specified in table 2-2. Note that Pl04 is keyed so that it will fit on the R/W card only one way.
- Connect the test leads, provided with the FTU, between the HFSV card and the null meter on the FTU panel. Observe polarity.
- 5. Set the WRT INHIBIT/NRM switch on the FTU to WRT INHIBIT.
- 6. Apply ac power to the drive. The POWER lamp on the HFSV card should light up.

^{*} TB304A/B only.

- 7. Power-up the drive.
- 8. Assure that S3 on the HFSV card is set to X1 (no attenuation of output), and that S2 is set as required for the first head, Servo (S) or Data (RW), to be checked for alignment.
- 9. Turn on the FTU.

If the drive under test has the Write Protect feature, the WRT PROT light on the FTU Panel will be lit.

10. When the drive is up to speed and the READY light on the FTU panel is lit, actuate RESET, then RTZ. Carry on as detailed by the head alignment procedure in the maintenance manual for the drive under test. When the heads have been aligned, remove the CE pack from the drive. Install the scratch pack if further tests are to be conducted using the FTU.

Installing the Head Alignment card automatically "write protects" the drive. (This is true even if the drive does not have the Write Protect feature, which merely allows Write Protect to be implemented from the drive's operator panel, and in addition provides the Write Protect signal in the I/O lines.) Therefore, if Write, Write then Read, or Write Format operations are to be conducted, the HFSV card must first be removed from the drive.

• 2-16 83319600 D

TABLE 2-4. TROUBLE ANALYSIS, SHEET 1 OF 3

Assume:							
1. A & B I/O cables connected between drive and FTU.							
2. Power applied to FTU and drive.							
3. ±5V indicators on FTU panel are lit.							
4. DATA/ADDRESS BYPASS switches OFF.							
5. FTU set to READ track format.							
6. READ indicator comes on when START switch (on FTU panel) is actuated.					_		
Conditions:	1	2	3	4	5	6	7
Seek error.	N	Y		_	_	_	_
Clock error in SERVO position.	N	_	Y	_	_	_	_
Clock error in XTAL position.	II -	_	-	N	Y	_	
Sector Mark error	N	-	-	-	-	Y	-
Drive Fault	N	-	-	-	-	-	Y
Actions:							
Go to sheet 2 Conditions.	1	-	_	_	-	-	-
Actuate RESET, RTZ, START	-	1	-	-	-	-	-
Refer to drive maintenance manual	-	2	-	-	-	4	1
Set XTAL-SERVO switch to XTAL; Actuate RESET, START	-	-	1	-	-	-	-
Check TP5 (WRITECLK) on FTU panel.	-	-	-	-	1	-	-
Refer to FTU diagrams.	-	-	-	-	2	3	-
Check B-cable for Servo Clk signal. Troubleshoot discontinuity in drive, cable, FTU.	-	-	_	1	_	-	_
Check that sector switches in FTU and drive are set correctly	-	-	-	-	_	1	-
Check TP1 (SECTOR MK) on FTU panel.	-	-	-	-	-	2	-

UPPER LEFT QUADRANT: Assumptions needed for tests, as well as the test conditions.

UPPER RIGHT QUADRANT: Results of the test. N = No; Y = Yes; - = don't care.

LOWER LEFT QUADRANT: Actions to be taken for each test result.

Numbers show sequence of actions for the test results in a given column. After each Action, the test is repeated and, if LOWER RIGHT QUADRANT:

needed, the next Action is taken.

X = a "no error" or "problem solved" situation.

2-17 83319600 D

TABLE 2-4. (CONT'D) TROUBLE ANALYSIS, SHEET 2 OF 3

Conditions (cont'd):	1	8	9	10	11	12	13	14	15	16	17	18	19
Address error	N	Y	-	-	-	-	-	-	-	-	-	-	-
Data error, OFFSET and DATA STROBE switches in center-off position	N	-	Y	-	-	-	ı	ı	-	1	-	-	-
Data error on WRT•RD retry	-	-	-	N	Y	-	-	ı	-	-	-	-	_
Data error, OFFSET switch in + (FWD) position	-	-	_	-	ı	N	Y	•	-	•	•	-	-
Data error, OFFSET switch in - (REV) position	-	-	-	-	-	-	-	N	Y	ı	-	-	-
Data error, DATA STROBE switch set EARLY	-	-	-	-	-	-	1	-	-	N	Y	-	-
Data error, DATA STROBE switch set LATE	-	-	-	-	-	-	-	ı	-	-	_	N	Y
Actions:													
Track was read without error	Х	-	-	-	-	-	-	-	-	-	-	-	-
Perform WRT FORMAT for track in error; READ re-written track.	-	1	-	-	-	-	-	-	-	-	-	-	_
Perform WRT•RD retry as follows:													
(Set FTU switches as indicated below)		_				- -	-	-	-		1		
WRT-RD SELECT to WRT•RD													
ACCESS MODE to DIRECT			1	_	_					_		-	_
SEQ/MAN to MAN			-										
HD ADRS to select CYL ADRS failing track													
Actuate START; check for conditions 10 or 11													
Drive has demonstrated its ability to recover data.	-	-	-	Х	-	х	-	х	-	х	-	х	-
Unrecoverable error. Perform WRT FLAG procedure for track in error.	-	2	-	-	-	-	-	-	-	-	-	-	-

TABLE 2-4. (CONT'D) TROUBLE ANALYSIS, SHEET 3 OF 3

Actions (cont'd)	1	8	9	10	11	12	13	14	15	16	17	18	19
Set OFFSET switch to + (FWD) position; READ track in error.	-	-	-	-	1	-	_	-	-	-	-	-	-
Check Bit 2 TP on FTU panel; if missing, refer to FTU Diagrams.	-	-	-	-	-	-	1	-	-	-	-	_	-
① Check BOB2 in drive; if missing, check I/O cable and Fwd Offset logic in drive.	-	-	-	-	ı	ı	2	-	ı	ı	ı	-	-
Set OFFSET switch to - (REV) position; READ track in error.	-	_	-	_	-	-	3	-	-	-	1	-	-
① Check Bit 3 TP on FTU panel; if missing, refer to FTU Diagrams.	-	-	1	_	-	-	-	-	1	-	-	-	_
① Check BOB3 in drive; if missing, check I/O cable and Rev Offset logic in drive.	-	-	-	-	-	١	-	-	2	-	ı	-	_
Return OFFSET switch to center-off; set DATA STROBE switch to EARLY. READ track in error	-	-	-	-	-	1	1	1	3	_	-	-	_
① Check Bit 7 TP on FTU panel; if missing, refer to FTU Diagrams.				-	-	-	-	-	1	-	1	_	_
① Check BOB7 in drive; if missing, check I/O cable and Early Strobe logic in drive.	-	-	-	-	-	-	-	1	ı	-	2	_	_
Set DATA STROBE switch to LATE; READ track in error.				-	-	-	-	-	-	-	3	_	_
① Check Bit 8 TP on FTU panel; if missing, refer to FTU Diagrams				-	_	-	_	-	-	-	-	_	1
① Check BOB8 in drive; if missing, check I/O cable and Late Strobe logic in drive.	-	-	-	-	_	-	-	-	-	-	-	_	2
Unrecoverable error. Perform WRT FLAG procedure for track in error.	-	-	1	-	-	-	1	-	-	-	1	-	3
													_

When checking for the presence of these bits, the DATA ERROR BYPASS switch must be on ("up" position). This allows reading to continue in the event of an error. The switch should be off ("down" position) when checking for the conditions.

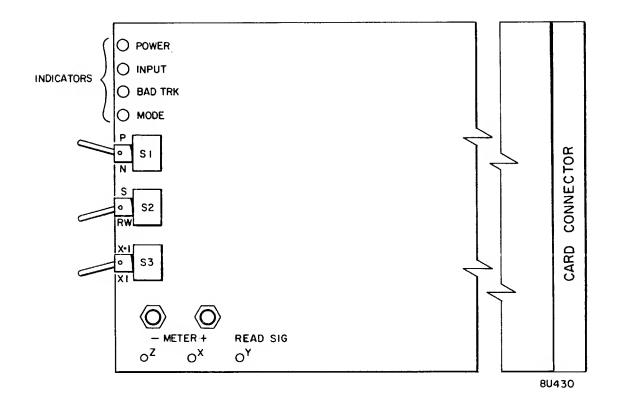


Figure 2-4. Head Alignment Card

Indicators

POWER Lit when power is applied to the card.

INPUT When lit, indicates that input signals are too low for HFSV to operate.

BAD TRK When lit, indicates a short duration loss of input. A one-shot keeps the LED lit for at least 4 seconds.

The lamp will light when Sl is toggled.

MODE Lit when S2 is in the S
(Servo) position or when
S3 is set to X.1. When
either of these conditions
exists, read/write head
alignment error cannot be
measured.

Switches

- S1 Changes the polarity of the alignment signal to the null meter.

 P = positive, N = negative. Algebraically subtract P from N to determine alignment error:

 P = +30 mV, N = -40 mV; Error = 70 mV.
- "S" position selects Servo head as input to HFSV. "RW" position selects a data head as input to HFSV.
- S3 Changes sensitivity of HFSV. "X•1" position attenuates card output by a factor of 10, and alignment error cannot be accurately measured. "X1" position does not attenuate HFSV output; alignment error can be accurately measured.

83319600 D

SECTION 3

THEORY OF OPERATION

INTRODUCTION

The major portion of this section consists of flowcharts that describe the logic sequencing of each access and read/write mode. In the flowcharts, 3-digit numbers above each symbol show the cross-reference number in the Diagrams section where the element named within the symbol may be found. When it is helpful to show these references in the supporting text, the cross-reference number is preceded by the letters CR, and the entire reference enclosed in parentheses. Thus, (CR 117)

refers to the diagram that has 117 in the CROSS REF NO rectangle of the title block.

A functional block diagram of the FTU logic circuits is presented in figure 3-1.

I/O LINES

Table 3-1 defines the I/O signals on the A and B cables. An asterisk after a signal name means that the signal is also present in the I/O Bypass cable.

TABLE 3-1. I/O LINES

Signal Name			Function						
	"A" CABLE (TO DRIVE)								
Tag Bus Lines	Four lines that define the operation to be performed by the drive. The Unit Select Tag gates the four Unit Select lines to the drive. Tag l gates the cylinder address and initiates the seek. Tag 2 gates the head address. Tag 3 is the control select that gates the function to be performed.								
Bus Out Lines	<u>Tag 1</u> *	<u>Tag 2</u> *	<u>Tag 3</u> *						
Bit 0*	1	1	Write Gate - Enables write circuits in drive, unless drive is write protected.						
Bit 1*	2	2	Read Gate - Enables read circuits in drive.						
Bit 2*	4	4	Servo Offset Plus - Offsets the actuator from the nominal on cylinder position toward the spindle.						
Bit 3*	8	8	Servo Offset Minus - Offsets the actuator from the nominal on cylinder position away from the spindle.						
Bit 4*	16	16	Fault Clear - Pulse to clear the Fault Summary flip-flop in the drive.						
Bit 5*	32	-	Address Mark Enable - When combined with a Writ Gate, Address Mark is written. When combined with a Read Gate, an Address Mark search is initiated.						
Bit 6*	64	-	RTZ - Pulse that causes the drive actuator to seek to track zero.						
Bit 7*	128	-	Data Strobe Early - Enables the drive's PLO data separator to strobe the data at a time earlier than optimum.						

TABLE 3-1. I/O LINES (Contd)

Signal Name Function									
"A" CABLE (TO DRIVE)									
Pur Out Ideas									
Bus Out Lines	Tag 1 Tag 2	Tag 3							
Bit 8*	đa	ta Strobe Late - Enables the drive's PLO ta separator to strobe the data at a time ter than optimum.							
Bit 9*	th	lease - Pulse sent to drive to clear e Channel Reserved flip-flop. Applicable ly to dual-access drives.							
Unit Select Lines	Four lines used to select the drive. The binary code on the lines must match the code on the logic plug in the drive.								
Sequence Power	Allows the FTU to powe mode.	r up the drive if the drive is in Remote							
	"A"	CABLE (FROM DRIVE)							
Sector Mark (1) Signal derived from the servo track. The FTU will check a maximum of 128 sector marks per revolution.									
Fault	Indicates that one or more of these faults exist in the drive: DC power fault, head select fault, write fault, write or read while off cylinder, and Write Gate during a Read operation.								
Seek Error	Indicates that the unit was unable to complete a move within 500 ms, or that carriage has moved to a position outside recording field.								
On Cylinder*	Indicates that the servo has positioned the heads over a data track.								
Index* ①	Provides a point of reference to begin R/W operations after On Cylinder has been detected.								
Unit Ready	Indicates that selecte and no fault exists.	d unit is up to speed, heads are loaded,							
Open Cable Detector	Inhibits Unit Selectio Gate when "A" cable is	n and any unwanted command such as Write disconnected or controller power is lost.							
Address Mark Found*	Indicates that an Addr	ess Mark has been found.							
Write Protected	Indicates that the dri drive has the Write Pr	ve is in the Write Protect state if the otect option.							
	"B" CABLE	(TO DRIVE)							
Write Data	Carries NRZ data to be	recorded on disk pack.							
Write Clock	Transmits the Write Cl	ock signal.							
	"B" CABLE	(FROM DRIVE)							
Servo Clock*	Phase-locked 9.677 MHz dibits.	clock generated from the servo track							
Read Data	Read Data Carries NRZ data recovered from the disk pack.								
	Table continued on next page								

TABLE 3-1. I/O LINES (Contd)

Signal Name	Function	
	"B" CABLE (FROM DRIVE)	
Read Clock	Signal that is synchronous with the detected NRZ data.	
Seek End	Seek End indicates that a Seek operation has terminated.	
Unit Selected	When the four unit select bit lines compare with the logic plug on the control panel, and when the unit select line is true, then the Unit Selected line is true.	
*	Indicates that the signal is also carried by the I/O Bypass cable.	
1	In some drives, this signal is in the B-cable instead of the A-cable.	

C

SALIENT LOGIC ELEMENTS

Table 3-2 describes the purpose of flip-flops and registers that may not be obvious at first glance, or that are not explained else-

where in the test. The table is arranged by CR number for convenience when using the logic diagrams.

TABLE 3-2. SALIENT LOGIC ELEMENTS

CR. No.	Element	Purpose			
102	Last Access FF	Sets to indicate that a R/W operation is to follow the Seek operation now being performed.			
103	Alternate FF	Used during Continuous seeks to determine whether the CAR or the Cylinder Address switches will serve as input to the drive via the Cylinder Address mux.			
103	Clocked Not On Cyl FF	Enables incrementing of cyl address during Random mode. Cleared by On Cyl Detected signal to hald incrementing.			
105	Sel Fixed Hd FF	Set whenever Fixed Heads AND MMD are selected from FTU panel. Cleared when either SMD or MOVABLE switch positions are selected. Toggled by + Clr Cyl signal. (See Fixed Head Operation.)			
106	Hd Adrs Compar- ator	Compares current head address (in HAR) with maximum head address for drive under test, depending upon position of panel switches as shown below.			
;		FIXED/BOTH/MOVABLE DRIVE HEADS Max Hd No			
		MOV/BOTH ● () ● (LO) 1 MOV/BOTH ● () ● (HI) 3 not applicable SMD ● 5 4 not applicable SMD ● 19 18 FIXED ● () X 3 (See Fixed Head Operation.)			
107	Offset Mode FF	Indicates Fwd or Rev (+,-) offset during Offset Operations.			
	Not Offset I FF	Set when FTU is stopped, or if Offset is not in effect. Cleared at T3, or at the end of Or Cylinder Lockout delay if Offset is programmed.			
	Not Offset II FF	Set at T3, cleared for Offset operation or during Mainten- ance mode.			
108	Wrt Then Rd FF	A forced clear is maintained on this FF except for WRT•RD of Offset operations. When the clear is released, the FF is toggled by each IM.* This means that reading is done on every second disk revolution, and that a new head won't be selected until the second revolution (Read phase of WRT•RD) has been completed.			
	Last Rd/Wrt FF	Set is a R/W operation is not to follow a Seek sequence, or if the current R/W is using the highest-numbered head, or if Manual head select mode is active, or if in the Random mode with Sequential head selection. Also set for an error.			
108	Rd/Wrt Enable FF	Set by the Index Mark following the appearance of ON CYL if a R/W operation is to follow the Seek sequence. Cleared by Index Mark if the current R/W operation is the last before seeking to a new cylinder (Last Rd/Wrt FF set).			
	Set at Index time for all R/W operations except Write Format to allow the FTU to sense the sync bit at the start of the address field and to read the address field. Cleared after the address field has been read (or at Index time for a Write Format sequence).				
* Inde:	* Index Mark (Table continued on next page)				

83319600 C

TABLE 3-2. SALIENT LOGIC ELEMENTS (Contd)

CR. No.	Element	Purpose				
109	Write Start FF	Enables the writing of Address Mark (AM) during a Write Format sequence. Allows writing the data field during Write (including Write Format) operations. Enables the setting of the Rd Start FF.				
	Rd Start FF	Enables reading the AM for all R/W operations except Write Format. Enables the setting of the Rd Sync Start FF.				
	Rd Sync Start FF	Enables sensing the sync bits and comparing the words for both address and data fields.				
	Wrt Sync Start FF	Enables writing the data sync bit and data for a Write sequence, or writing the address/data sync bits and the address/data fields for a Write Format sequence.				
:	Sync Check FF	Cleared by the setting of Rd Sync Start FF, set when the address (or data) sync bit has been detected. If not set, the FF causes an Address (or Data) Error.				
110	Wrt Gate Signal	Raises the Write Gate line to the drive (CR 120) via TAG 3 (control select).				
	Read Gate Signal	Raises the Read Gate line to the drive.				
110	Rd Compare Enable Signal	Permits comparing the address and data fields against the contents—of the Word Mux $(q.v.)$.				
111	Wrt Sync/Found Sync FF	Set when either sync bit has been written (Write) or detected (Read). When set for either sync bit, enables the bit Counter.				
	Write AM FF	Set during Write Format (only) to write the AM if the Address Mark switch is active. Gates the Address Mark Enable signal to the drive (CR 120) via TAG 3.				
	Search AM FF	Set during all R/W operations except Write Format to initiate a search for address mark (Addr Mk Enable) when Read Gate signal is true and the Address Mark switch is active.				
112	Adrs Not Complete FF	Set by Index Mark Gated (IM AND no error). Cleared at end of Address field.				
	Not End Adrs Field FF	Set early on in the R/W cycle, this FF is cleared by the clearing of Adrs Not Complete FF to indicate the end of the Adress field. When cleared, it causes the Read and Write Reset signals that clear the R/W control FFs on CR 109.				
	Word Cntr	Active for the address field only to gate Address Words 1, 2, and 3 to the Word Mux. At end of address field, the counter sits at the count of 3 until cleared by IM Gated. That count permits the data words to be written or compared, depending upon whether Rd Gate or Wrt Gate is active.				
112	Read Word Cnt Increment FF	Set when - Bit Cnt 4 goes high (at Bit Count 0) if Rd Gate is true. Prevents incrementing the Word Counter when the Bit Counter is loaded with 7 (Bit Cnt 4 high) upon the detection of the sync bit. (Refer to C.R. 111.)				
113	Word Mux	Controlled by the Word Cntr (WC). Determines what is transferred to the Pattern register (q.v.) during Write operations or to the Word Comparator (q.v.) during Read operations:				
	(Table continued on next page)					

TABLE 3-2. SALIENT LOGIC ELEMENTS (Contd)

CR. No.	Element	Purpose			
		WC= 0 1 2 3 Gates contents HAR Addr not Pattern of Mux used Sw.			
		Addr Addr Data Wd 1 Wd 2 Wd 3 Wds			
	Data Sync Bit FF	Set by IM Gated, but has no effect until WC=3. Causes Bit 7 of Data Word 1 to be a 1, regardless of the value of the Bit 7 Data Pattern switch. (Bit 7 of Data Word 1 is the Data Sync bit.) The next Increment Word Count signal finds the FF's CD input low, and clears the FF. This ensures that the true value of the Bit 7 Data Pattern switch will be transferred to the Word Mux for all words after Data Word 1.			
114	Pattern Reg.	An 8-bit shift register with parallel and serial inputs and outputs, as follows: Write: Parallel inputs (from Word Mux), serial output (to NRZ Write Data FF). Read: Serial input (from drive), parallel outputs			
114	NRZ Write Data FF	(to Word Comparator). When cleared, will send zeros to the drive if Write Gate is high, thereby causing the drive to write the zero fields (see track format, figure 2-3). When the preclear (reset) input goes high, the FF is clocked by Clock Data pulses (servo clock signals from the drive) and either sets or clears, depending upon the state of the serial output stage in the Pattern register.			
115	Word Comparator	Active only during Read operation. Compares the serial information received form the drive, as seen in the Pattern register, against the Address or Data Words, depending upon the input presented to the Word Mux. A faulty compare will cause the + Compare line to go low. If the Defective Track flag bit is present in the Address field, the Wrt Flag Detected FF will set, setting the Defective Sector FF to force + Compare high for that track. When not actually comparing, + Compare is held high (Rd Compare Enable is low) to avoid irrelevant data/address error indications.			
120	Bus Out Mux	Delivers TAG information to the drive via the A-cable transmitters (CR 123, 124) or the I/O Bypass cable (CR 403):			
		Input Selected 0 1 2 3			
		Output (TAG 1) (TAG 2) (TAG 3) Cyl Head Control Not Addr Addr Select Used			

ACCESS (SEEK) MODES

GENERAL

A Seek operation begins by setting the Access Enable FF (CR 102). This is done manually by actuating the START switch to provide the Start Access signal, or automatically by the FTU logic when the read/write sequence (if any) for the previous Seek operation has been completed.

Four elements provide the timing for the access modes.

- Servo Clock Counter (CR 118). A hexadecimal counter that counts the servo pulses from the disk (repetition rate = 9.667 MHz). It is basically a Divide by 8 or Divide by 12 counter, controlled by the speed of the drive under test. For 2400 rev/min drives, the counter divides by 8; for 3600 rev/min drives, it divides by 12.
- 2. Character Counter (CR 118). A 15-stage counter that is loaded with the count of 1 for each index mark (disk revolution), providing an error has not occurred during the preceding revolution. When an error appears the counter is disabled, allowing the character count at the time of the error to be displayed.
- 3. Access Timing Counter (CR 103). A hexadecimal counter that is operational whenever a Seek is to be performed. The counter advances each time bit 2¹ of the Character Counter goes from high to low.
- 4. Access Timing Decoder (CR 103). This decimal decoder provides T-pulses (T0 through T7) from the encoded outputs of the Access Timing Counter. Table 3-3 describes how the T-pulses are used by the FTU.

TABLE 3-3. ACCESS TIMING FUNCTIONS

			1
Count	C.R. No.	Function Provided	Operational During
т0	120	1. Raises Tag 2 (Head Select) to drive so as to transmit head address during any access sequence.	All R/W
	120	2. Pseudo TO that transmits contents of HAR to drive for each R/W disk revolution after the first (ON CYL) revolution.	All R/W
Tl	104	1. Loads Cyl Addr Reg with contents of Cyl Addr switches if CAR=0.	Seq Rev seek
	104	2. Increments CAR.	Seq Fwd or Random seek
	103	3. Toggles Alternate FF.	Continuous seek
т2	104	1. Load CAR with contents of Cyl Addr switches.	Direct seek
	106	2. Clear HAR unless Manual Hd Sel mode.	All R/W
т2.5	105	3. Load Cyl Addr Mux with contents of CAR.	All seeks
	106	4. Decrease count in CAR (trailing edge)	Seq Rev seek
т3	123	1. Gate Cyl Addr to drive (Tag 1)	All seeks
	107	2. Set Not Offset II FF	Seek with Offset
	102	3. Set Last Access FF if R/W follows.	All seeks
	107	4. Clear Not Offset I FF	Seek without Offset
Т4	106	1. Load HAR with contents of head address switches if in Manual head select mode.	All R/W
Т5 Т6		Not Used	
т7	102	Clear Access Enable FF	All seeks
	·	(See Seek flowcharts for specific actions that occur between T7 and the start of a R/W operation.)	
	107	Clear Not Offset II FF to initiate On Cylinder Delay	Maintenance Mode

SEQUENTIAL REVERSE (SEQ REV) SEEK

Figure 3-2 shows the Sequential Reverse Seek flowchart.

After setting all switches for the desired operation, RTZ must be actuated in order to clear the Cylinder Address Register (CAR) so that the contents of the Cylinder Address switches can be loaded into CAR at Tl of each "first seek" excursion of the Access Timing Counter. The Cylinder Address switches must contain a valid address for the device being tested or a Seek error will result.

For Sequential Reverse seeks, CAR counts down; for other operations (except Direct Seek), it counts up. An On Cylinder signal is returned by the drive after T7. Any error will reset the Run FF and Start Access delay (one-shot) to prevent further seeks.

When doing seeks only (no intervening R/W operations), Access Enable is cleared at T7, but is set again as soon as the T7 pulse disappears. (See the -T7 input to the 4-way NAND controlling the clock input to Access Enable -- cross reference 102). This permits uninterrupted seeks until a Stop, Reset, or Error condition occurs.

For any R/W operation, the Last Access FF is set at T3. This says that a R/W operation must be completed before another seek operation can be initiated. A R/W operation begins by setting the Rd/Wrt Enable FF as soon as an Index Mark is detected after the drive is On Cylinder. The leading edge of Index Mark then forces a pseudo T0 pulse that gates the contents of HAR (head address register) to the drive. The leading edge of each succeeding index mark increases the count in HAR (except for Manual head selection, shown below), and the trailing edge gates the address.

When HAR reaches maximum, the Last Rd/Wrt FF is set. This clears the Rd/Wrt Enable FF at the end of the current R/W cycle, which is signalled by the next appearance of Index Mark. With the Last Access FF cleared, as it was at the start of the R/W operation, and provided that no error stops have occurred, Access Enable again sets to initiate another seek.

For manual head selection mode, the Last Rd/Wrt FF remains set. This means that the selected track is read (written) once. The ensuing index mark then causes another seek to be initiated.

83319600 3–9

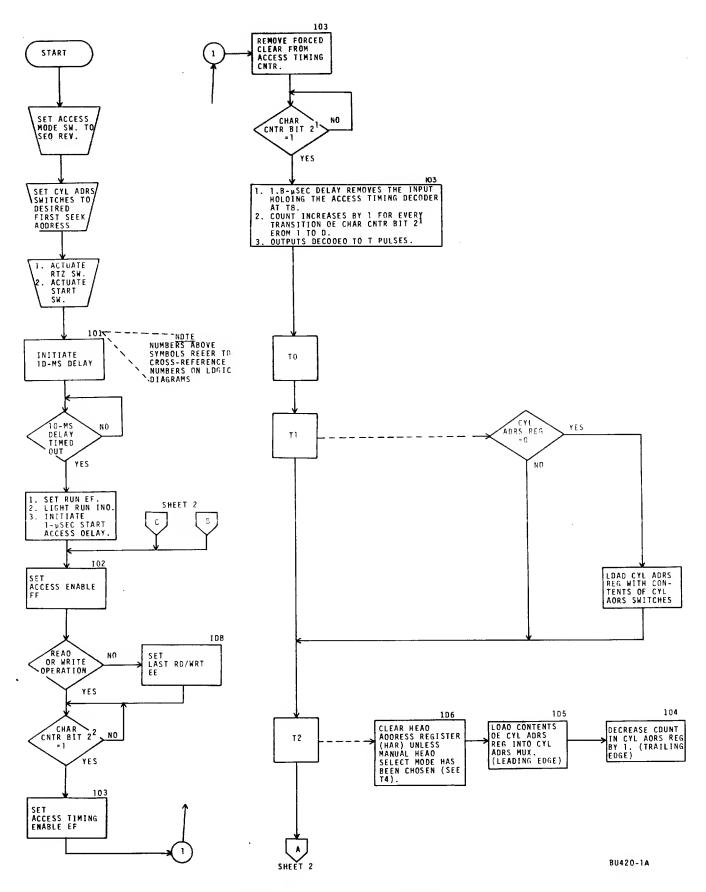


Figure 3-2. Sequential Reverse Seek Flowchart (Sheet 1 of 2)

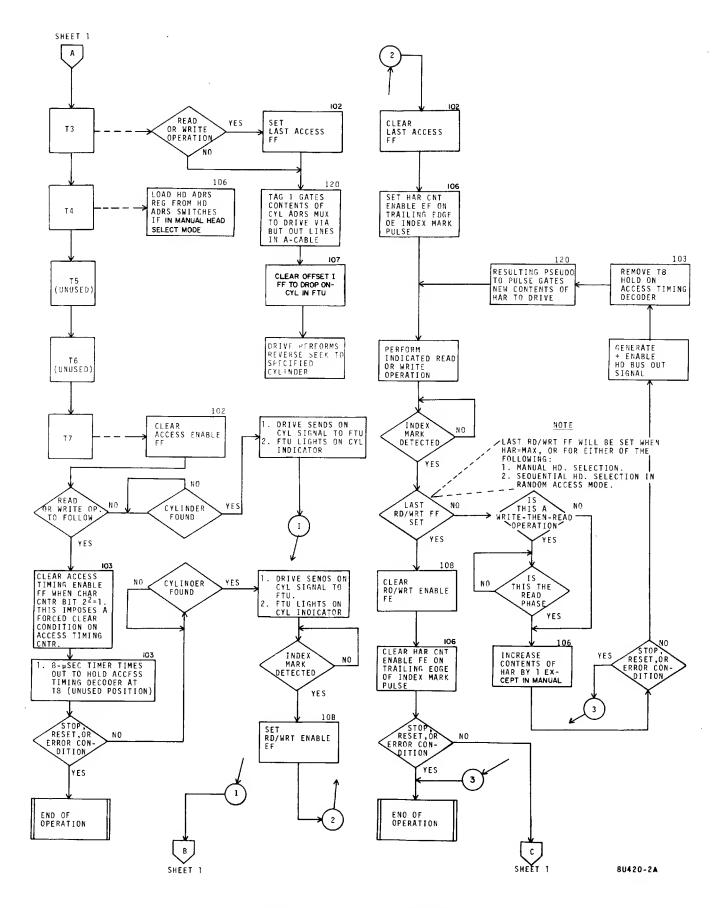


Figure 3-2. Sequential Reverse Seek Flowchart (Sheet 2 of 2)

SEQUENTIAL FORWARD (SEQ FWD) SEEK

Figure 3-3 shows the Sequential Forward Seek flowchart.

This operation is essentially the same as Sequential Reverse. The only difference is that the count in the Cylinder Address register (CAR) is increased by 1 at T1, rather than being decreased by 1 at T2. Because the first seek address is not critical, it is not necessary to clear CAR before starting the operation. In practice, however, an RTZ seek is usually performed prior to the SEQ FWD function, so CAR will be zero.

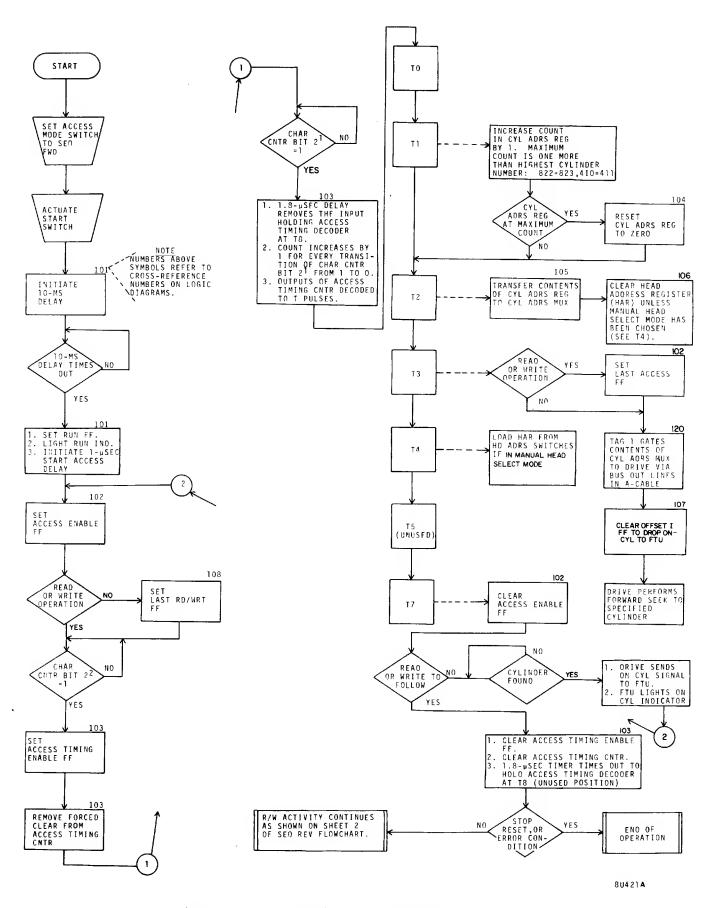


Figure 3-3. Sequential Seek Flowchart

CONTINUOUS (CONT) SEEK

Figure 3-4 shows the Continuous Seek Flow-chart.

This operation performs repeated seeks that alternate between the address set in the Cylinder Address switches and the address present in CAR at the start of the operation. The contents of CAR do not change during the operation.

The "first seek" address depends solely upon the state of the Alternate FF at the beginning of the operation. If the FF is set, it will be cleared at Tl and the contents of the Cylinder Address switches will be transferred to the Cylinder Address Mux at T2, and thence to the drive at T3. On the other hand, if Alternate is initially in the cleared state, it will be set at T1, causing the contents of CAR to be gated to the drive at T3. Normally, an RTZ function precedes a CONT seek, and leaves the Alternate FF in the set state. The first Continuous seek, then, will be to the address set in the switches.

As with sequential FWD or REV access modes, a sequential head select mode results in reading (writing) an entire cylinder before doing the next seek. For manual head selection, the selected track is read (written) once, and a new seek initiated.

3-14 83319600 C

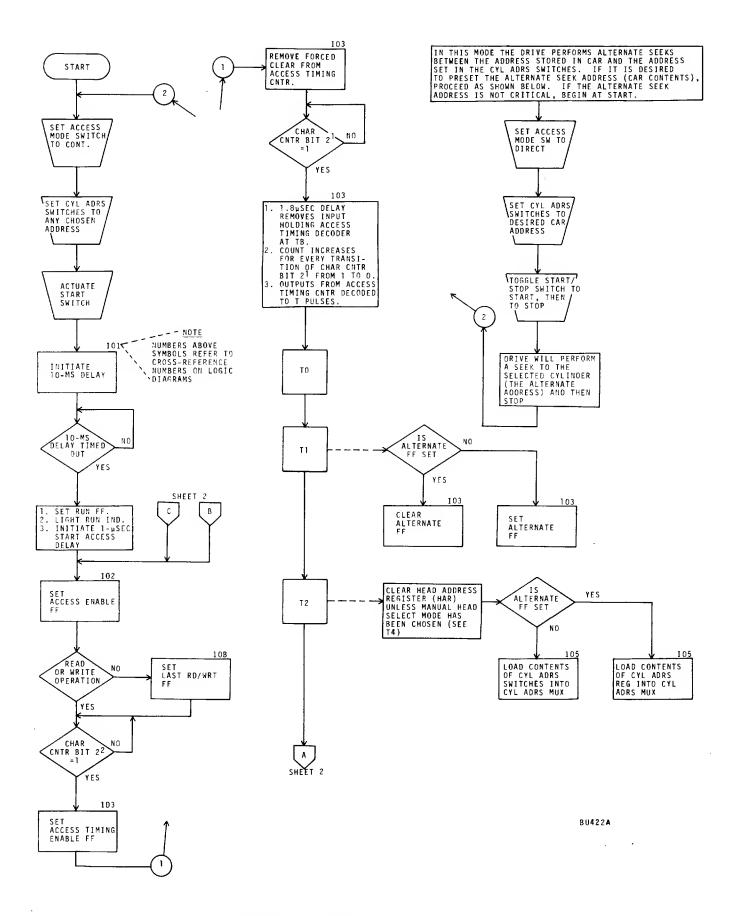


Figure 3-4. Continuous (Alternate) Seek Flowchart (Sheet 1 of 2)

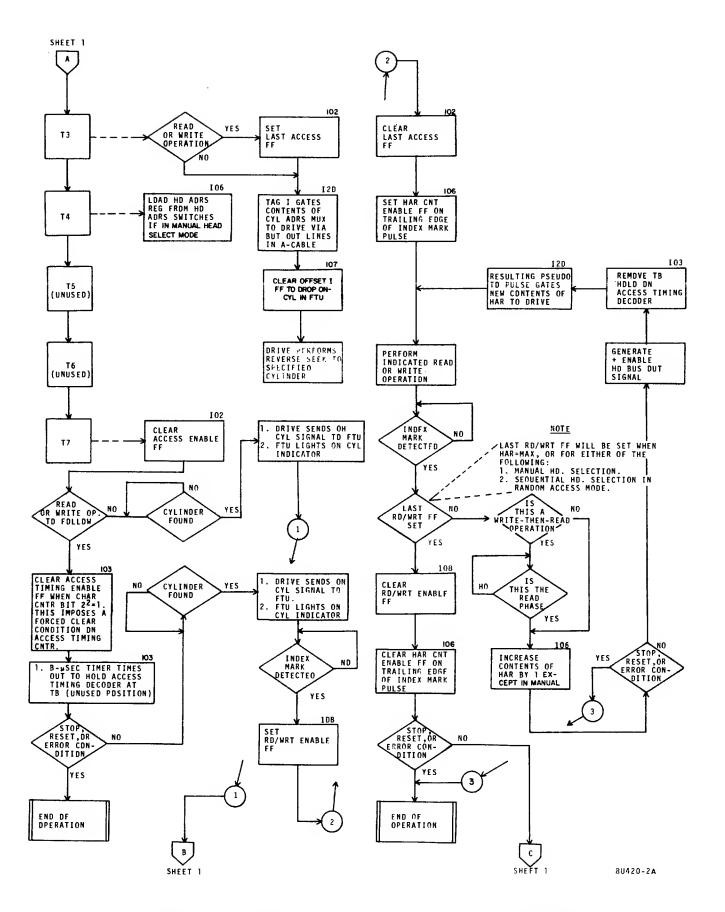


Figure 3-4. Continuous (Alternate) Seek Flowchart (Sheet 2 Of 2)

DIRECT SEEK

Figure 3-5 shows the Direct Seek flowchart.

When the access mode is Direct, the clock input to the Access Enable FF is disabled.

The FF is preset (preset input enabled) by actuating the START switch, and cleared (preclear or Reset input enabled) at T7. Consequently, only one seek is performed for each actuation of the START switch.

83319600 C 3-17

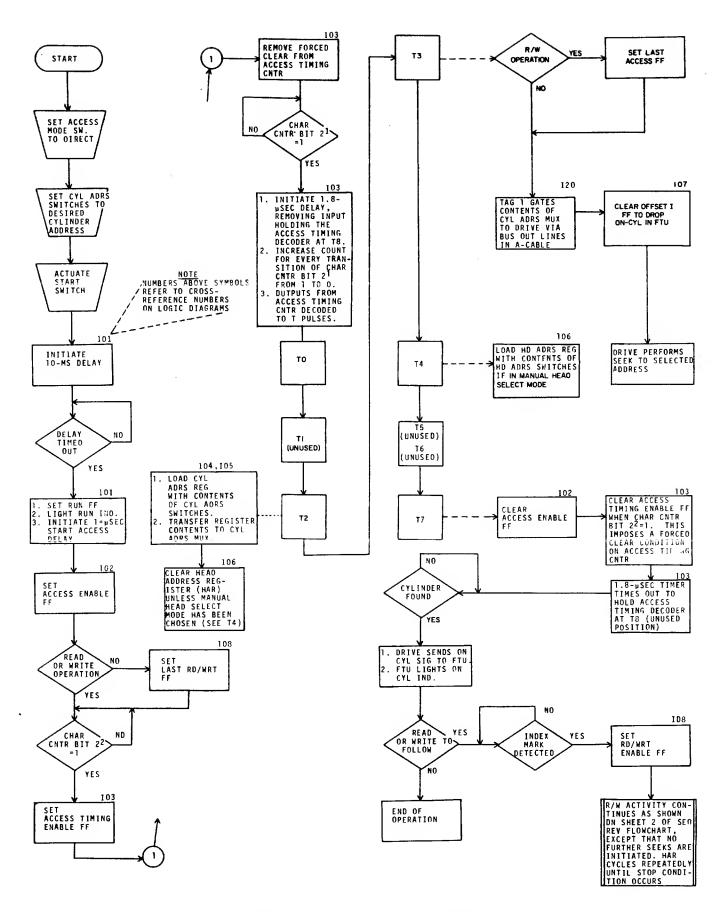


Figure 3-6. Direct Seek Flowchart

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RANDOM (RAND) SEEK

Figure 3-6 shows the Random Seek flowchart.

In this mode, the contents of CAR is increased by 1 for each servo clock pulses (FTU Write Clock) that occurs while the drive is "off cylinder". When the drive returns an On Cylinder signal, CAR stops counting and contains the address that will be sent to the drive when the next Seek command is issued.

The contents of CAR is loaded into the Cylinder Address Mux by the first 806-kHz pulse occurring during T2. At T3, this address is gated to the drive. Even if the drive is already on cylinder (as it would be, for example, if RTZ were selected just prior to

the Random operation), it brings down On Cylinder for a minimum of 30 μsec . For the first Random seek, then, CAR counts up during this 30- μsec period. For succeeding seeks (in Random), the counting time is determined by how long it takes the drive to move to the new cylinder and respond with the On Cylinder signal.

This operation is unique in that it does not clear HAR during a Sequential head selection. Moreover, whether in Sequential or Manual (head select modes), only one R/W operation is executed for each seek. That is to say, for Write Then Read mode, a seek is initiated for every two disk revolutions; for other R/W modes, a seek is initiated after each revolution.

83319600 _C 3-19

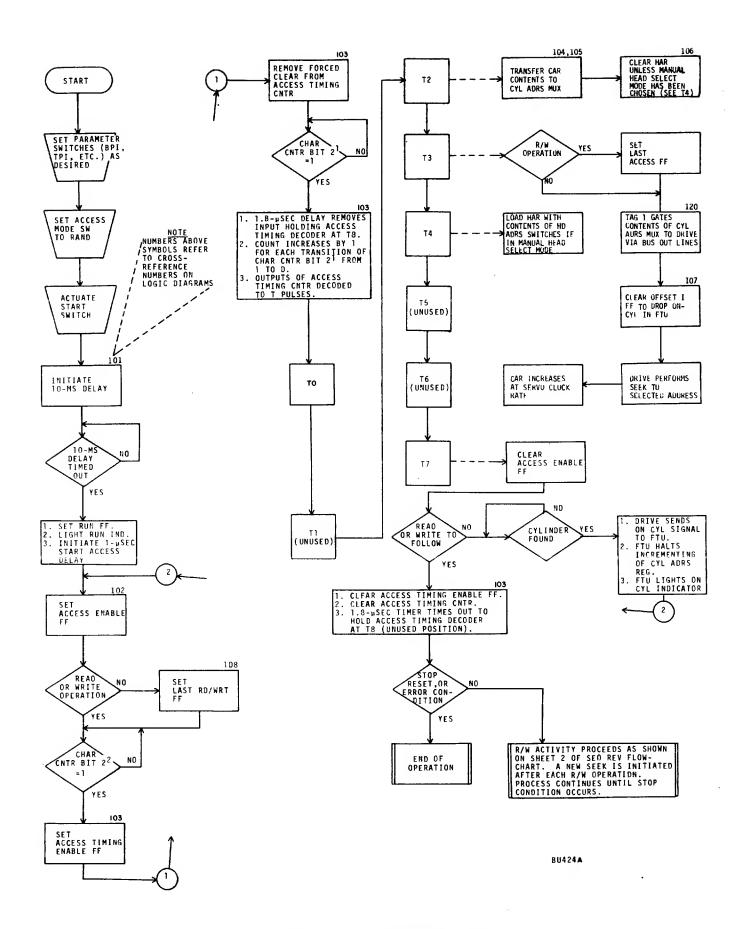


Figure 3-6. Random Seek Flowchart

READ/WRITE OPERATIONS

Read or Write operations are performed by raising the Read Gate or Write Gate signals to the drive, as shown in figure 3-7. Figures 3-8 through 3-13 show how each Wrt-Rd mode (except Write then Read, which is a combination of Write and Read operations on successive disk revolutions) affects and interprets the basic track format. Timings shown in figure 3-7 are valid for a 3600 rev/min drive, which as this is written are the only drives available for testing with the TB304. The other figures relate the track format to character counts, which are the same for either speed.

WRITE FORMAT SEQUENCE

The Write Format sequence writes an entire track from the Address Mark to the end of the data field, arbitrarily timed to coincide with character 13,376. Figure 3-8 shows the now-familiar track format, with the character counts that start and stop the various fields. Use this figure as a reference when following the Write Format flowchart (figure 3-9).

NOTE

One character is equal to 12 bits of data. One byte = 8 bits. Therefore, 2 characters = 3 bytes.

83319600 D 3-21

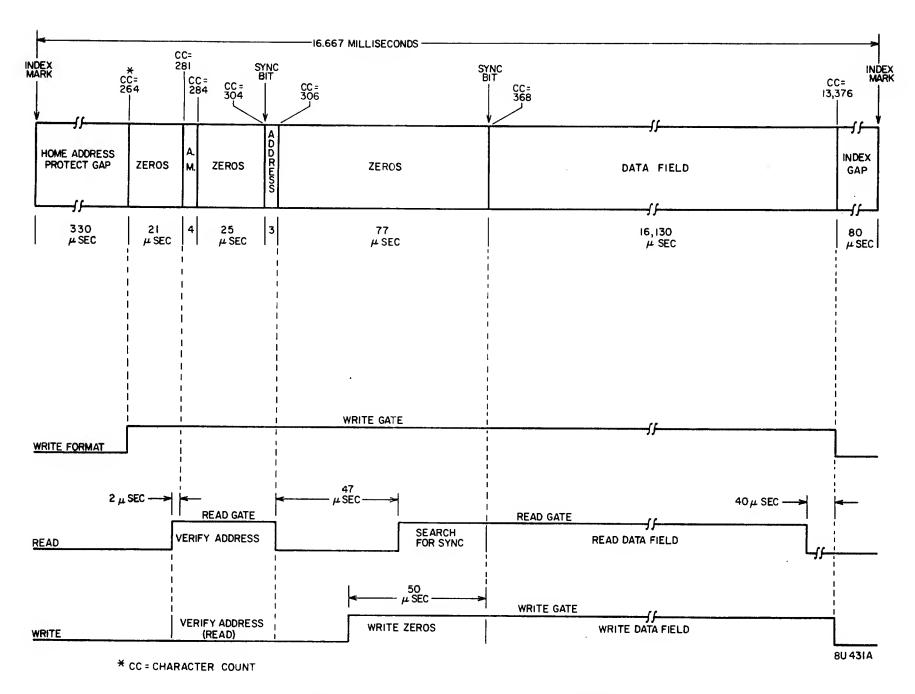
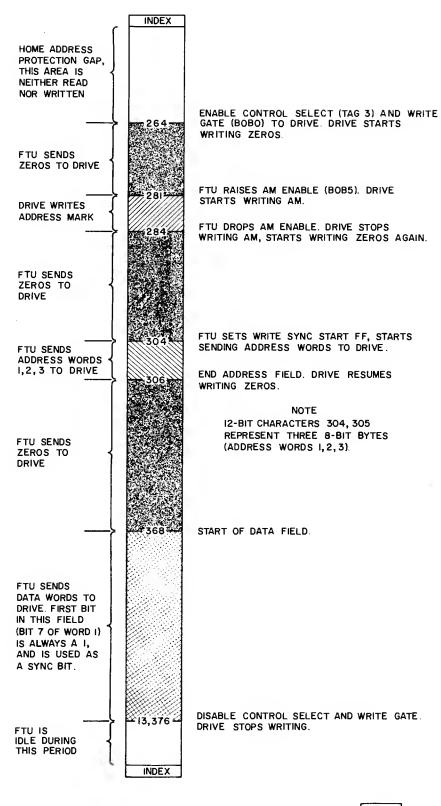
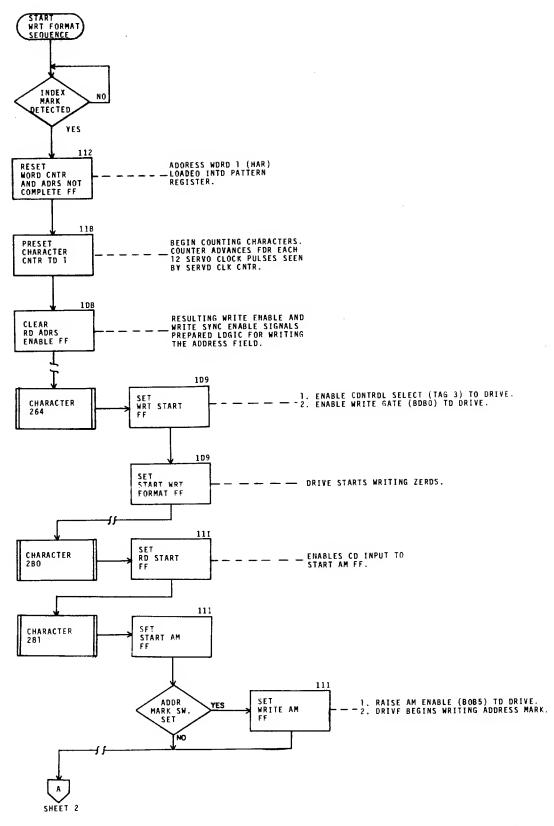


Figure 3-7. Track Format and Basic R/W Timing



8U426

Figure 3-8. Write Format Track Pattern



BU425-1

Figure 3-9. Write Format Flowchart (Sheet 1 of 5)

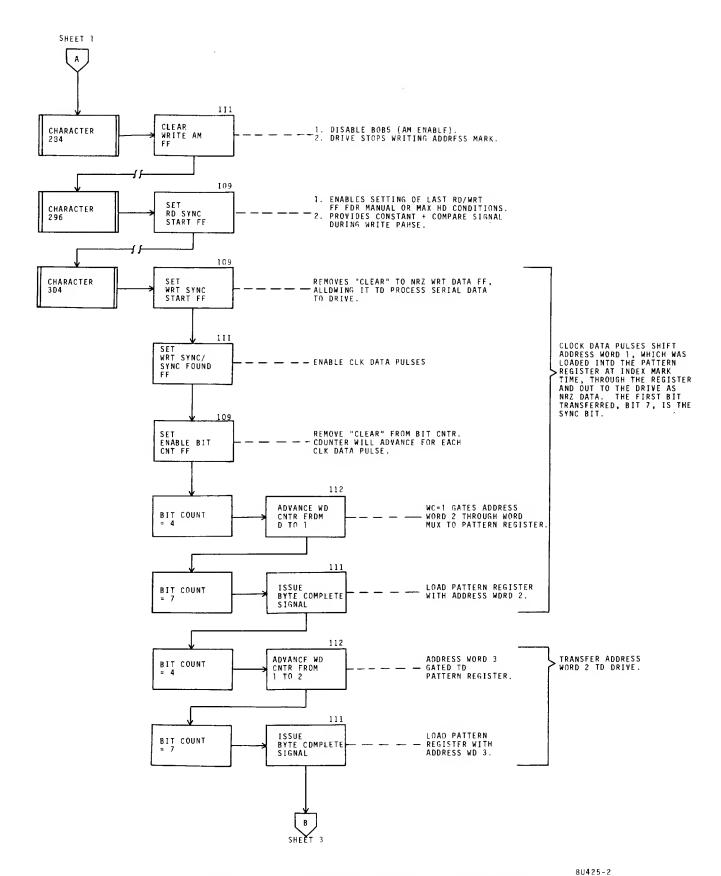


Figure 3-9. Write Format Flowchart (Sheet 2 of 5)

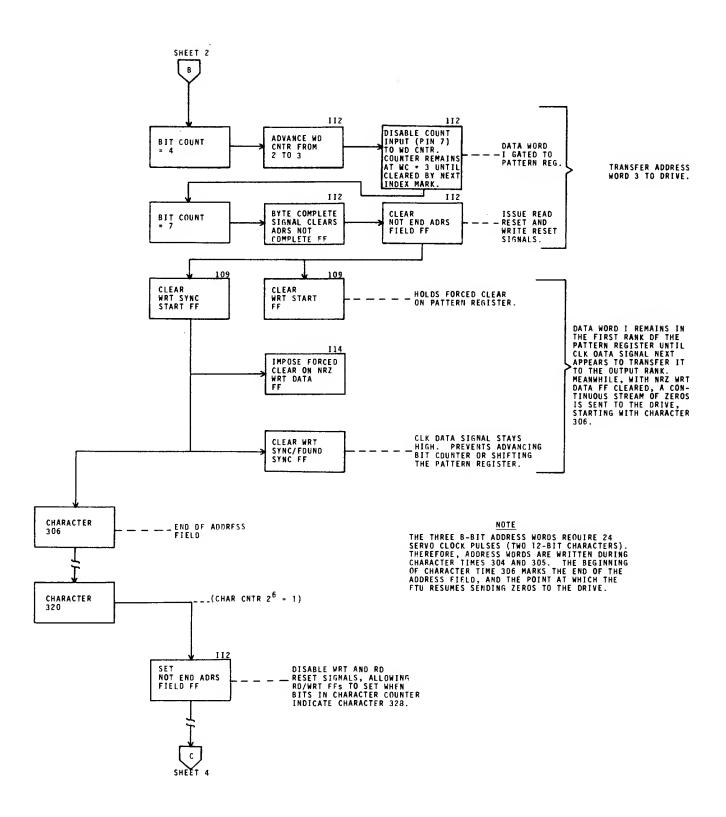


Figure 3-9. Write Format Flowchart (Sheet 3 of 5)

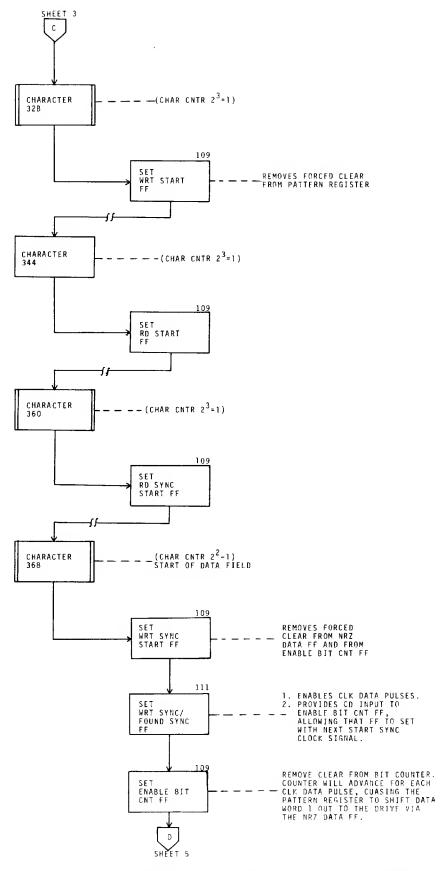


Figure 3-9. Write Format Flowchart (Sheet 4 of 5)

83319600 C

3-27

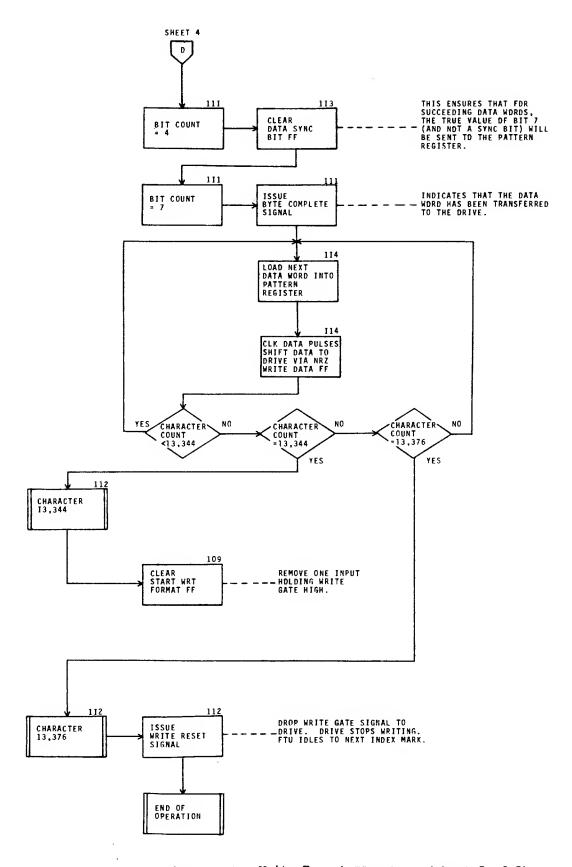


Figure 3-9. Write Format Flowchart (Sheet 5 of 5)

8U425-S

READ SEQUENCE

The Read sequence brings up the Read Gate twice. Once to read the address field and again to read the data field. Although the zero field following Address Mark is "read" by the FTU, the bits cannot be shifted in the Pattern register because the Clk Data pulses do not start until the Address Sync bit is found (at Character time 304). Figure 3-10 shows where the FTU "idles" through

the first portion of the zero field that follows the address field.

When checking for the data sync bit, Read Gate is raised early enough to allow for any minor variation in disk speed — that is, 16 counts before the sync bit is supposed to appear (roughly 20 μ sec). By the same token, Read Gate is dropped 32 counts before the end of the data field.

83319600 C 3-29

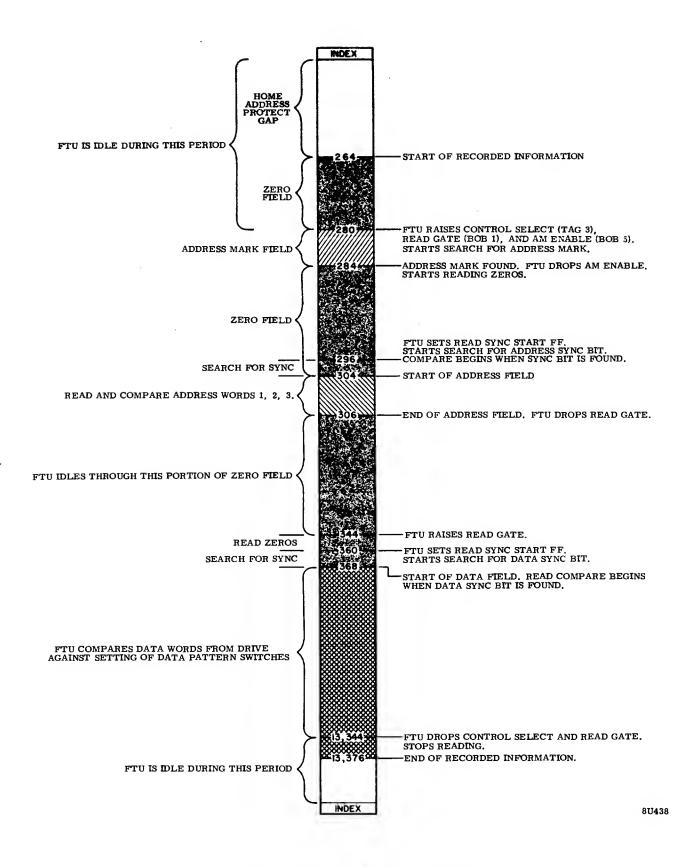


Figure 3-10. Read Track Pattern

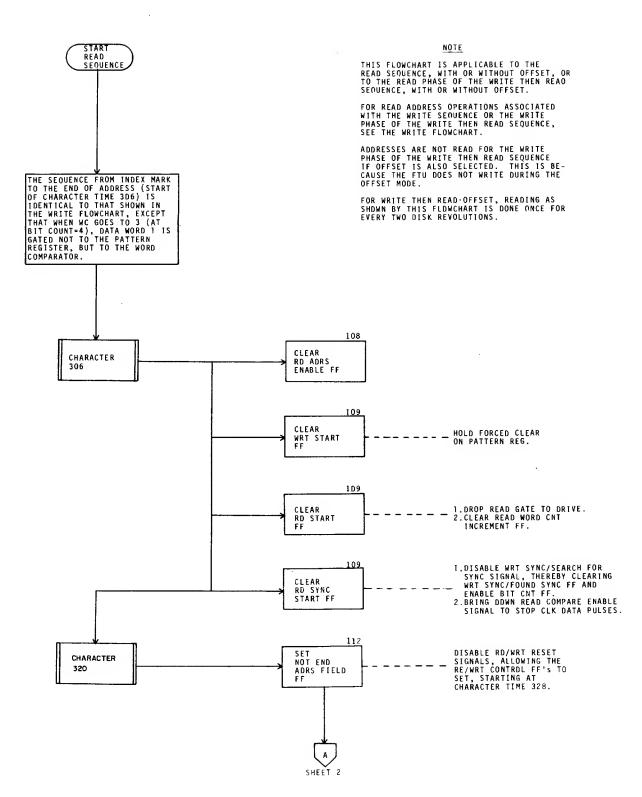


Figure 3-11. Read Flowchart (Sheet 1 of 3)

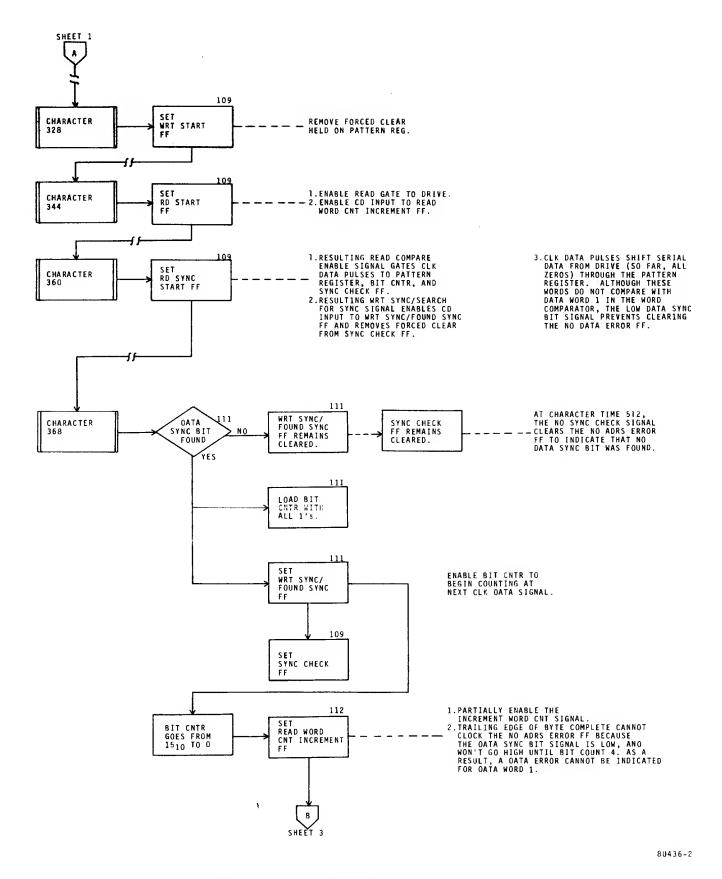


Figure 3-11. Read Flowchart (Sheet 2 of 3)

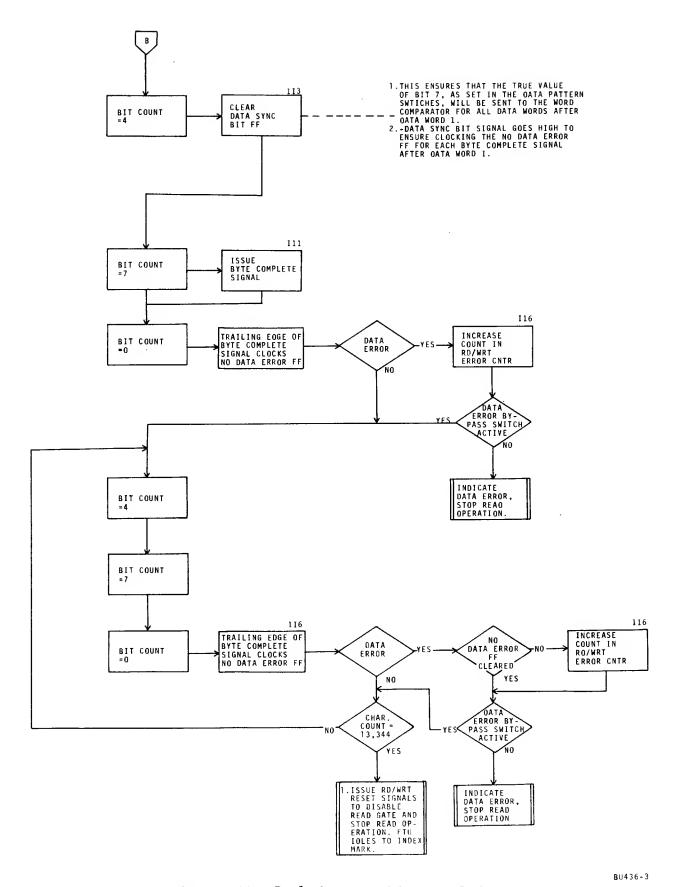


Figure 3-11. Read Flowchart (Sheet 3 of 3)

WRITE SEQUENCE

The Write sequence is identical to the Read sequence up to character time 328, at which point the FTU raises Write Gate and the drive

starts writing zeros. The data field is written as for Write Format. The Write track pattern and the Write Sequence flowchart are shown in figures 3-12 and 3-13, respectively.

3-34 83319600 C

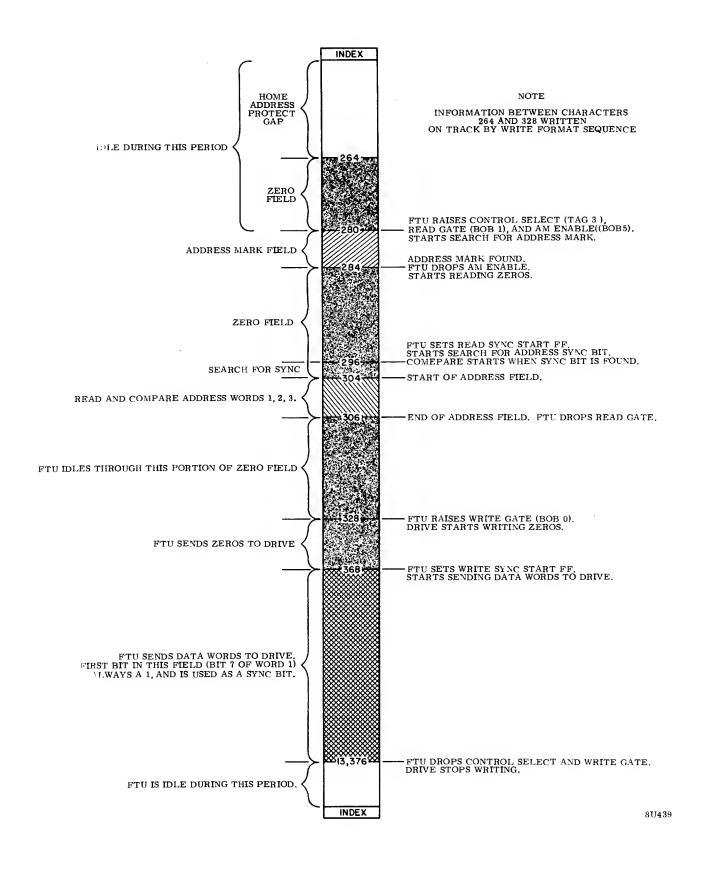


Figure 3-12. Write Track Pattern

83319600 C 3-35

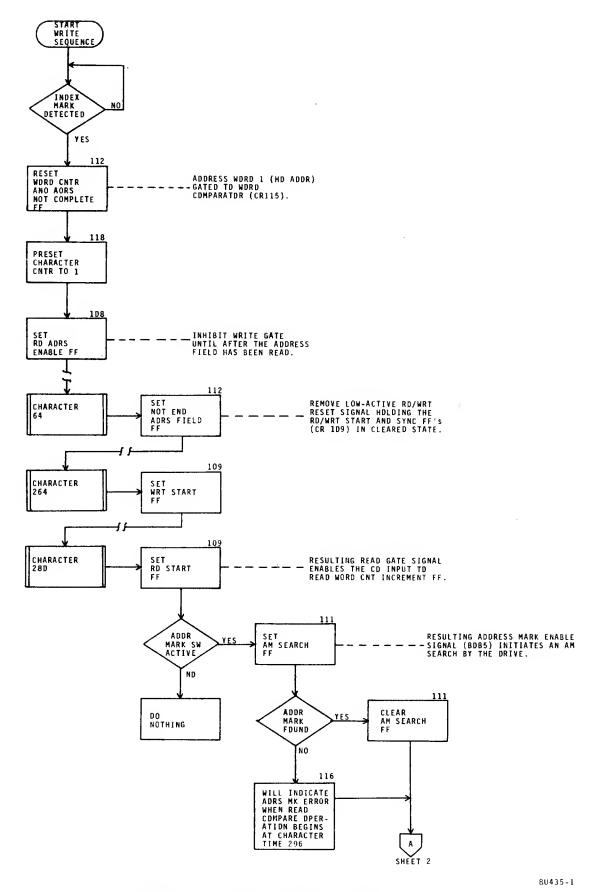


Figure 3-13. Write Flowchart (Sheet 1 of 5)

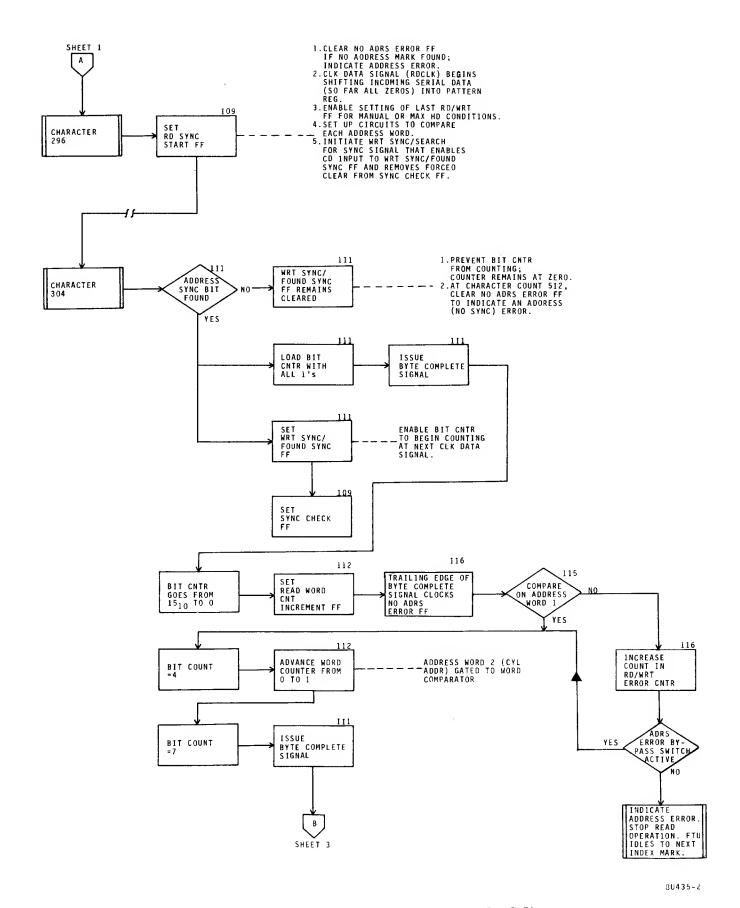


Figure 3-13. Write Flowchart (Sheet 2 of 5)

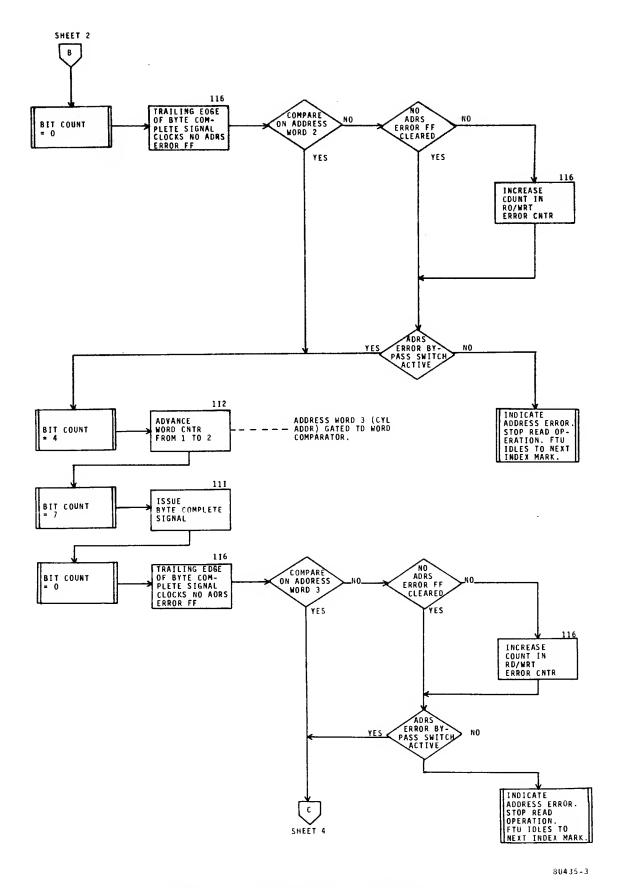
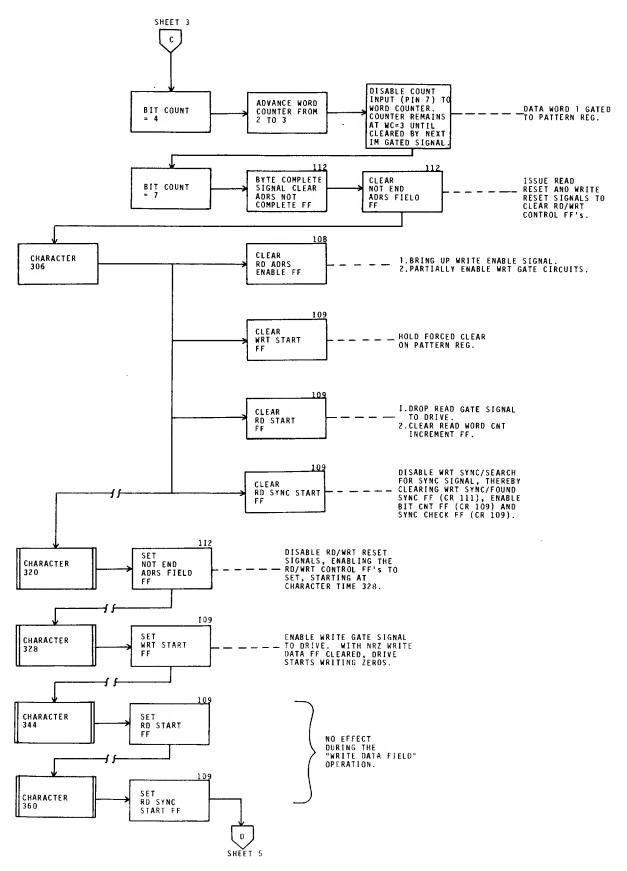


Figure 3-13. Write Flowchart (Sheet 3 of 5)



80435-4

Figure 3-13. Write Flowchart (Sheet 4 of 5)

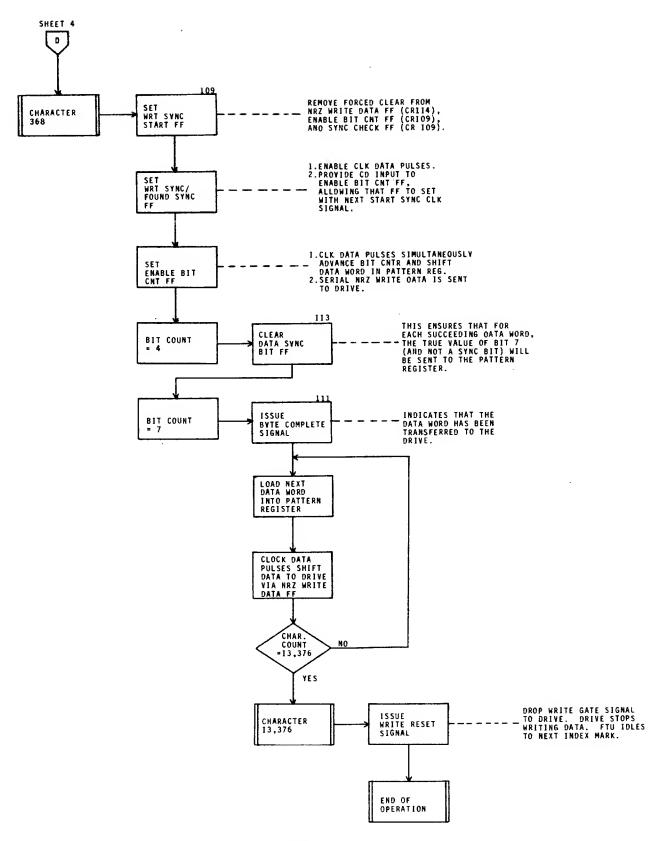


Figure 3-13. Write Flowchart (Sheet 5 of 5)

80435-5

OFFSET MODE/DELAYED ON CYL

Figure 3-14 describes how the On Cylinder Detected signal is delayed by 4 ms during an Offset operation. The delay is necessary because the On Cylinder signal from the drive, once the drive has found the cylinder address, drops while the heads are moved to the offset position. When the offset move has been completed, On Cylinder comes up again to

retrigger the 4-ms delay. When the delay times out, the On Cyl Delayed FF is set, propagating the On Cyl Detected signal to the FTU logic.

The delay is also operative when the MAINTE-NANCE switch on the FTU Panel is active.

Writing cannot be performed during the Off-set mode.

83319600 C 3-41

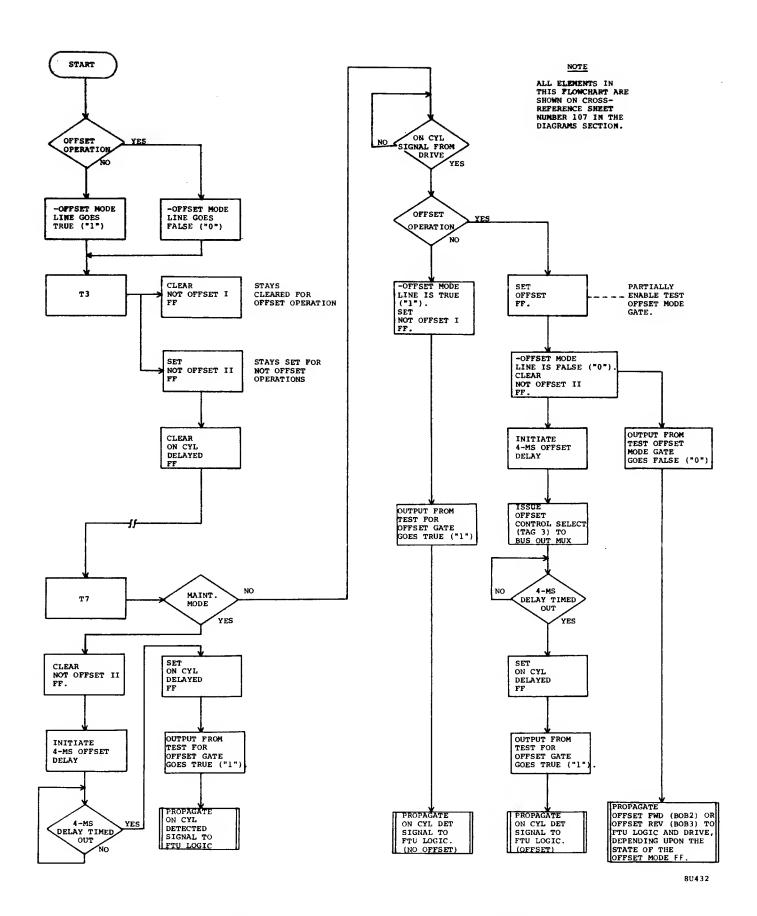


Figure 3-14. Offset/Delayed On Cyl Flowchart

FTU ERROR LOGIC

Of the five error indications provided on the FTU control panel, SEEK ERROR (as well as FAULT indicator) is generated in the drive. The following pages describe the four error indications that are detected by the FTU logic.

ADDRESS ERROR/DATA ERROR LOGIC (CR 116)

If the FTU is in Maintenance mode, or if the I/O Bypass cable is connected, the address/data error logic is disabled. The FTU will not stop for these errors, nor will the error(s) be counted in the R/W Error Counter. In the Normal mode, the error logic is disabled by holding both the Address Error and Data Error FFs in a preset state.

A preset pulse to these FFs also occurs at Index Mark time if the corresponding Error Bypass switch is active. Under this Error Bypass condition, the FTU will not stop after an Address or Data error, but the error will be counted by the R/W Error counter. The presence of each error type is manifested by clearing the appropriate Error FF. Because the R/W Error counter is edge-triggered, contiguous errors (errors with no "valid" word between them) from either of the Error FFs will be counted as one error.

Address Errors

Address errors are of three kinds:

- a. Address Mark error
- b. Sync Check error
- c. Address Word error

Address Mark Error (CR 111)

When the Address Mark switch is active, the AM Search FF is set by Read Gate at character time 280. The FF is cleared when the address mark is found. If the FF is still set (no address mark found) when Read Compare Enable comes up at character time 296, the Address Mark Error signal clears the Address Error FF (CR116).

Sync Bit Error (CR 116)

The address field sync bit occurs at about character time 304. The Data field bit occurs at about character time 368. If either of these sync bits are missing (-Sync Check = "1"), the Address Error FF will be cleared.

Address Word Error (CR 116)

If one of the three address words does not compare, the + Compare line will go low, as

will the CD input to the Address Error FF. Because the FF is clocked by the trailing edge of Byte Complete for address words 1, 2 and 3 (-Word Count 3), the FF will clear.

It may be of interest to note that the + Compare line is held high until character time 304 (address), and again between the end of the Address field and character time 296, at which point the Bit Counter starts counting Data bytes. Thus, + Compare ensures that the FF will be initially set at the start of both the Address and Data fields, so as to be ready for the sync bit that begins each of those fields (as well as for any errors that might occur in the address field itself).

Data Errors

Data errors consist of Read Clock errors and Data Word errors.

Read Clock Error (CR 116)

Normally, Read Clock pulses will continually retrigger the Missing Read Clock one-shot. If, when Read Compare Enable comes up for the Address or Data fields, a clock pulse is missing, the one-shot will time out, thereby setting the Read Clock Error FF. This, in turn, clears the Data Error FF.

Data Word Error (CR 116)

The Data Error FF is set prior to the start of the Data field by the high held on the + Compare line. (The logic for Compare is shown on CRl17.) The -Data Sync Bit line is high after the Address field, so the Data Error FF is clocked by Byte Complete, starting at character time 296. When the hold on + Compare goes away at character time 368, the compare sequence begins. A Compare error is registered by clearing the Data Error FF at the trailing edge of Byte Complete. The next valid compare will again set the FF. As described above, contiguous bytes in error are counted as one error.

SERVO CLOCK ERROR LOGIC (CR 116)

A Ready signal accompanying the Start Access pulse sets the Enable Servo Clock Error FF CR117). If a period of 200 nsec occurs without an FTU Write Clock pulse, the Servo Clock Error FF is set. The condition can be cleared only by actuating RESET. The FTU Write Clock may be either servo clock pulses from the disk, or pulses from the crystal oscillator in the FTU itself, depending upon the position of the MAINT/NRM switch on the FTU.

SECTOR MARK ERROR LOGIC (CR 117)

The Sector Mark (SM) Error FF is held in a precleared state by one of the following:

- 1. Enable Sector Mark Error signal = 0
 (see CR 116 for conditions).
- The normally cleared state of the Enable Servo Clock Error FF.

If case 1 exists, SM errors are not detected. The case to consider, then, is that in which the preclear on the SM Error FF is released by the presence of a Ready signal at Start Access time.

Normal Sequence

Index Mark (IM) sets the Load Sector FF, imposing a Load condition on the Sector counter by bringing pin 9 of that IC low. The Sector Counter is actually loaded by the trailing edge of the IM. The leading edge of the first SM after Index clears Load Sector, allowing the counter to be advanced by the trailing edge of that (and each succeeding) SM. The next IM finds the sector count at maximum, which results in presetting the Test Sector FF. The trailing edge of that IM, of course, reloads the Sector Counter. The ensuing SM (leading edge) clears Load Sector, while its trailing edge advances the count and clears Test Sector.

Abnormal Sequence

A Sector Mark Error can occur for two situations:

- a. Index Mark appears before the Sector Counter has reached its maximum count.
- b. A sector mark appears when the Sector Counter is already at maximum.

Situation 'a' will occur if a sector mark is missing (or has not been read). Situation 'b' will occur if there are more sector marks per disk revolution than are indicated by the setting of the Sector Mark switches, or if an IM pulse is missing.

Situation 'a'

The first sector mark after Index clears the Test Sector FF as usual, providing a logical l at the CD input to the SM Error FF. The next Index presets the Load Sector FF, as usual. The following sector mark then clears Load Sector (as usual), providing a positive-going trigger pulse that now combines with the CD input to set the SM Error FF.

Situation 'b'

A sector mark that appears when the Sector Counter is already at maximum presets the SM Error FF.

The setting of the SM Error FF for either of the situations above ensures that the Load Sector FF will be set by the leading edge of the next sector mark. This holds the Sector Counter in a Load state, which prevents the counter from advancing. The circuit is returned to normal by clearing the SM Error FF. This occurs at Index Mark time if the Address Error Bypass switch is active, or when the RESET switch is actuated. Either case forces the + Enable Sector Mark Error signal low.

UNIT SELECT/RELEASE LOGIC

The contents of Unit switches 8,4,2,1 is available in two places in the A cable: on Bus Out lines 0-3 (CR 120), and on Unit lines 20-2³ (CR 124). In drives having the 60-pin A cable, the unit information is derived from the Unit lines. For 50-pin I/O drives, the Unit lines are not present, so the information must be obtained from the Bus Out lines. In either case, the Unit Select Tag (CR 124) is sent separately -- that is, not across the Bus Out lines.

With the UNIT SEL/REL switch in neutral, a forced clear is imposed on the Unit Select FF (CR 122). When the switch is raised to the SEL position, the clear is removed and a 5-ms pulse generated that sets the FF, causing the Unit Select Tag to be sent to the drive (CR124). At the same time, the pulse causes a 5-ms Unit Bus signal that selects input 3 of the Bus Out Mux (CR120), thereby gating the contents of the four UNIT switches to the Bus Out lines. (Unit lines 20-23 are static, whereas Bus Out Bits 0-3 are seen as a 5-ms pulse.)

Returning the switch to neutral drops the Unit Select Tag.

The momentary REL (Release) position of the switch is for use only in dual-access drives. Issuing the 5-ms Unit Select Tag is accomplished as described above. After another 5 milliseconds, the Release delay (CR102) times out. This causes input 2 of the Bus Out Mux to be selected (CR 120), sending the Release signal to the drive along with Tag 3. Returning the switch to neutral (center-off) drops the Tag 3 indication.

FIXED HEAD OPERATION

Three switch combinations can affect the Sel Fixed Hd FF (CR 105):

- If FIXED () is the combination selected, the FF is preset to select only fixed heads (numbered 0-47).
- If either MOVABLE or SMD positions are selected, the FF is force-cleared; in effect, it doesn't exist.
- If BOTH is selected from the FTU panel, the FF will toggle with each +Clr Cyl signal issued by the CAR logic (CR 104).

Let's examine these situations in order.

PRESET -- EXERCISE FIXED HEADS ONLY

Inputs to the Hd Adrs Comparator (CR 106) will allow a maximum head address of 3. The +Sel Fixed Hd signal (CR 105) enables the +CAR 512 and +CAR 256 lines from the display mux (CR 119) to the drive, informing it to select one of 48 fixed heads, depending upon the lower two bits of HAR and the lower four bits of CAR (in the FTU).

With HAR and CAR initially cleared, fixed head 0 is selected, followed by 1, 2, and 3 as HAR advances to maximum. CAR is now advanced, HAR is cleared, and the next cycling of HAR selects fixed heads 4, 5, 6, and 7. This sequence is repeated until CAR reaches 12, whereupon a 0.475 μs +Clr Cyl pulse clears CAR (CR 104) and the entire procedure begins again.

FORCED CLEAR -- EXERCISE MOVABLE HEADS ONLY

Action is as shown in the R/W mode flow-charts. The fixed heads are not exercised in this situation.

TOGGLE -- ALTERNATELY EXERCISE MOVABLE AND FIXED HEADS

Assume that the Sel Fixed Hd FF is initially cleared, along with CAR and HAR. All movable heads are first exercised through all access positions (as determined by the access mode), starting with cylinder 0, head 0. After maximum cylinder is reached, the +Clr Cyl pulse clears CAR and toggle-sets the Sel Fixed Hd FF.

All 48 fixed heads are now exercised as described above until, when CAR reaches 12, a low-active -Clr Cyl signal (CR 104) initiates the +Clr Cyl pulse (CR 105) to clear CAR and toggle-clear the Sel Fixed Hd FF. At this point, the pattern for exercising the movable heads is again entered, and the entire sequence starts anew.

HEAD ALIGNMENT CARD (HFSV)*

The Head Alignment circuit receives the positive and negative dibit information from the Servo or Read/Write heads and processes the information to produce the offset indica-Two types of offset indications are tions. produced: the visual indication available on the head alignment meter, and the sign information that is used by the controller during the automatic head alignment proce-The meter reading is a halved indication of head offset expressed in microinches. Total offset is the sum of the absolute value given for both positions of the POS/NEG switch. The sign information is a changing polarity signal produced each time the FTU meter passes through zero.

The alignment card receives dibit information from either the Servo head or one of the Read/write heads depending on the position of the R/W-SERVO switch.

The received dibit information is amplified and gain scheduled such that the total of the positive and negative signal is maintained at 500 mV. A normal On Cylinder signal would contain 250 mV of positive dibit information and 250 mV of negative dibit information. If the negative dibit information decreases to 150 mV the automatic gain control circuit causes the positive dibit information to increase to 350 mV. This results in the combined output being maintained at 500 mV.

Non-AGCed Servo and Read/Write head information is fed through switching circuits and applied to the input of a minimum level detector circuit. Because the read/write head information has a lower signal level, it is gated through an additional gain stage before it is applied to the level detector. As long as the signal level is of sufficient amplitude, the output of the level detector retriggers a one-shot circuit. The time constant of the one-shot is selected so that the circuit will not time out unless the output of the level detector fails to retrigger it.

The output of the minimum level detector's one-shot is used to gate on the midpoint detector circuit. The AGCed positive and negative dibit signals are biased above and below the zero threshold. When the two out of phase signals reached the zero point simultaneously, they turn on the midpoint detector. The output of the midpoint detector drives two one-shot circuits. The one-shots are retriggered by the second midpoint pulse before they time out. The time out of the one-shots is the Read Gate for the Peak Detector circuits.

83319600 D

^{*} TB304A/B only

The Peak Detector circuit alternately detects the positive and negative dibit peak amplitudes. These peaks are used to charge up two capacitors. The difference in potential between the two capacitors represents the

amount and the direction of the head offset. The two capacitors drive a differential amplifier. The amplifier output is then scaled and limited and used to drive the head alignment meter.

3-46 83319600 C

SECTION 4

MAINTENANCE

POWER SUPPLY VOLTAGE CHECKS

Two 5 V power supplies provide overvoltage protection that drops the output voltages to about 1 volt if the supply voltage should exceed approximately 6.5 volts. The exact point at which the drop in output occurs is preset at the factory with no load on the supply. In addition, each supply has a Voltage Adjust pot to set the full-load output voltage to 5 V, ±0.5 V. The location of these four potentiometers is shown in CR500 of the Diagrams section.

Holes in the front wall of the power supply compartment allow access for screwdriver adjustment of the two +5 V pots. The -5 V pots may be adjusted by using the finger tips to turn the blue plastic disk attached to the shaft of each potentiometer.

NOTE

Don't capriciously tweak the factory-set Overvoltage pots. Not only can the overvoltage protection be lost if the shaft is turned one way, but also, if the shaft is rotated the other way, premature protection may be invoked that will cause the output to drop off before the supply reaches its operating voltage. The Overvoltage Adjustment procedure given below is for those relatively rare occasions where the procedure is absolutely necessary.

OVERVOLTAGE ADJUSTMENT PROCEDURE

- Turn off the FTU and remove the cover from the power supply compartment.
- Disconnect the red (+5 V) wires going to the logic board and control panel from AlTB1-8. Do not disconnect the red wire coming from the +5 V.
- Turn the +5 V Overvoltage pot fully counter clockwise (to raise the overvoltage sense beyond the range of the +5 V supply, essentially negating any regulatory actions.)
- Turn the +5 V Voltage Adjust pot fully counter-clockwise to minimize the output voltage.
- 5. Turn on the FTU.

- Connect the ground probe of a VOM to a convenient ground in the FTU case.
- Slip the other VOM probe under the screw of AlTB1-8; tighten the screw to hold the probe.
- 8. Observing the VOM, turn the +5 V Voltage Adjust pot until the meter registers +6.5 V.
- 9. Still observing the VOM, turn the Over-voltage pot slowly clockwise until the meter shows an abrupt drop from +6.5 V to about +1 volt.
- 10. Turn down the Voltage Adjust pot so the meter reads +5 V. (This reading will probably change when a load is later applied to the supply.)
- 11. Turn off the FTU, reconnect the red wires to AlTB1-8.

Repeat steps 2 through 11 for the -5 V supply if necessary, using the blue wires, AlTB1-7, and minus voltages.

NOTE

After performing the Overvoltage Adjustment, it is necessary to re-adjust the supply voltage under load, as shown in the Voltage Adjustment below.

VOLTAGE ADJUSTMENT PROCEDURE

- Remove the cover to the power supply compartment, if this was not done as part of the Overvoltage Adjustment procedure.
- 2. Connect the ground probe of a VOM to the FTU ground stud or some other convenient logical ground; connect the other probe to AlTBl-8, for +5 V adjustment, or to AlTBl-7 for -5 V adjustment.
- 3. While observing the VOM, turn the Voltage Adjust potentiometer on the requisite supply until the meter reads 5 V, ± 0.5 V.
- 4. Repeat steps 2 and 3 for the other supply.
- Replace the cover to the power supply compartment.

TROUBLESHOOTING THE FTU LOGIC

GENERAL

When using the TB304 to troubleshoot problems in the drive, it would be reassuring to know that the tester is functioning properly; that is, that the drive is not adding FTU errors to the pattern it writes or reads on the scratch pack. That is why the Operation section stressed the need to perform repeatedly any exercise that caused error stops, in order to pin-point the drive, the scratch pack, the I/O cabling, or the FTU as the source of the problem.

The flowcharts in section 3 show the sequencing of signals and commands to most FF's and registers in the FTU for each access and R/W mode. The diagrams in section 5 are arranged functionally to minimize jumping from page to page while tracing a circuit. As a result, a break in the flow of a signal or command between two flowchart points may be easily related to the gates and inverters between those points. These elements will most always appear on one or the other of the diagrams denoted by the cross-reference numbers above the respective flowchart symbols.

Remember that with the Maintenance/Normal switch in MAINT, all lines from the drive are active with the exception of Read Clock, Read Data, and Unit Ready (which is artificially generated in Maintenance mode), and that the write functions are not impeded. The above statement assumes, of course, that the A and B cables are connected to the drive.

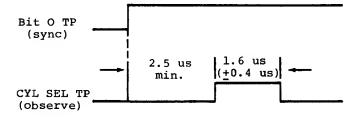
USING THE TEST POINTS

The stylized waveforms below should help in determining whether or not the FTU is performing properly. Not all test points on the panel are shown, but the method of operation may be extended to those not included.

Cylinder Select

Access Mode Cyl Addr Sw Bit 1 ON (up), others OFF

Actuate RTZ, then START. Drive alternates between cylinders 0 and 1.



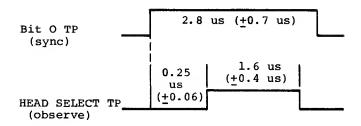
Head Select

DIRECT Access Mode Wrt-Rd Select WRT FORMAT MAN

Seq/Man

Bit 1 ON (up), others OFF Head Addr

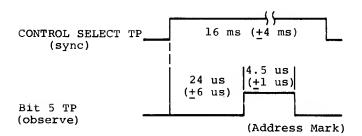
Actuate START.



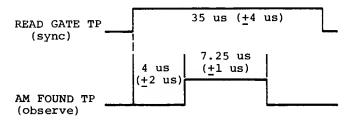
Address Mark

DIRECT Access Mode WRT FORMAT Wrt-Rd Select Seq/Man MAN Addr Mk/Sector Mk ADDR MK

Actuate START.



Actuate STOP. Place Wrt-Rd Select switch to WRT. Actuate START.

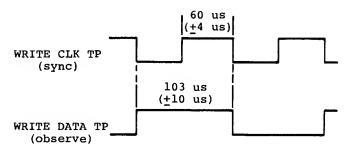


Write Clock/Data

Access Mode Wrt-Rd Select Data Pattern Sw DIRECT WRT

10 101 010 pattern

Actuate START.

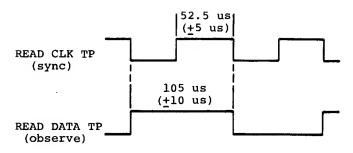


Read Clock/Data

Access Mode Wrt-Rd Select Data Pattern Sw DIRECT READ

(as for Write Clock/
Data, above)

Actuate START.



Read Gate/Write Gate

The Read Gate and Write Gate test points offer a rough index of the FTU's operation. When syncing on Index Mark, the R/W gate TP's should not deviate by more than 10% from the norms shown in figure 3-7.

SECTION 5

DIAGRAMS

This section contains the logic, cabling, and power diagrams for the TB304B/C and the TB304A. Yellow divider sheets identify the two diagram sets. The cross-reference numbers described below are duplicated for each set, except as indicated.

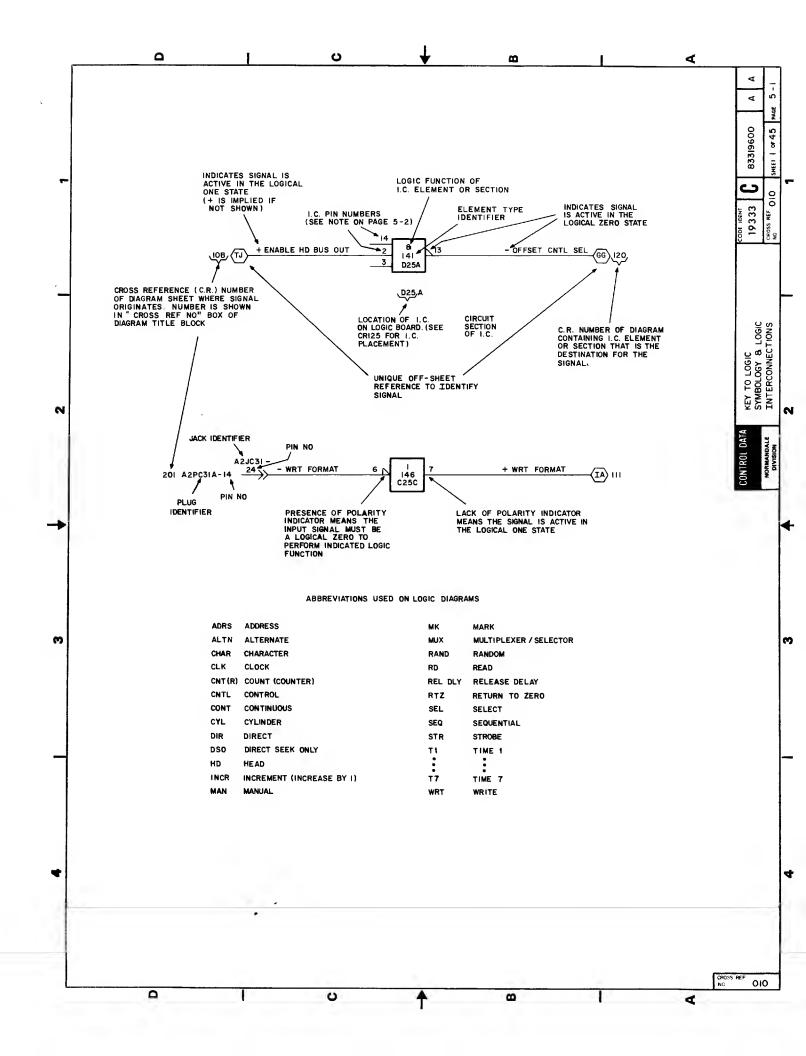
Cross-reference numbers for the diagrams fall into seven number series, as shown below. The first sheet in a series is usually a "locator" diagram that shows the physical arrangement of the electrical components within that series. A map of the logic board (IC placement) is given on CR sheet 125.

CR Series	Is Concerned With
010①	Key to Logic
100	Logic Diagrams
200	Logic Board/Control Panel Cabling
300	Panel Switches, Indicators, Test Points
400	I/O Cables
500 ②	Power Supply
600 ②	Type HFSV Head Alignment Card

5-0 83319600 D

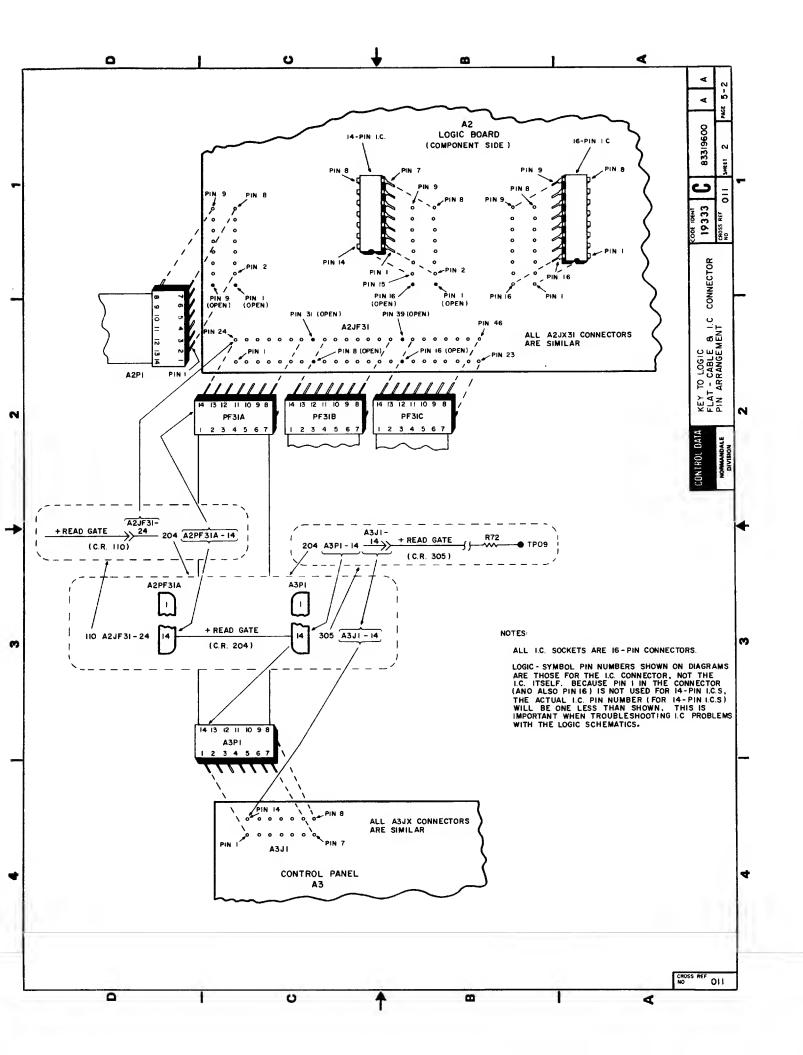
¹⁾ Found in front of TB304B/C divider, this series is common to both diagram sets.

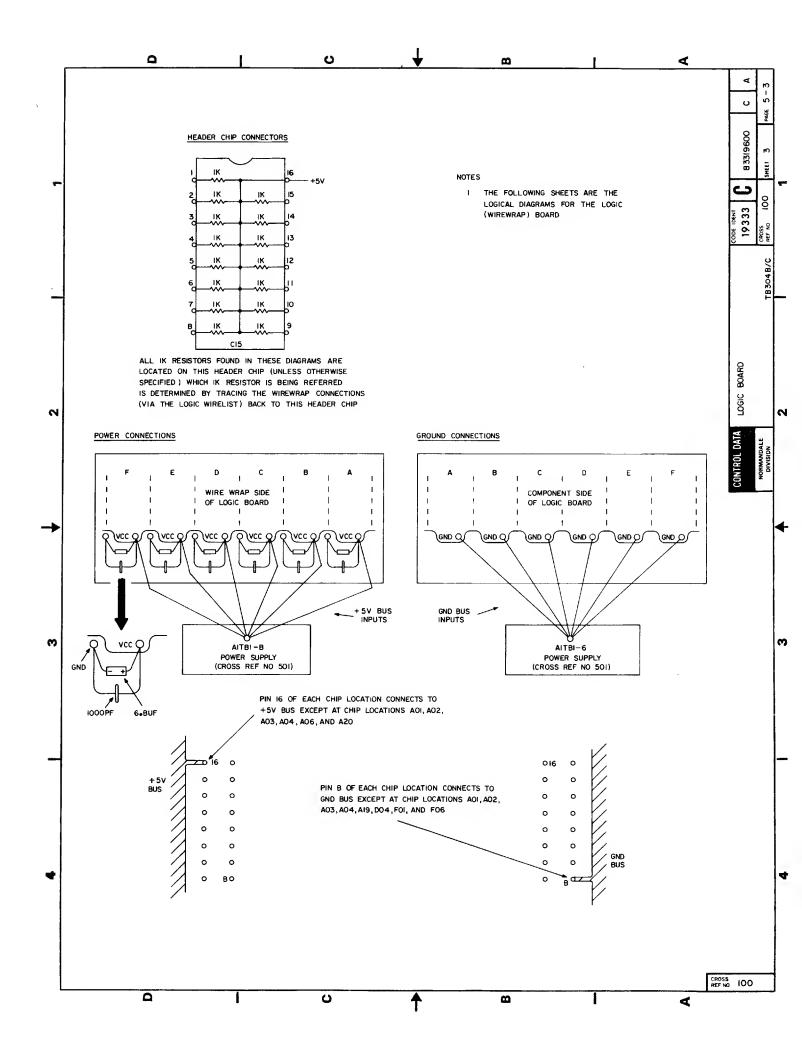
² Shown only with the TB304B/C diagram set, but applicable to the TB304A as well.

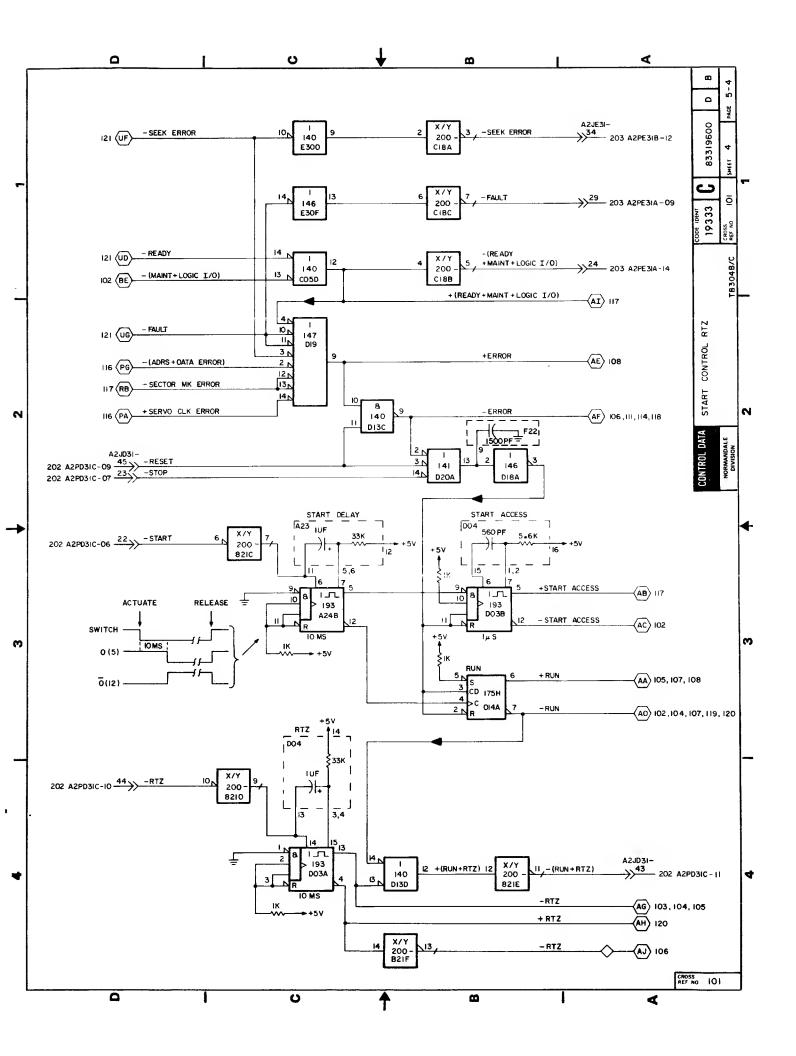


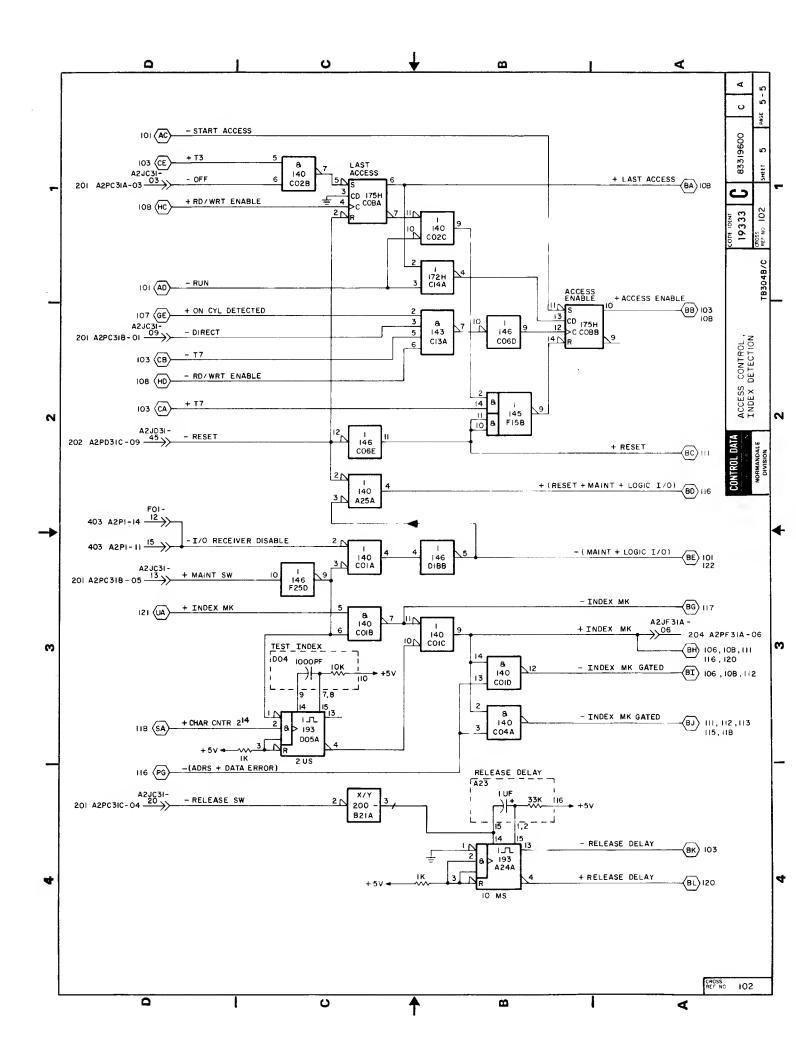
TB304B/C

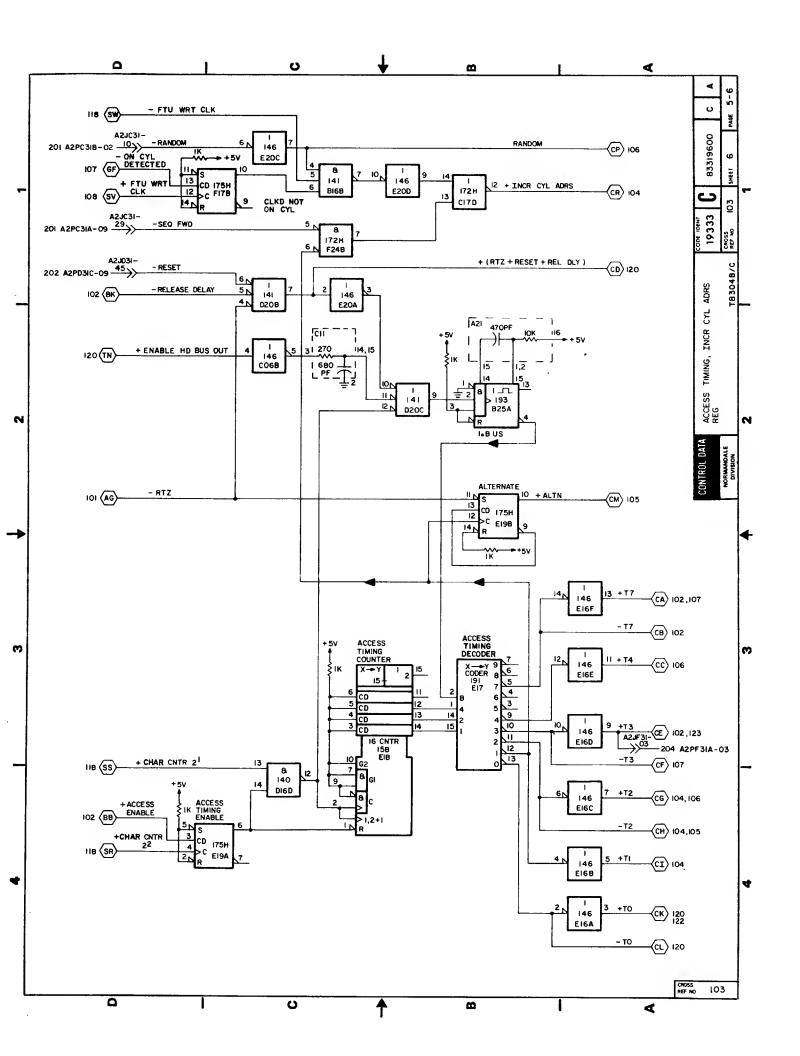
DIAGRAMS

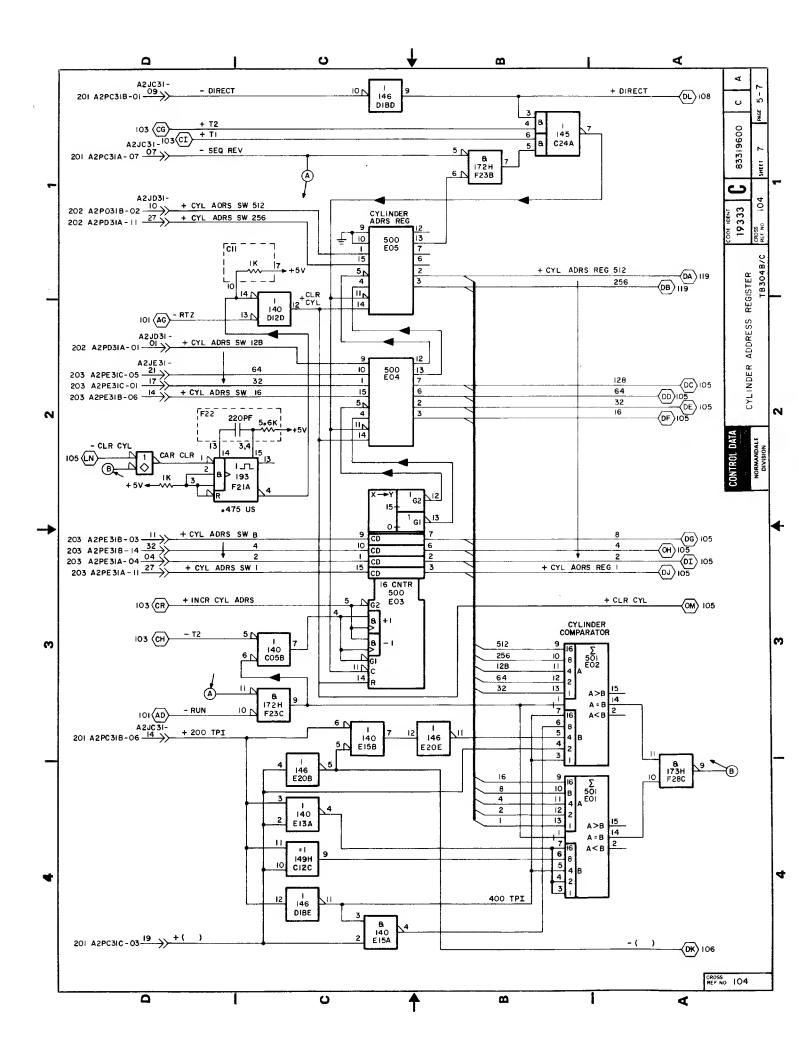


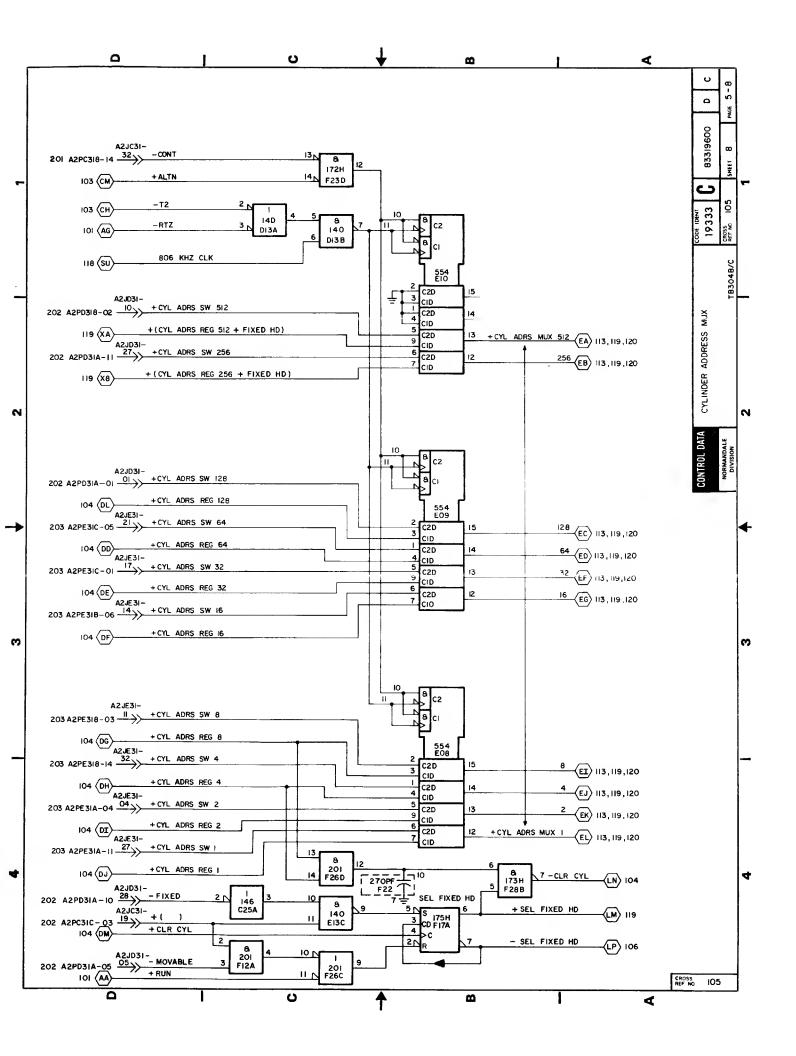


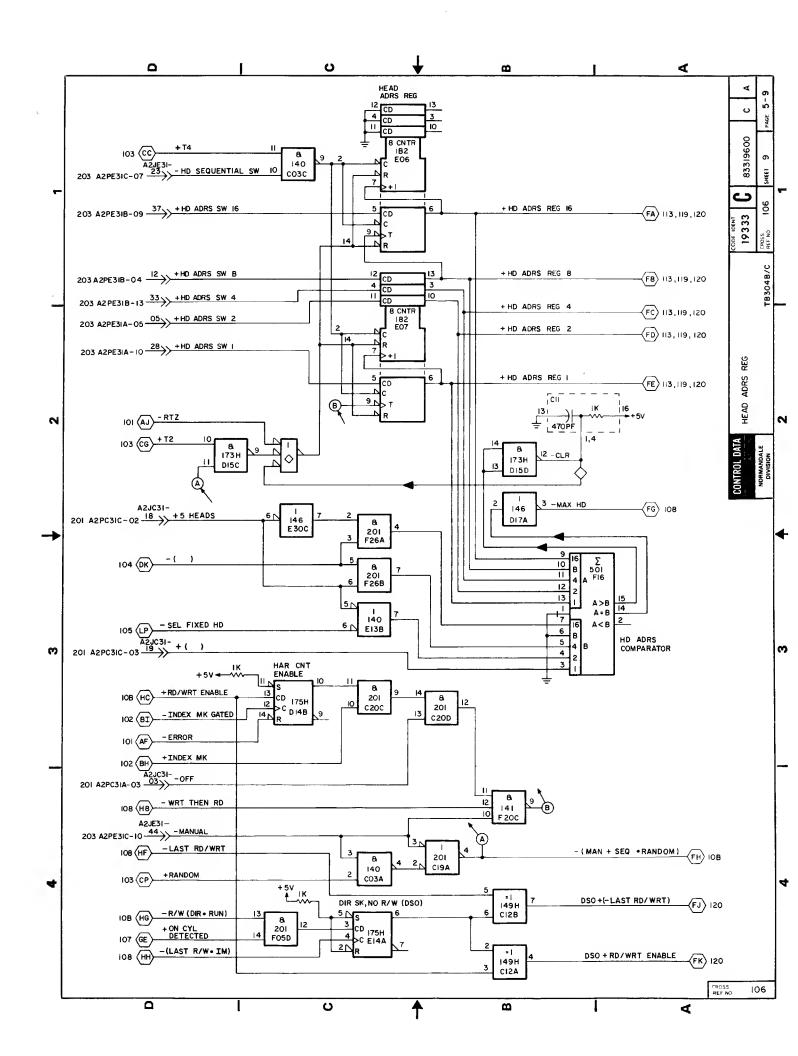


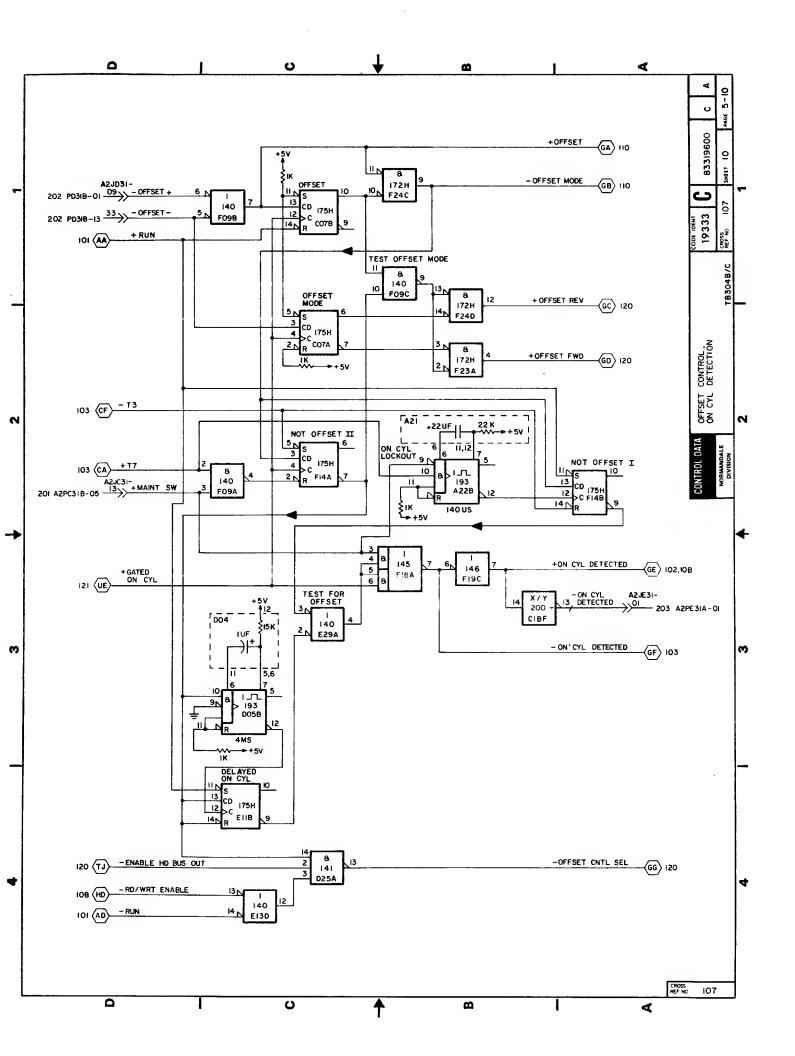


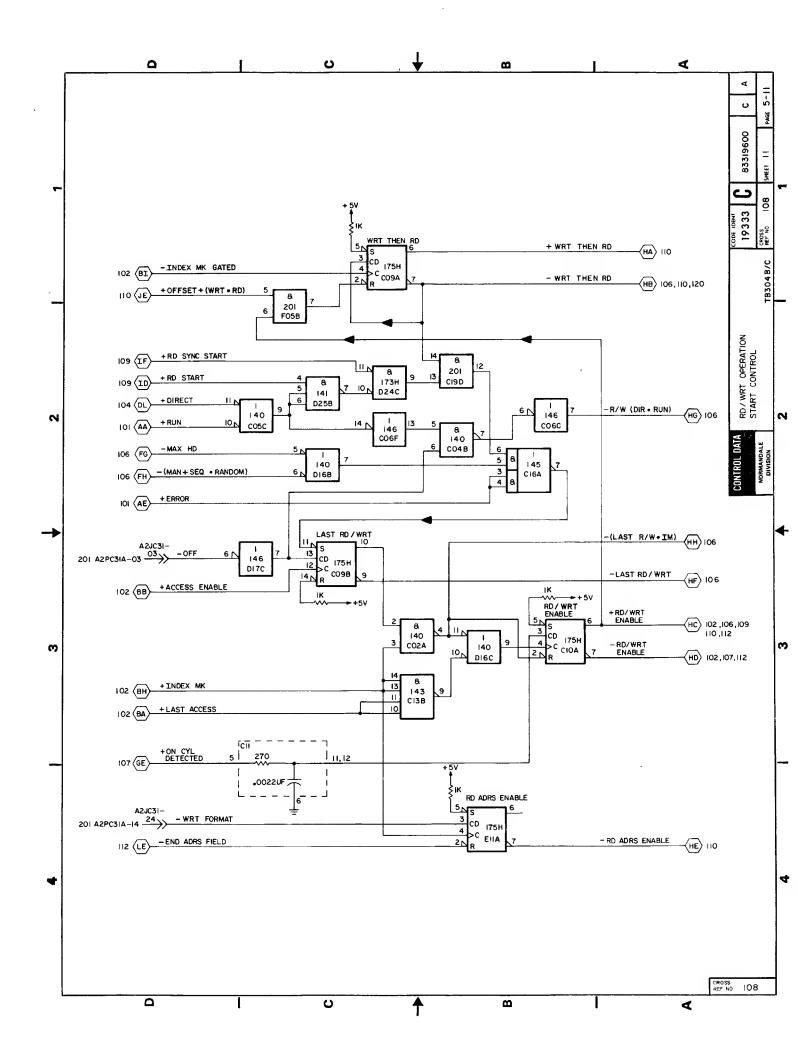


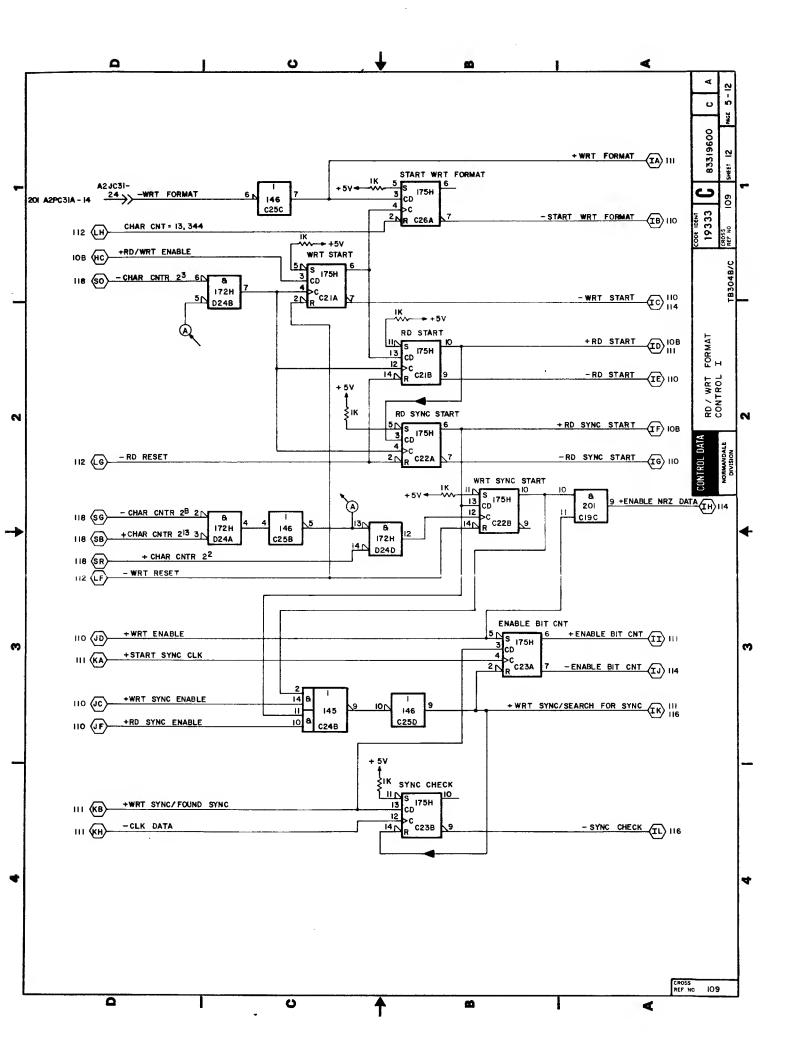


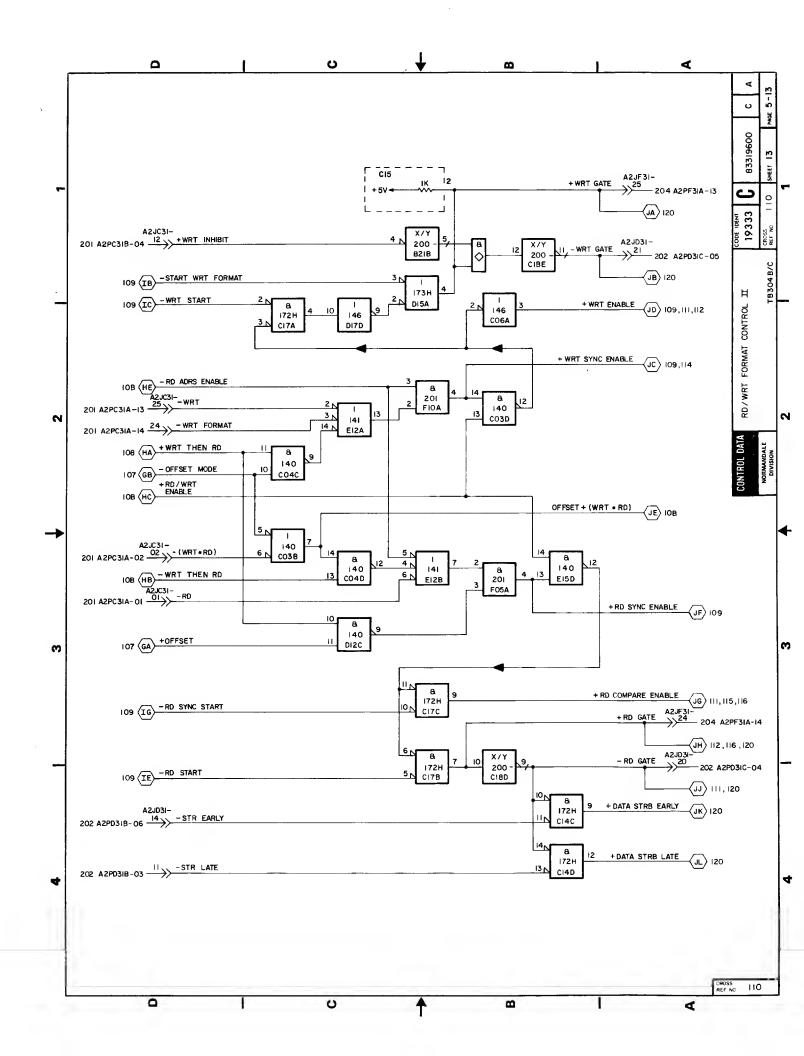


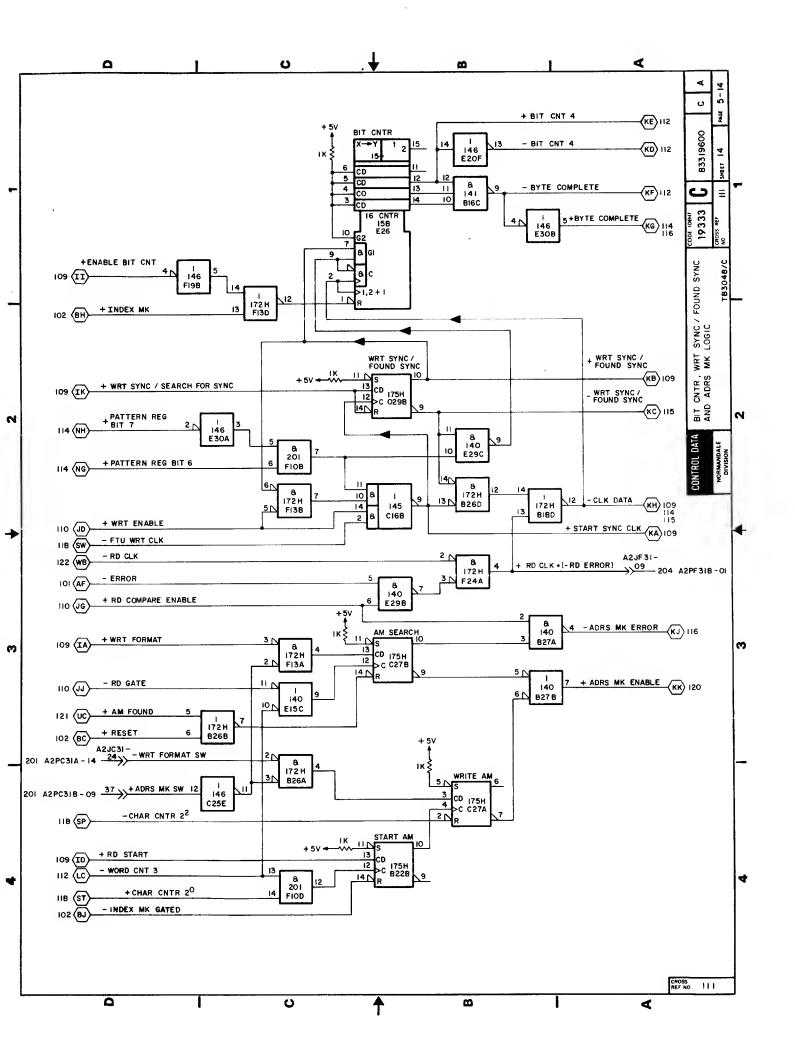


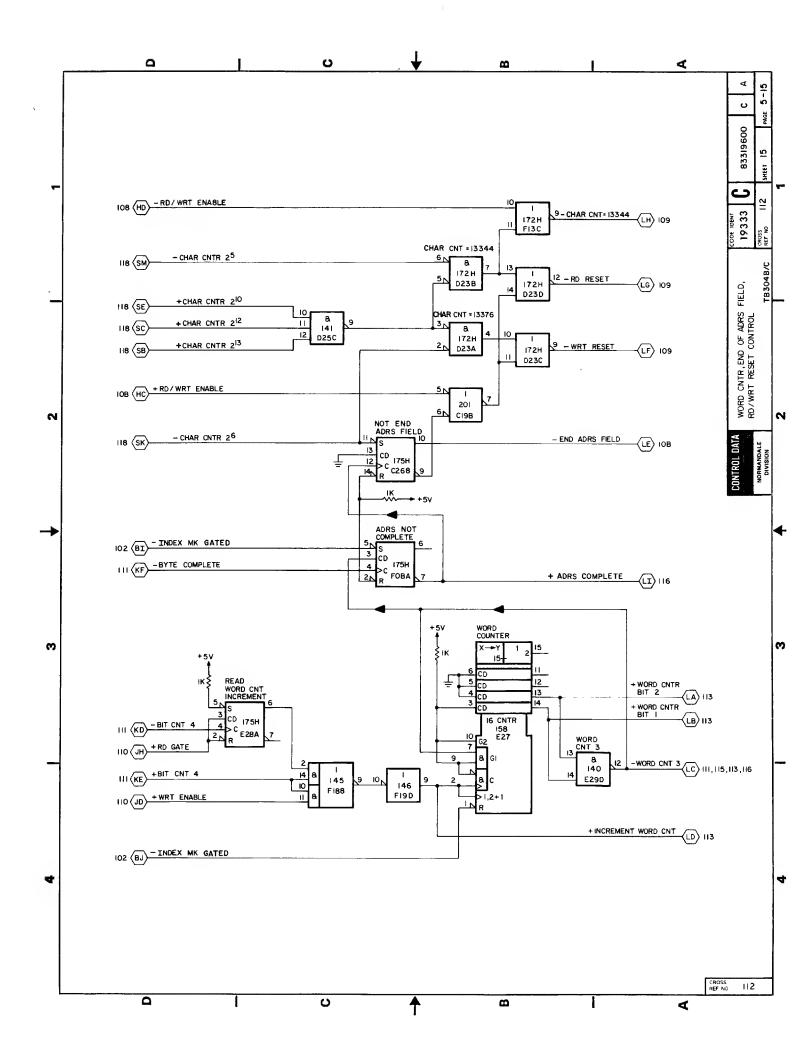


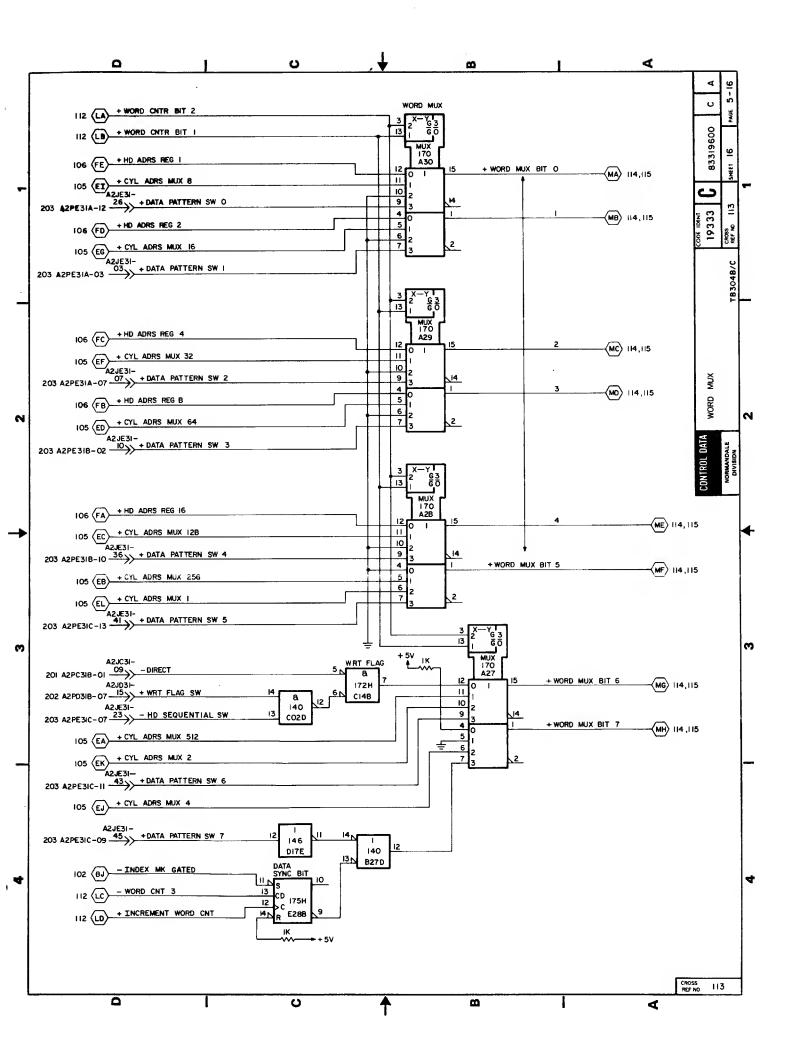


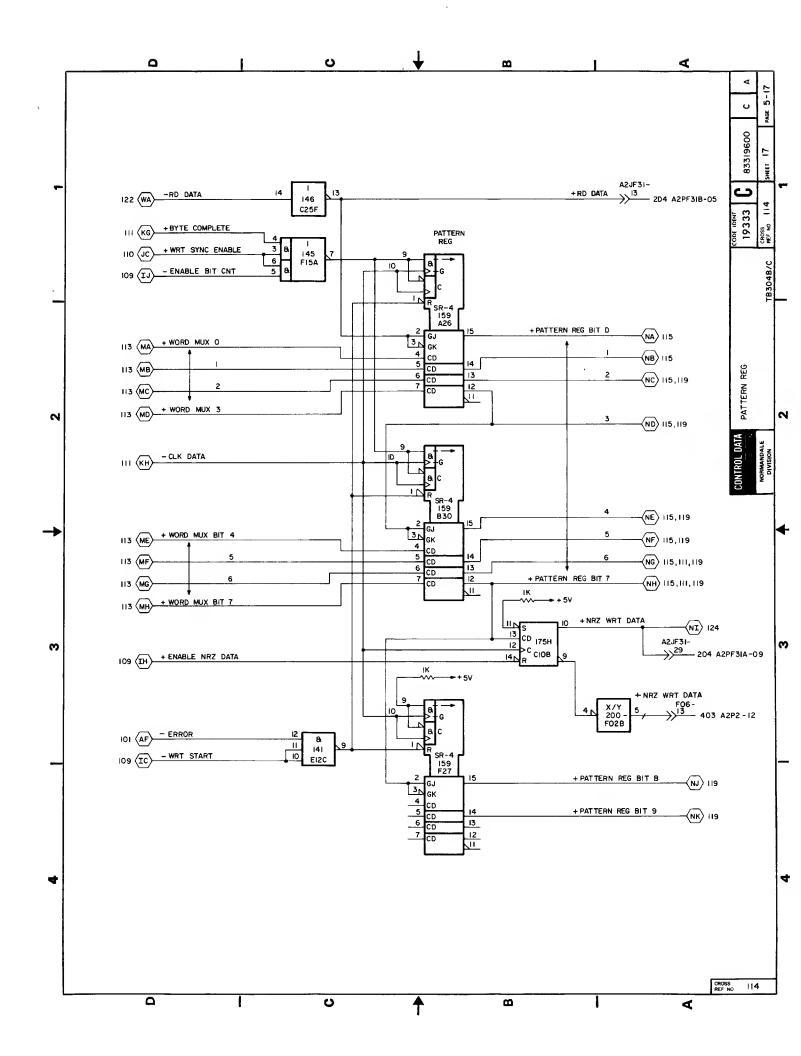


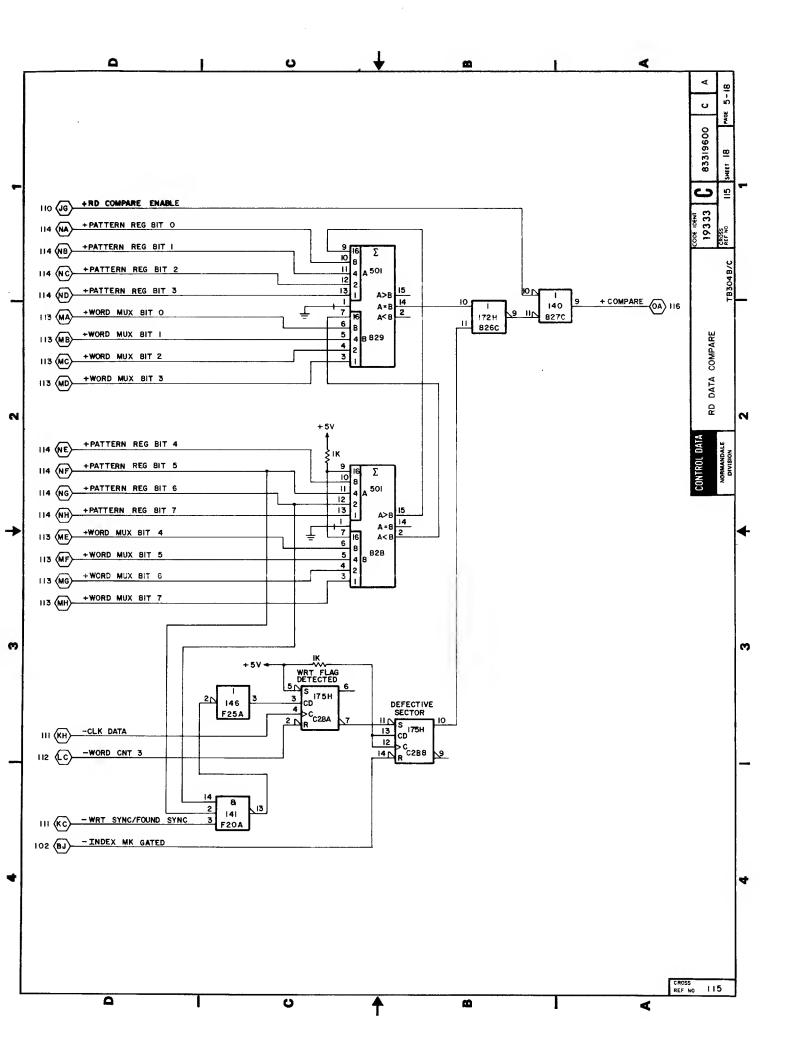


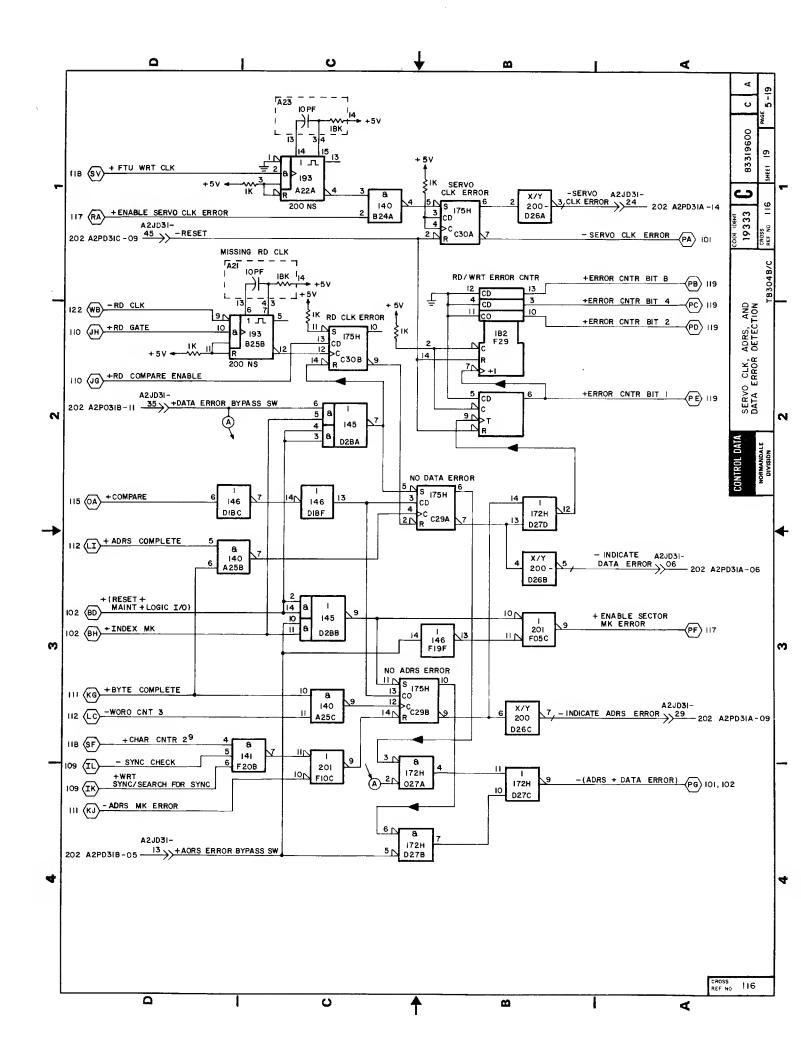


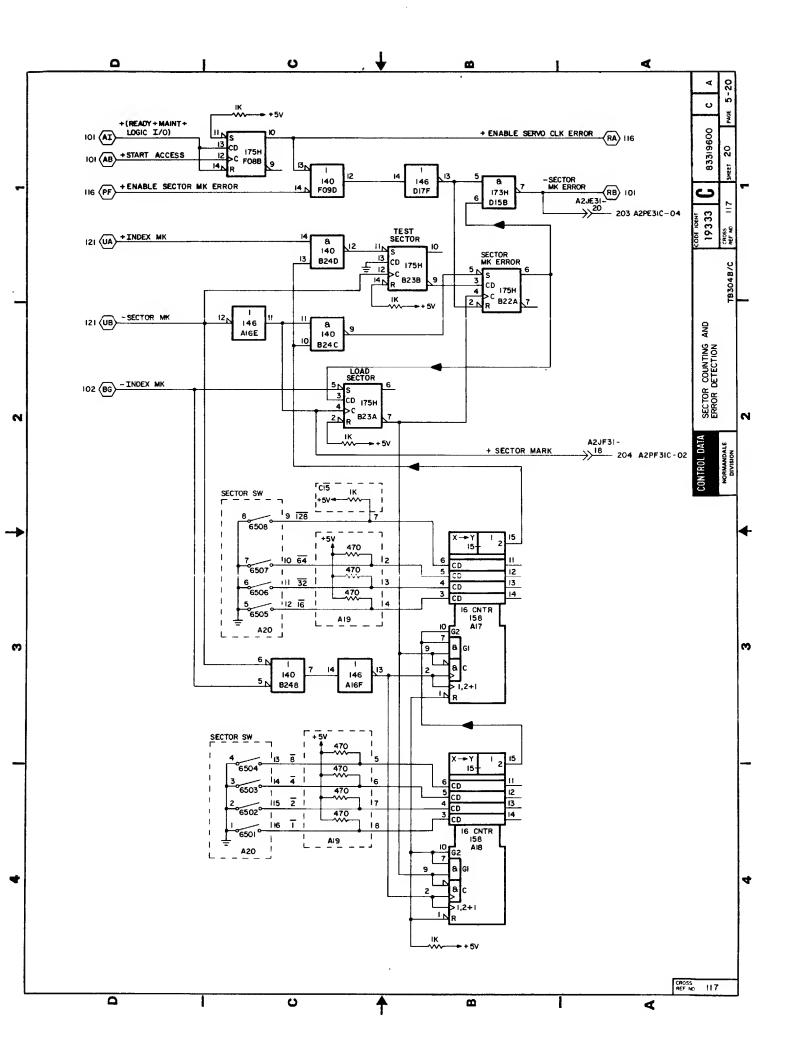


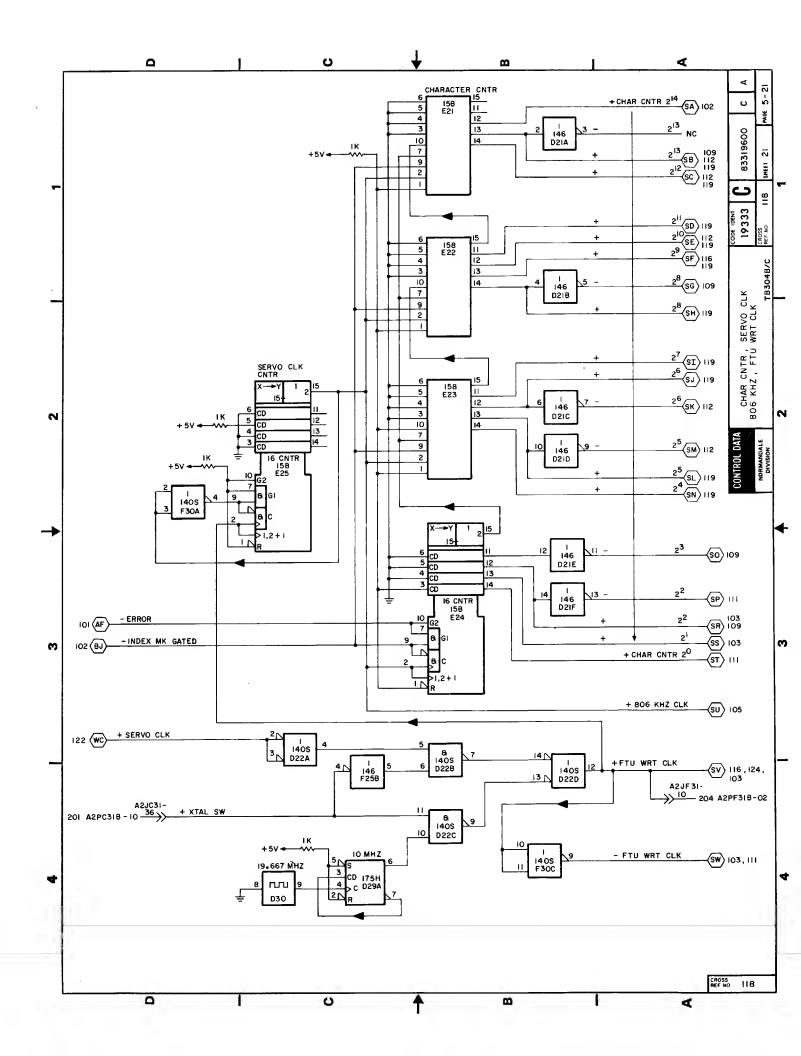


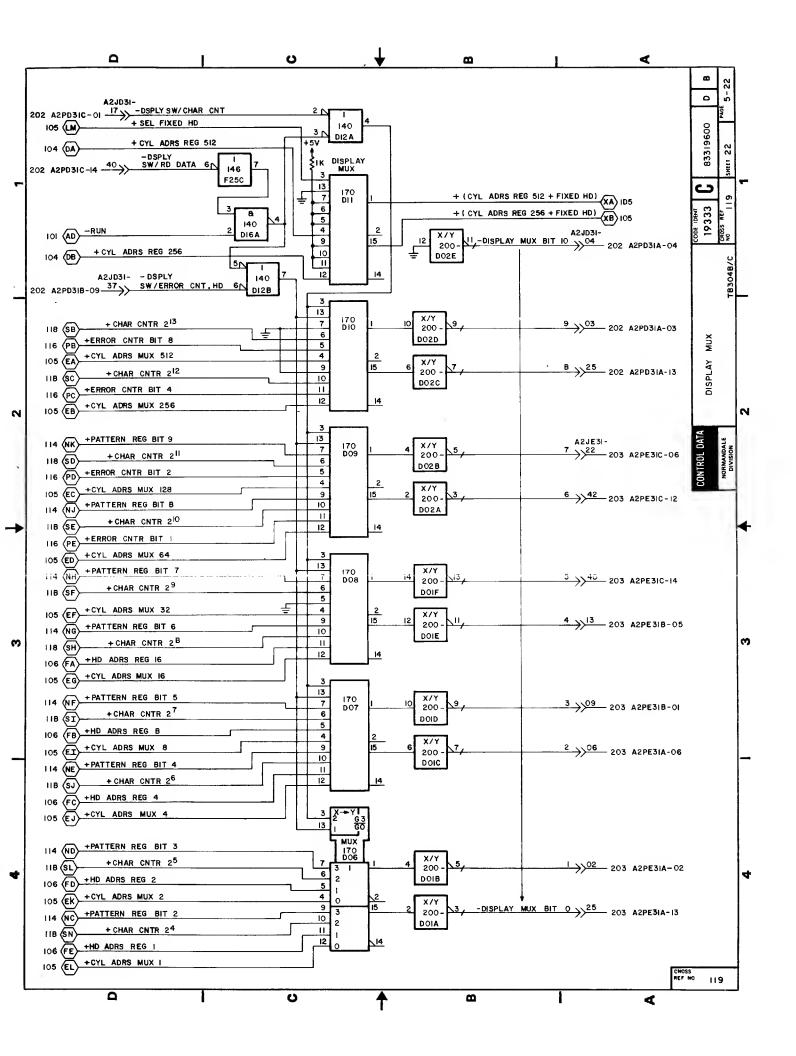


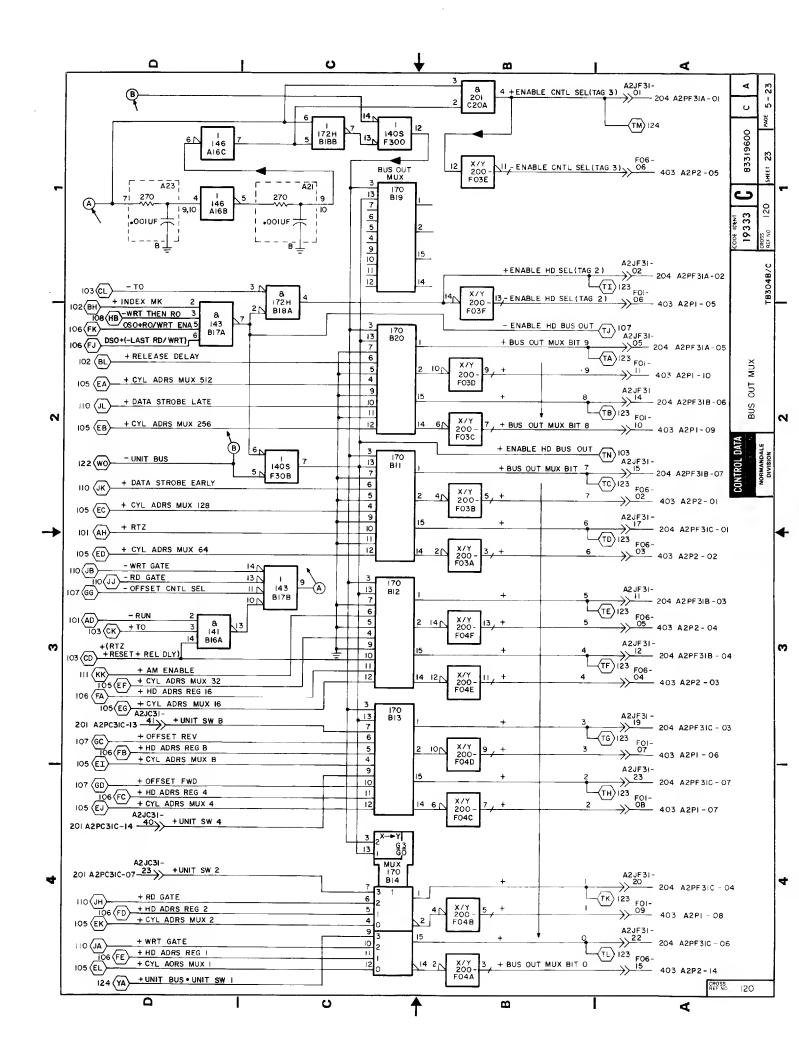


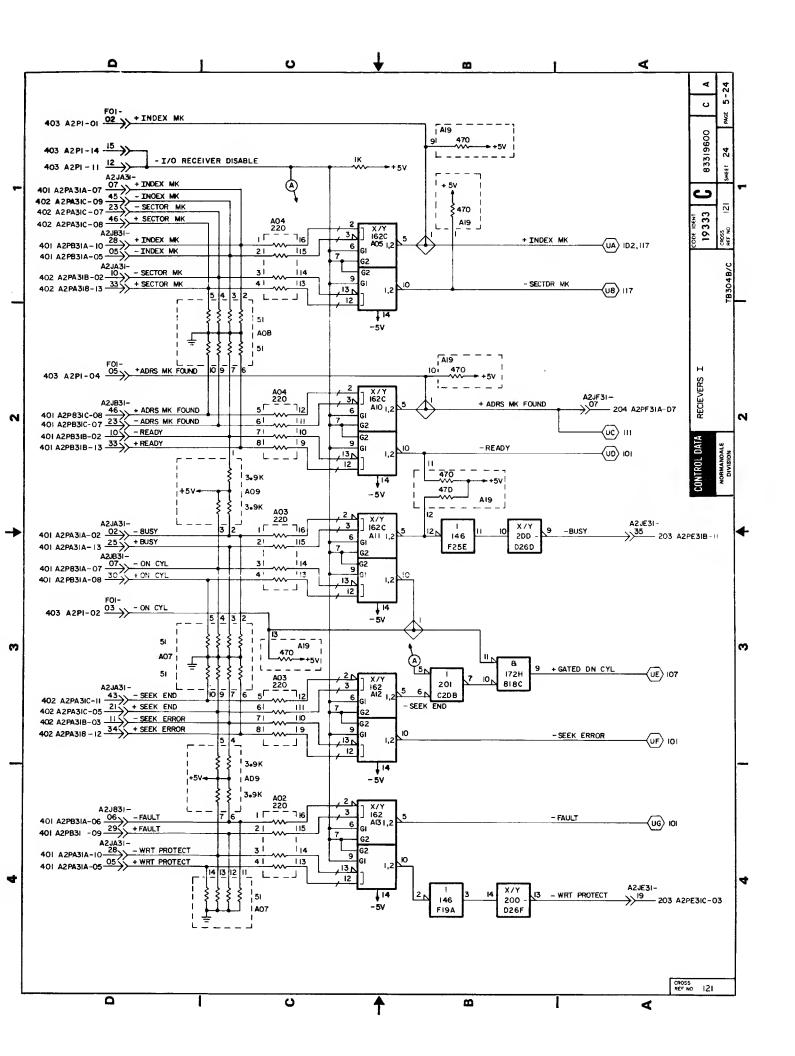


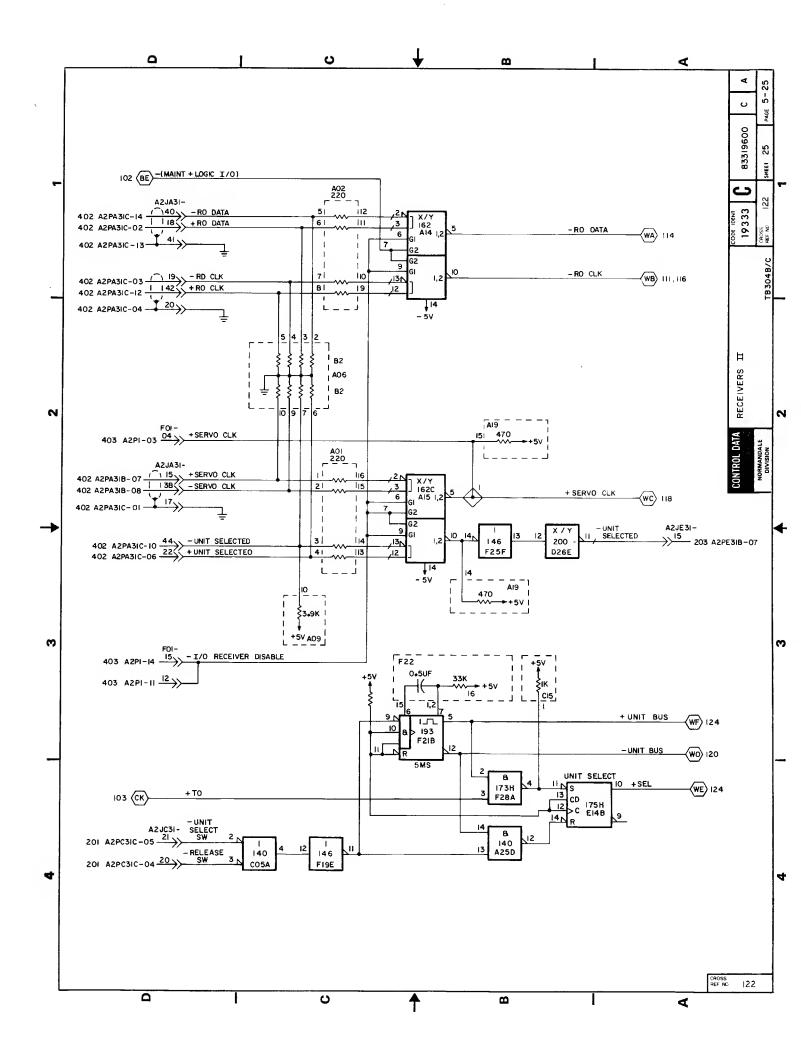


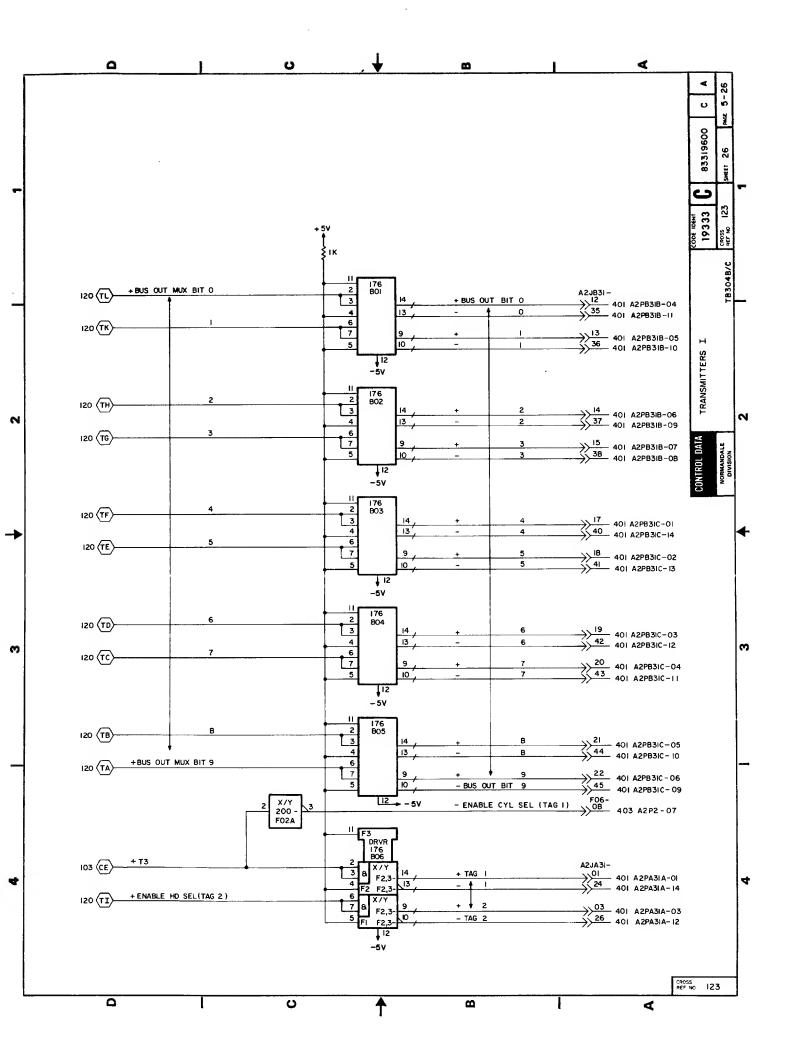


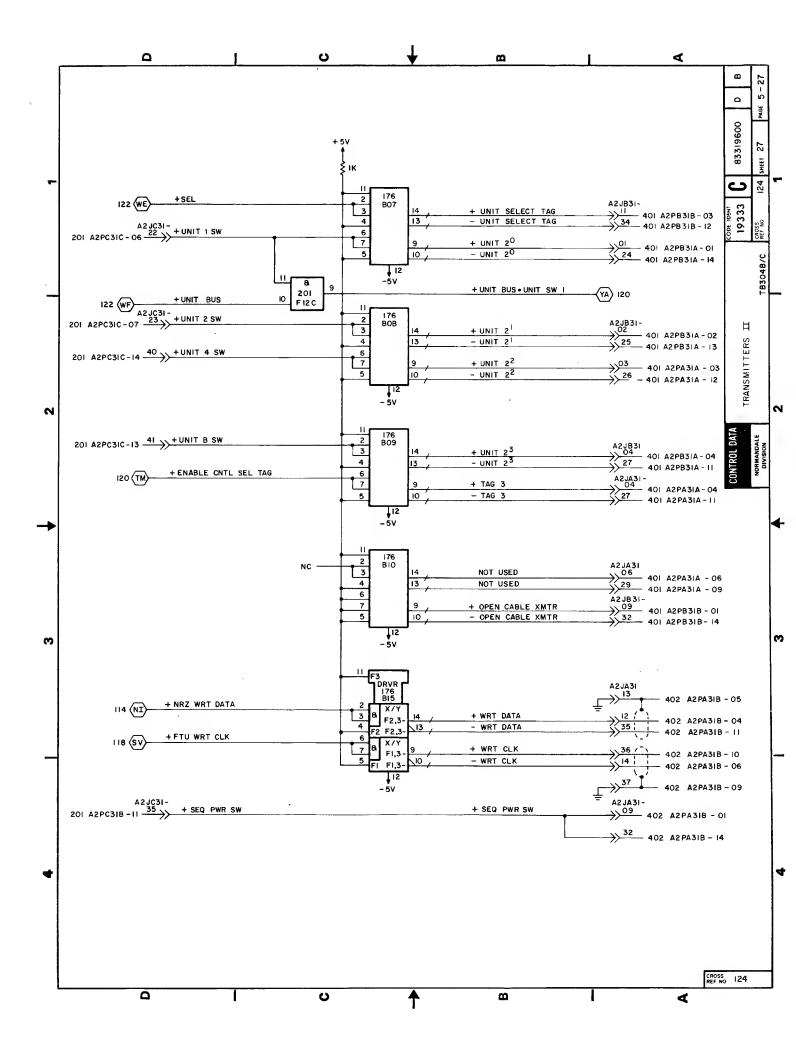


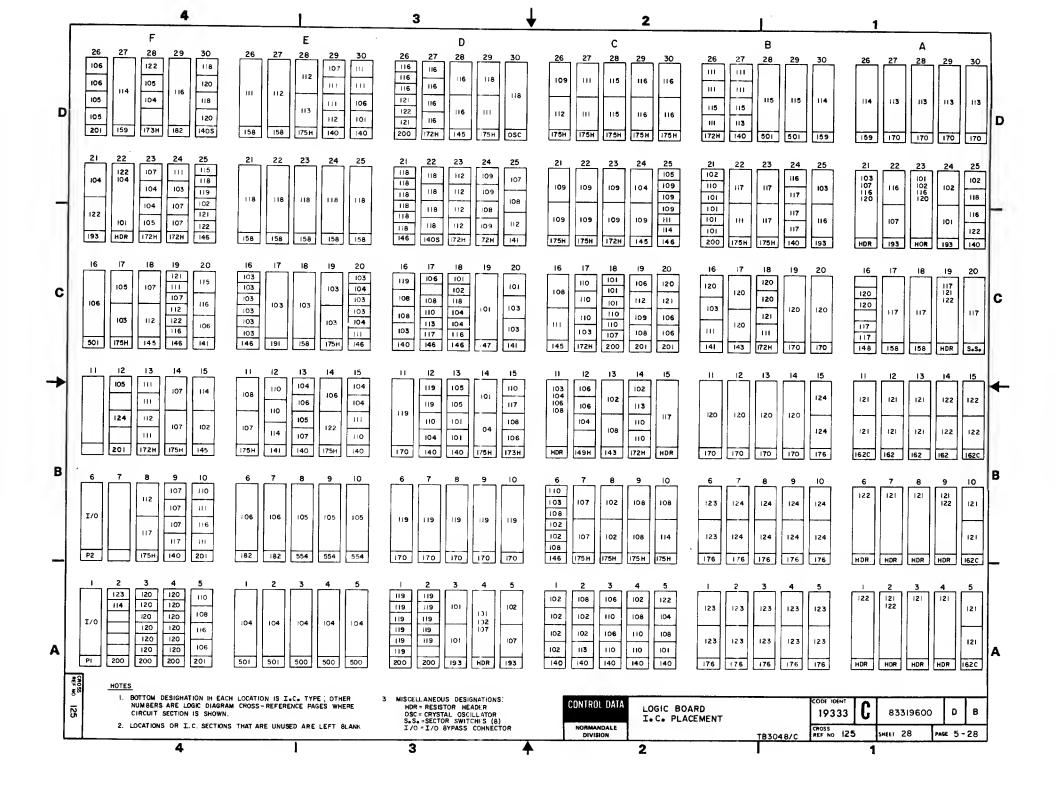


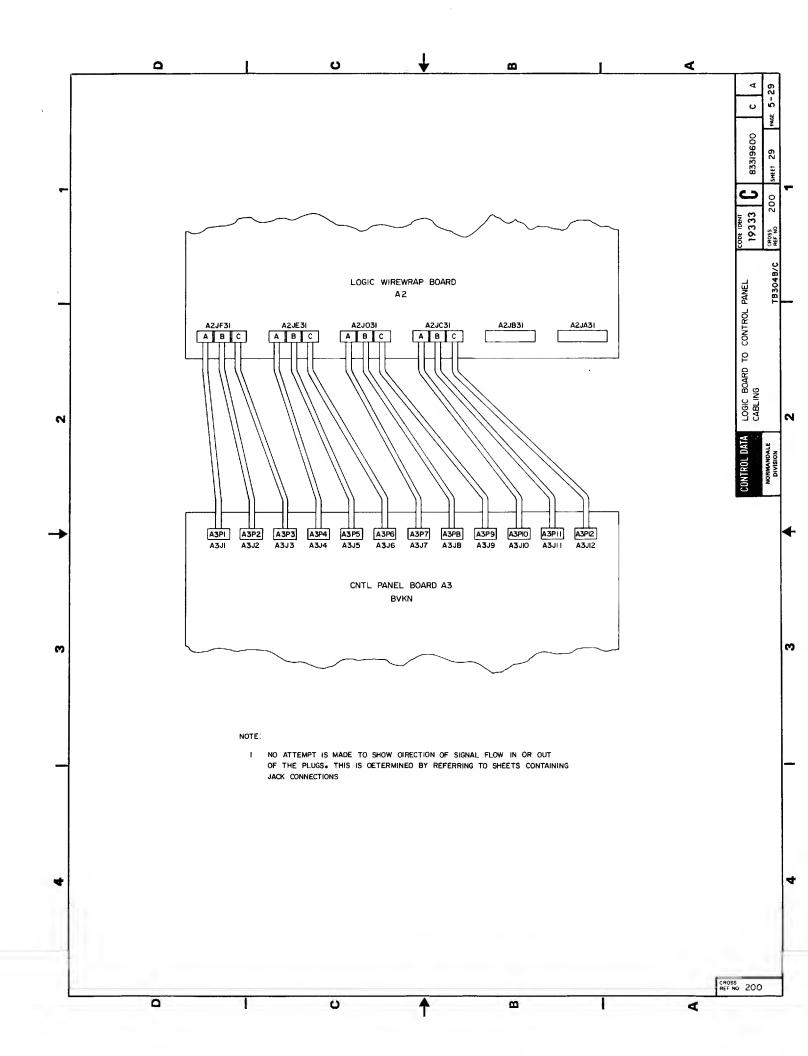


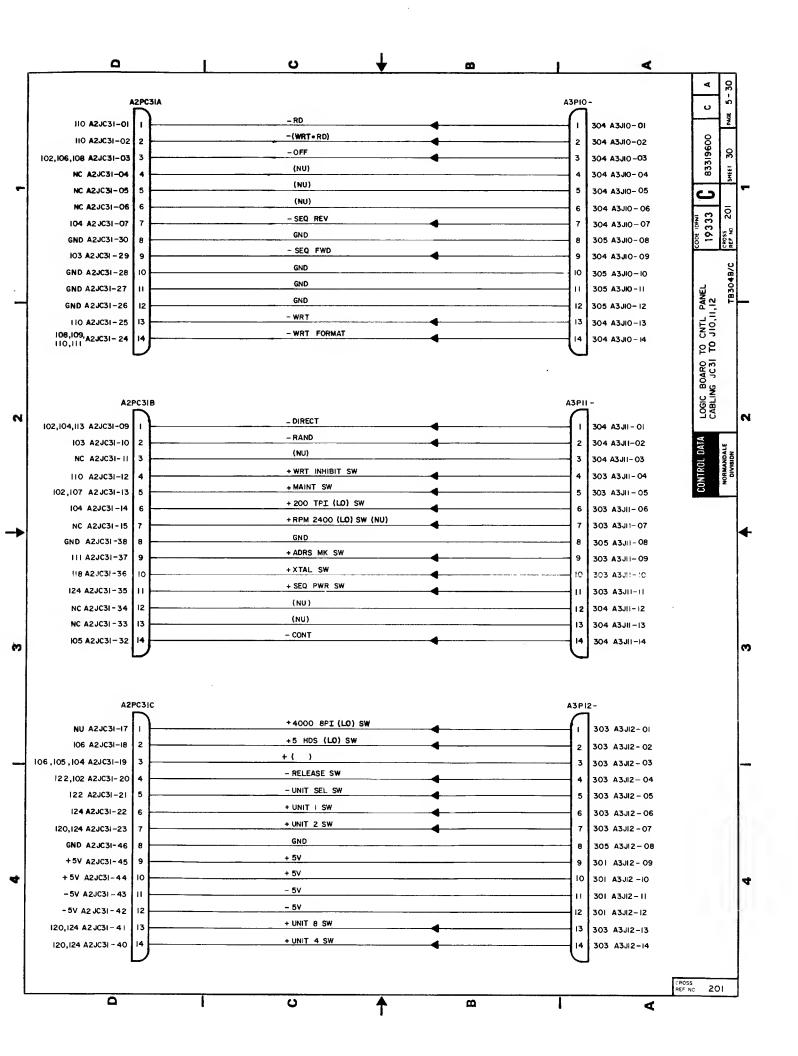


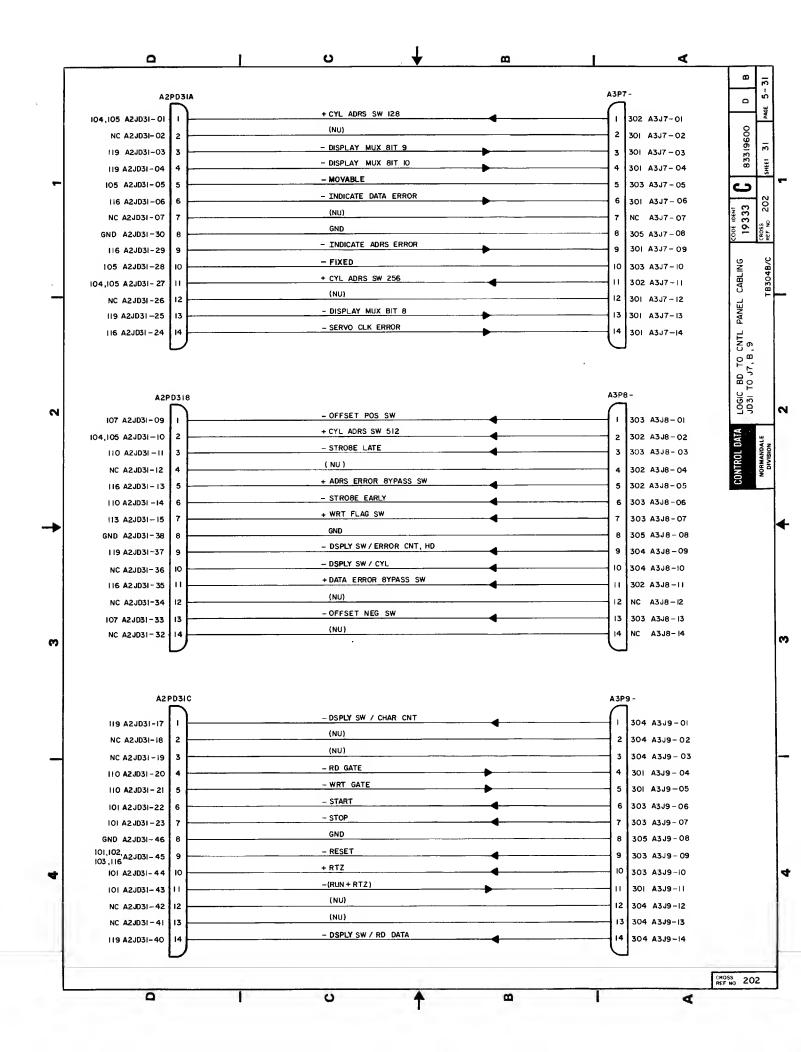


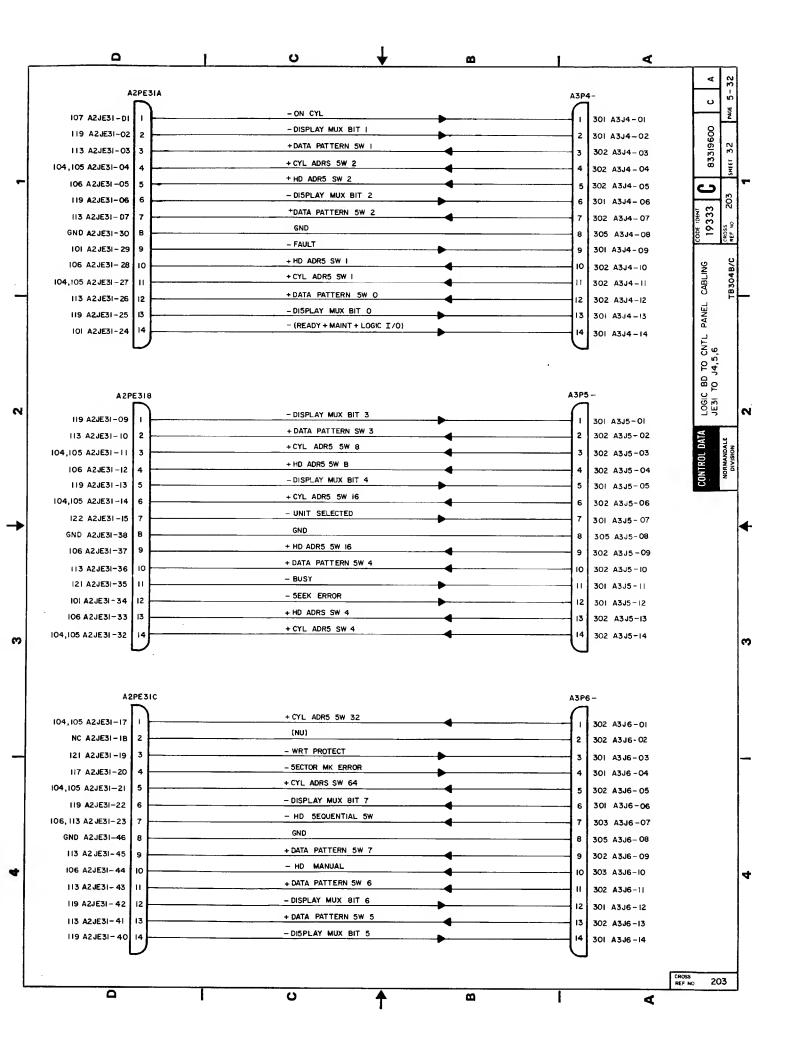


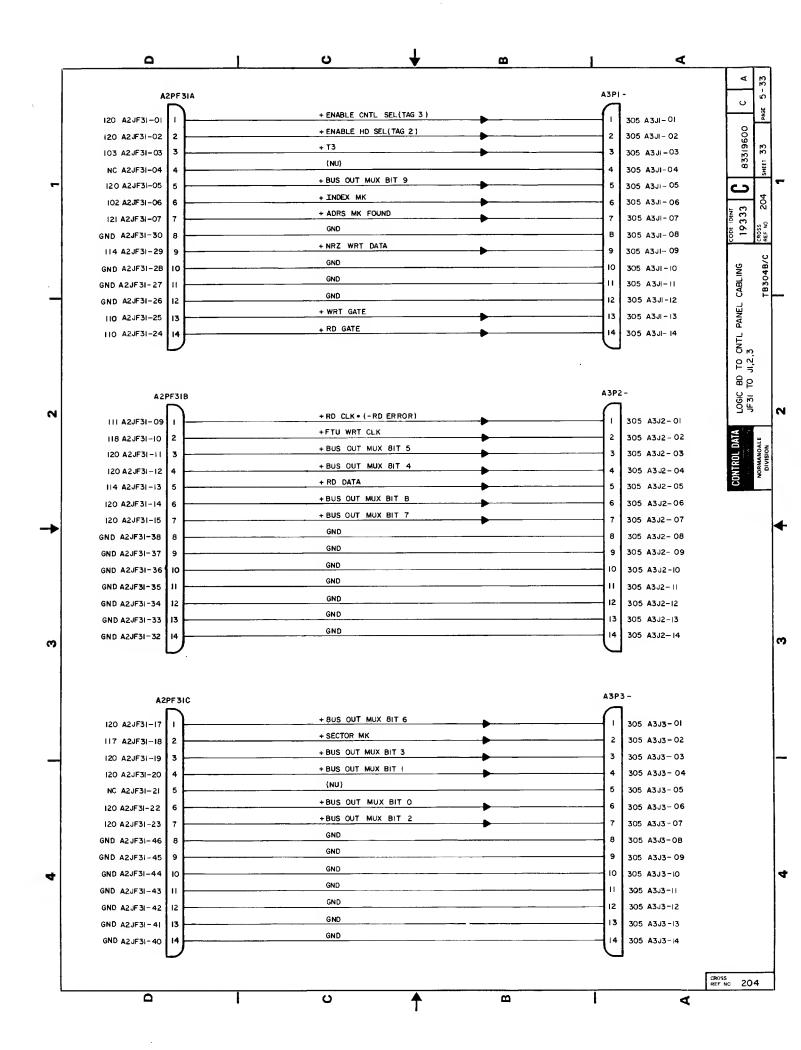


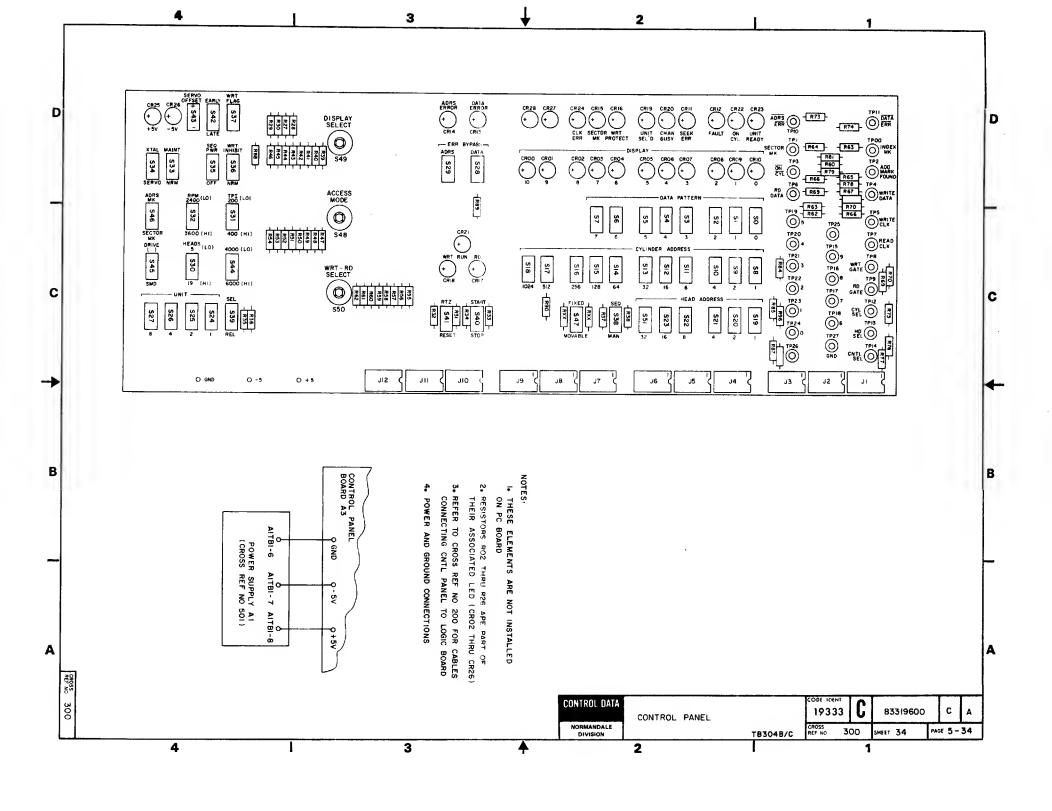


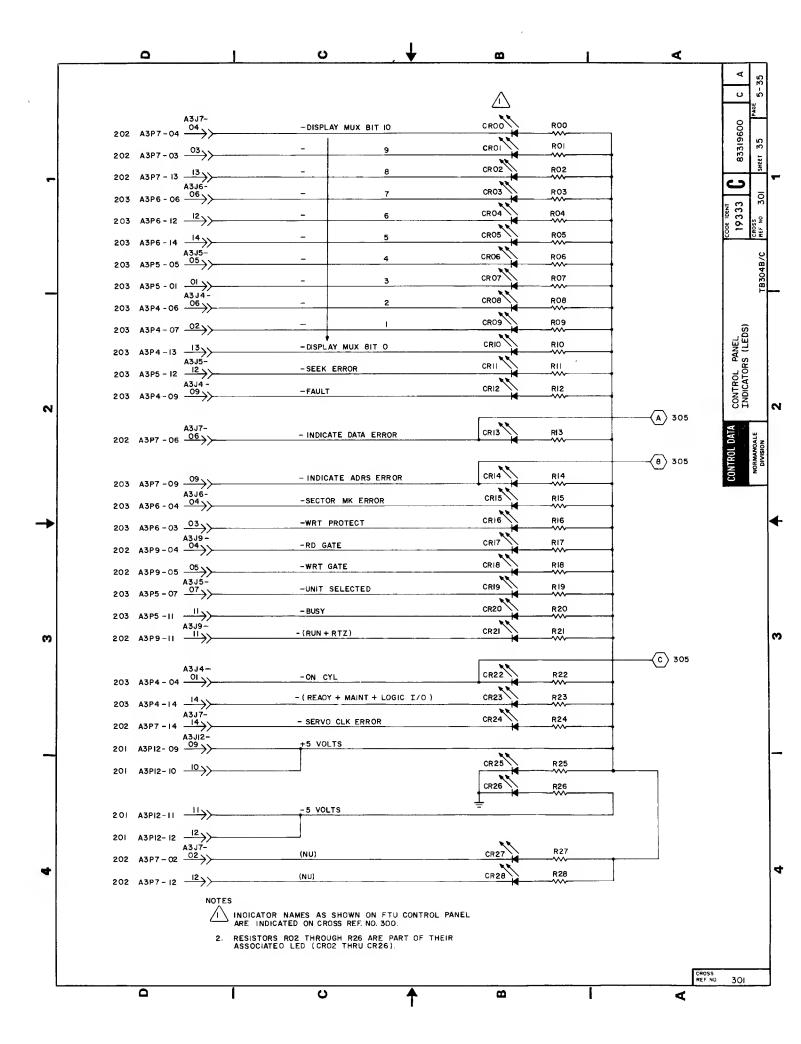


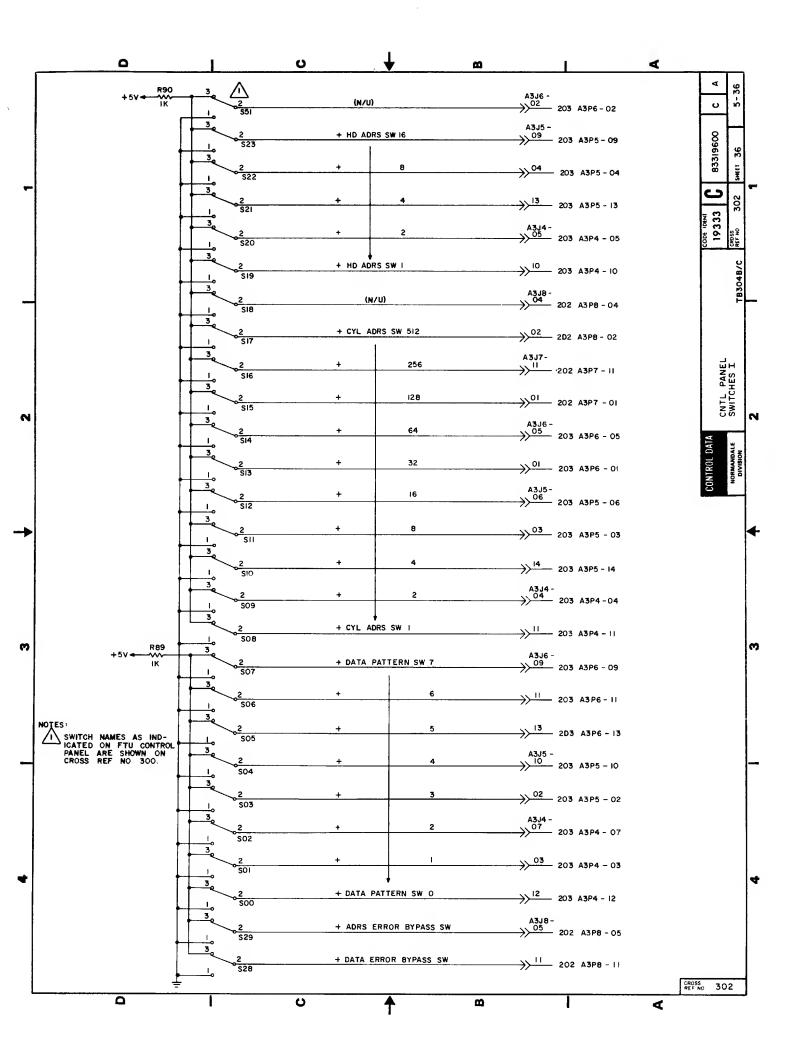


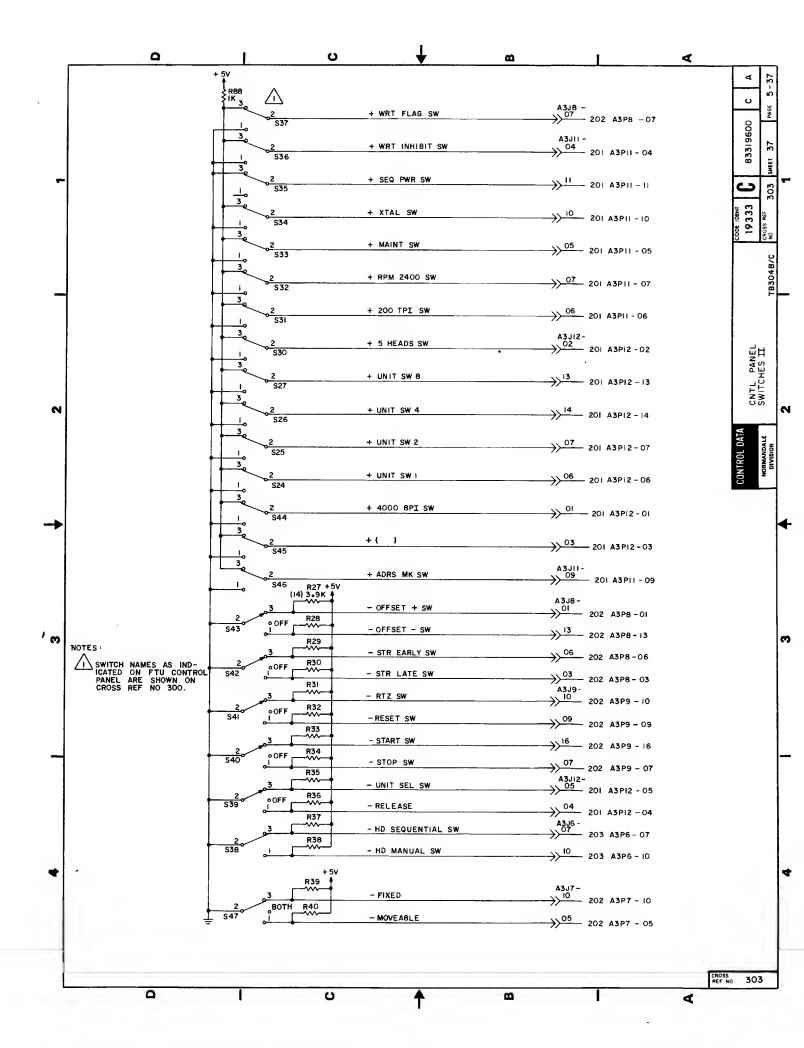


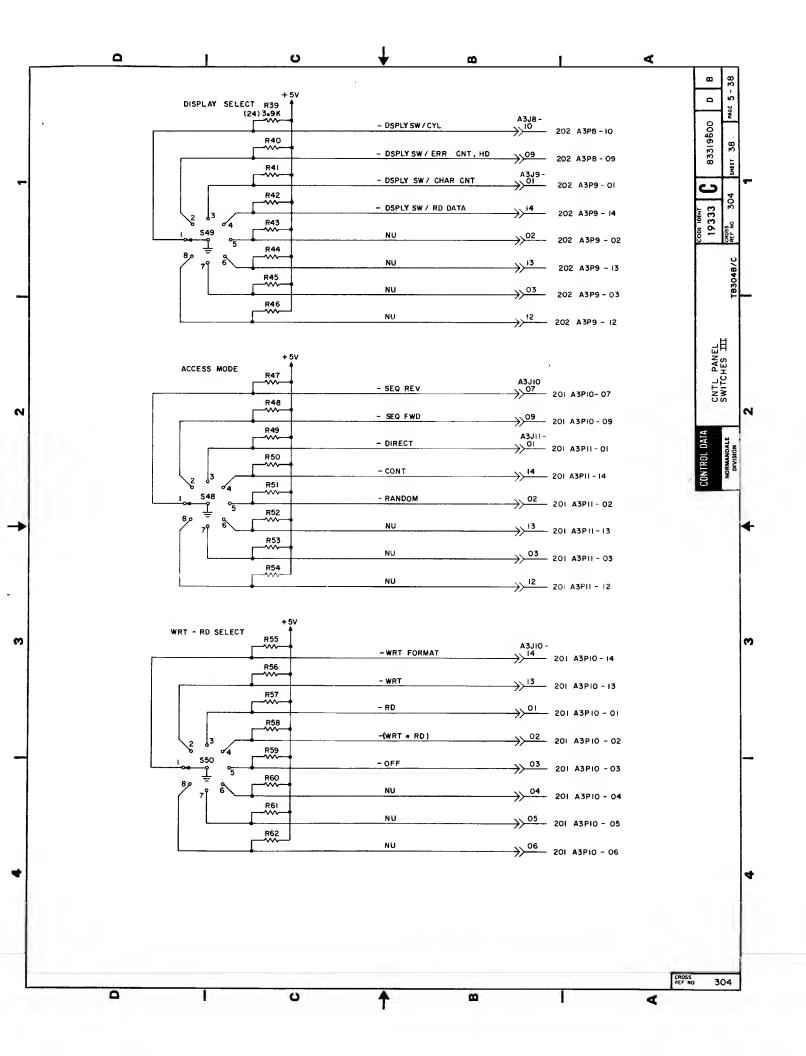


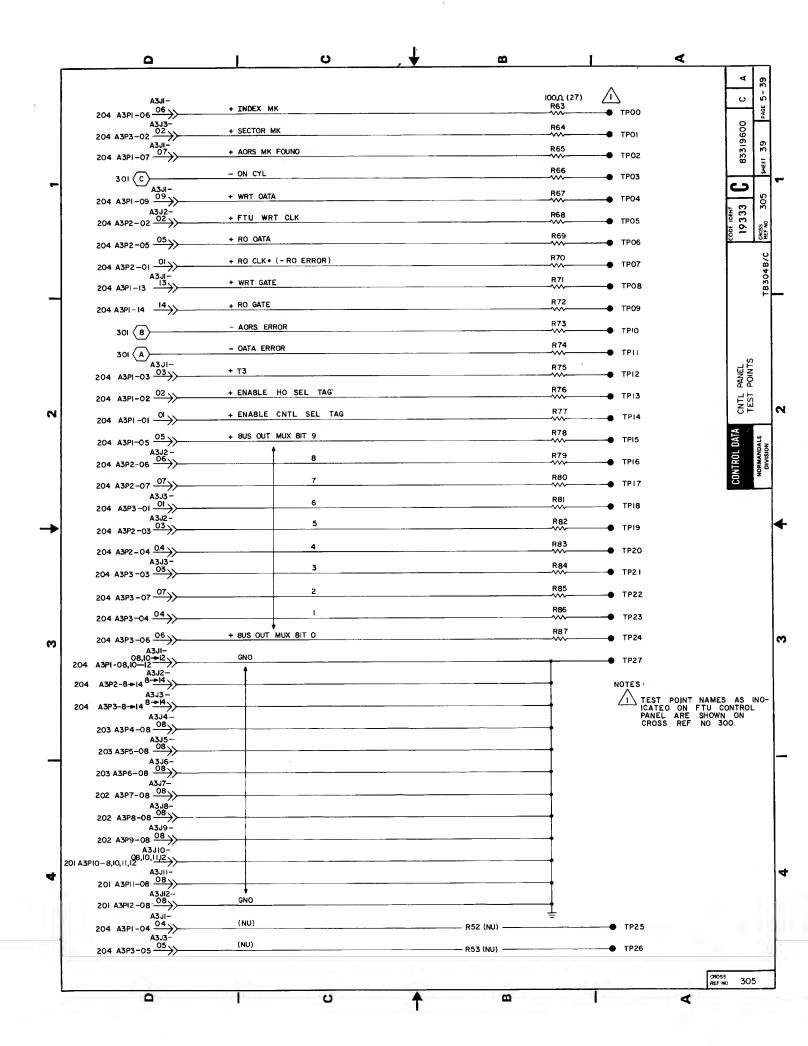


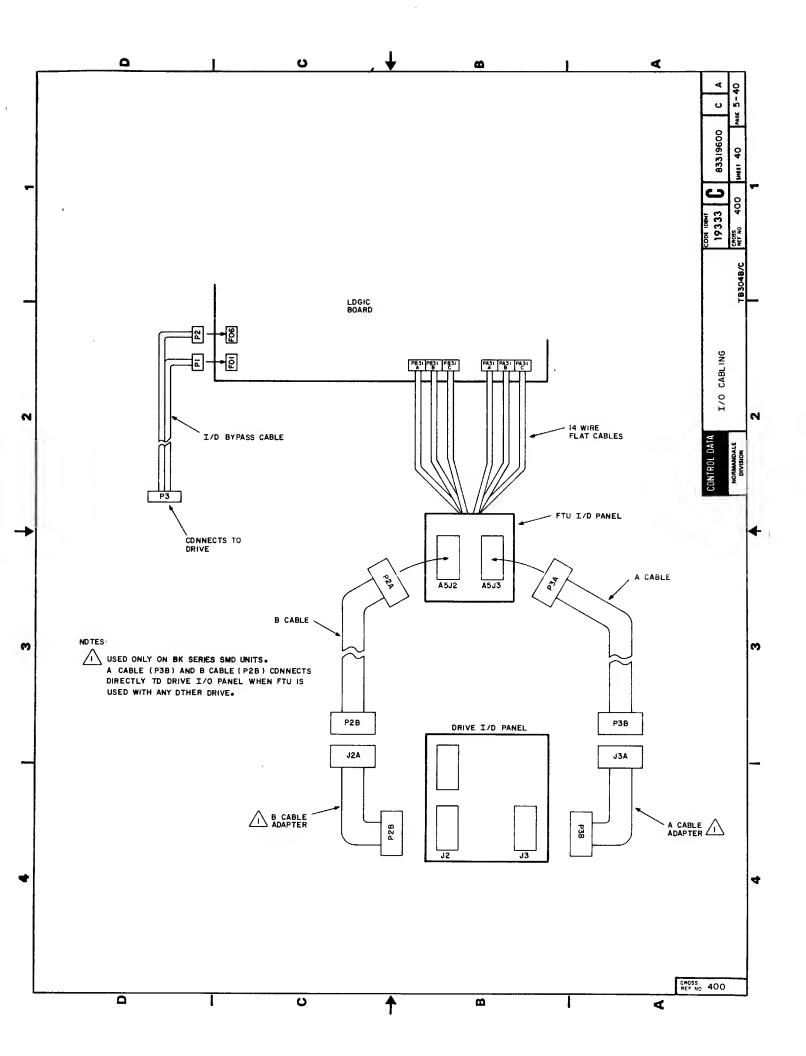


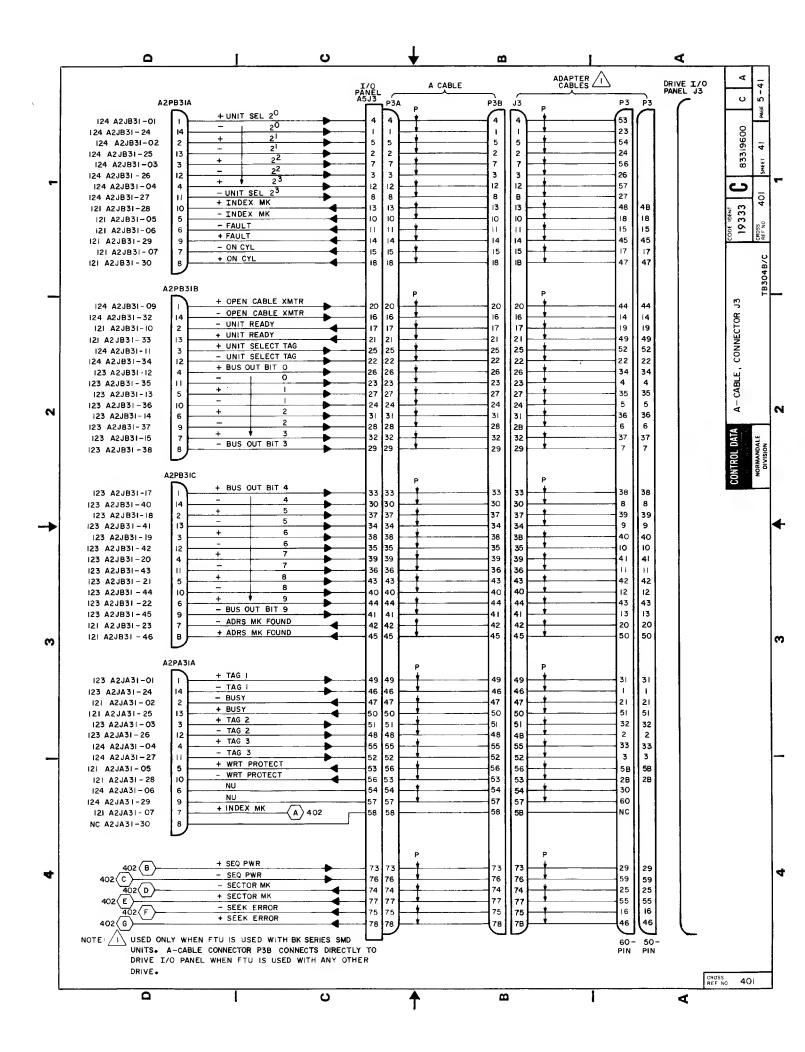


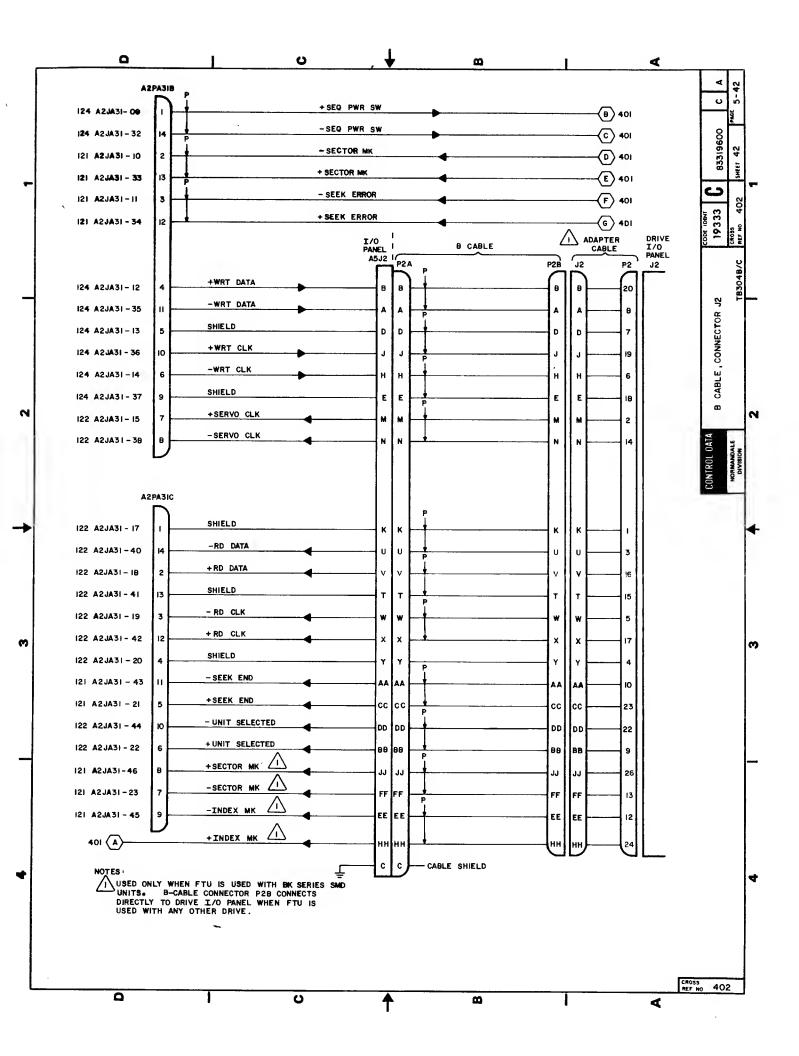


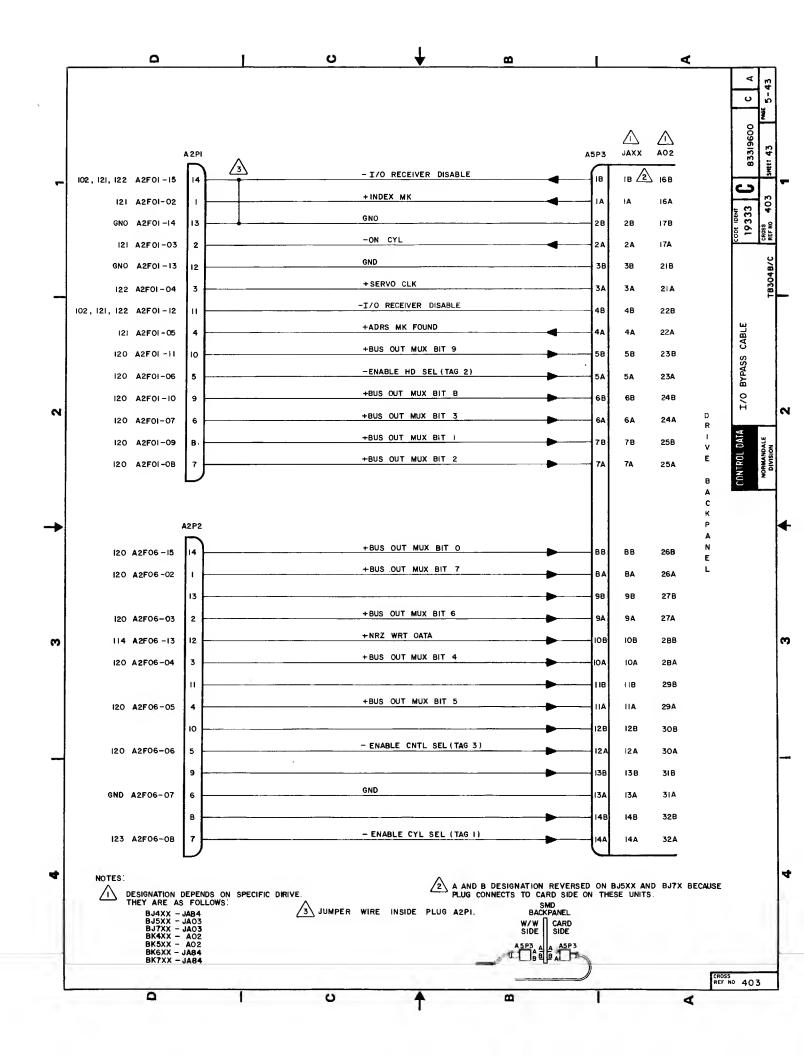


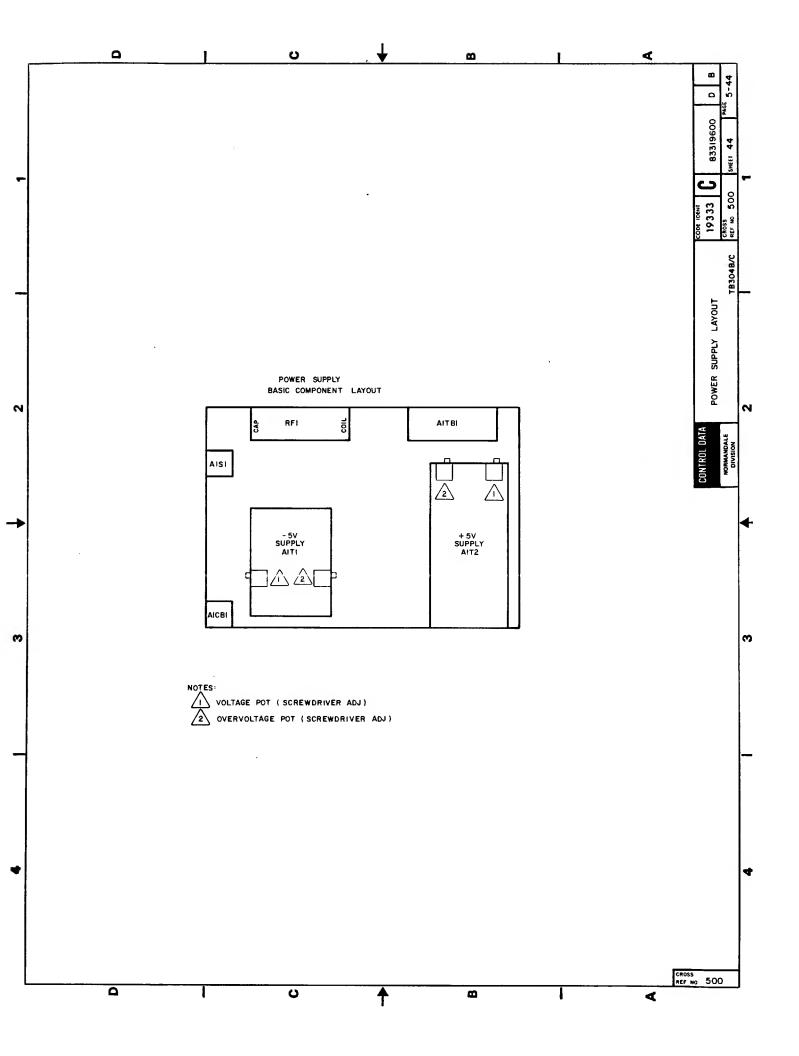


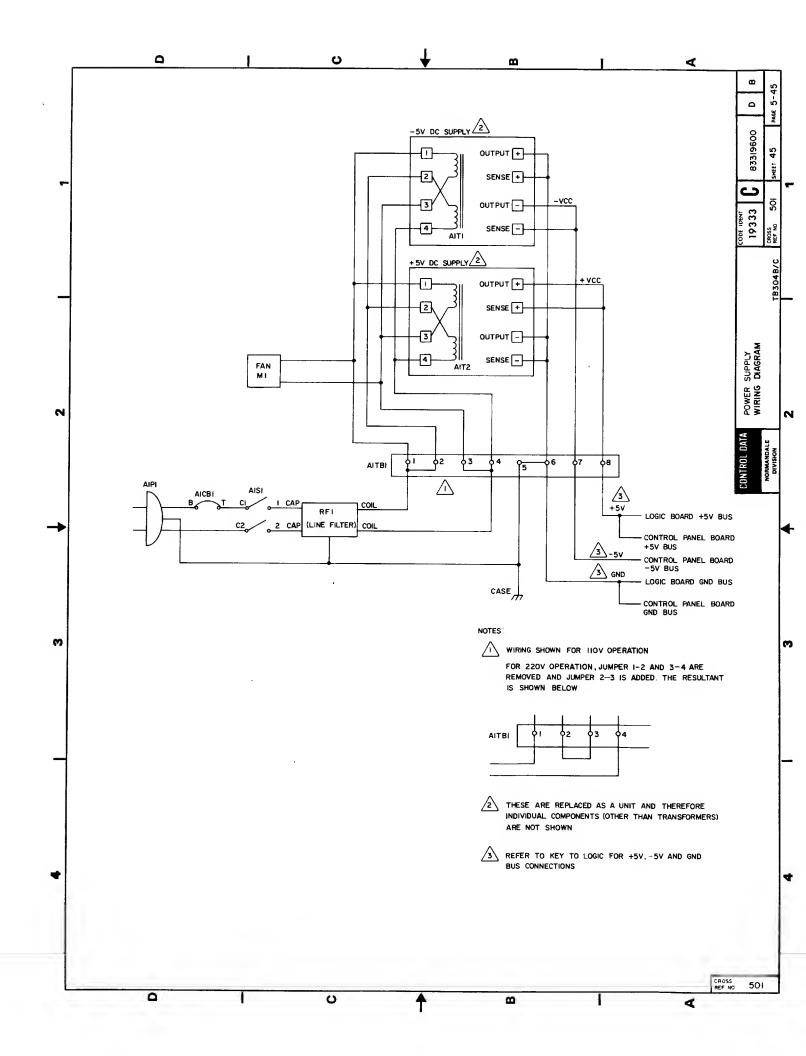


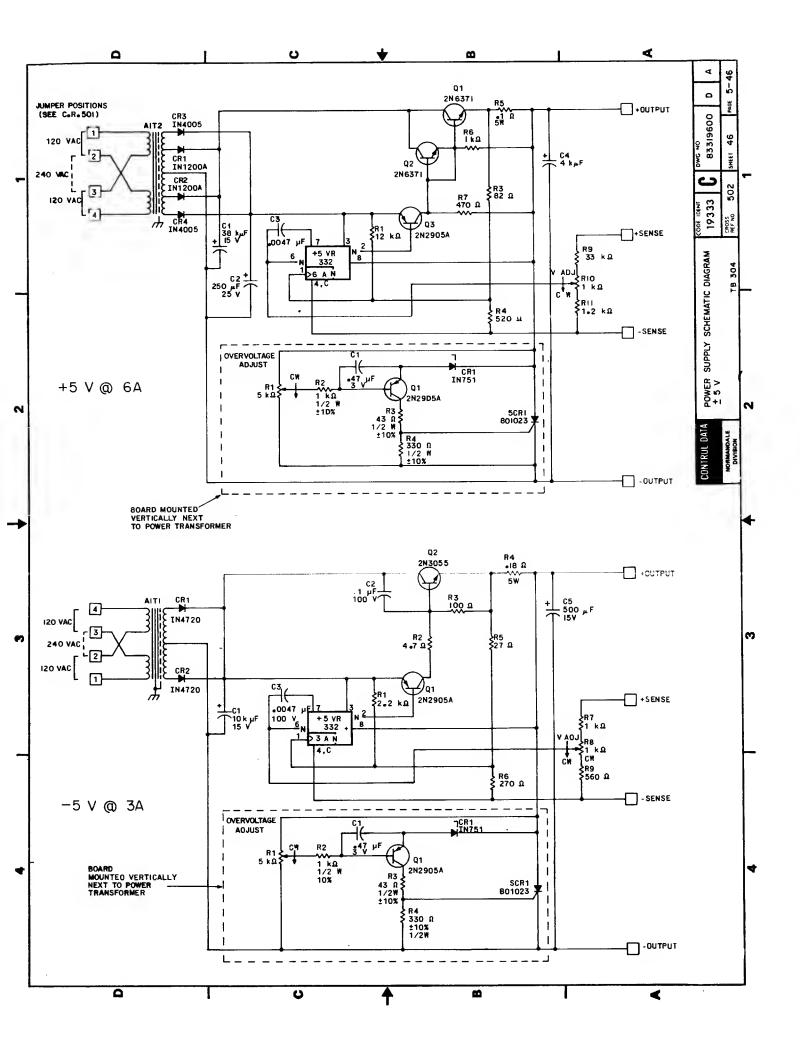


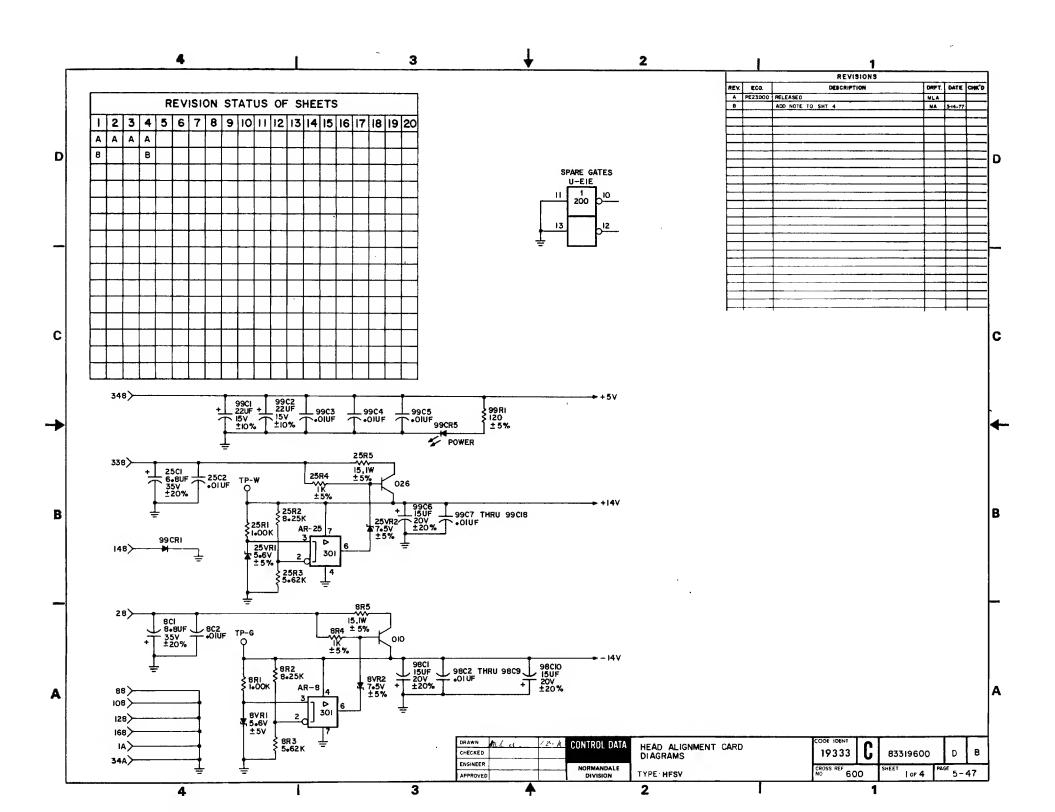


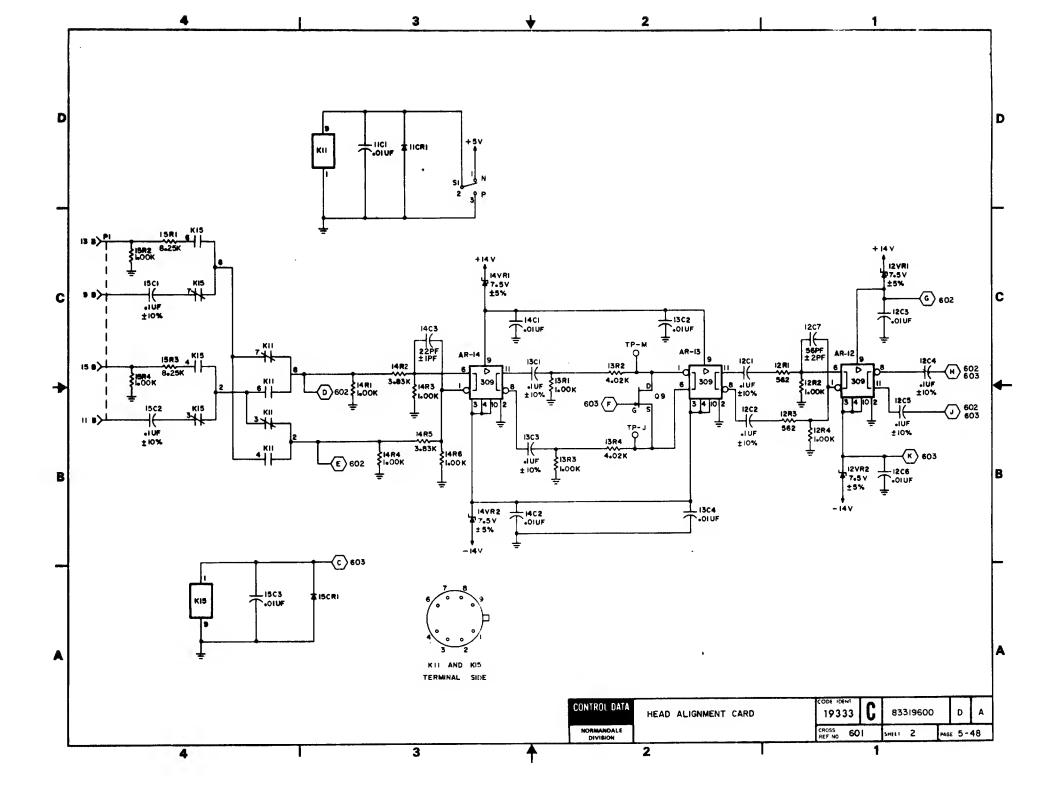


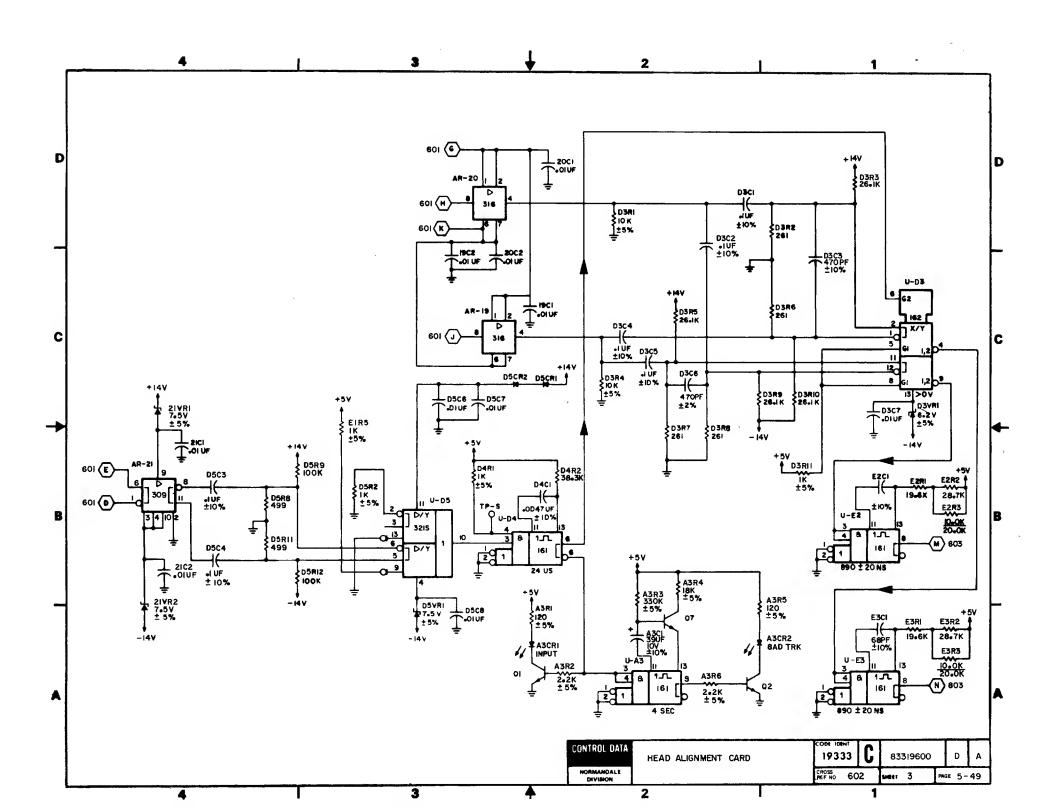






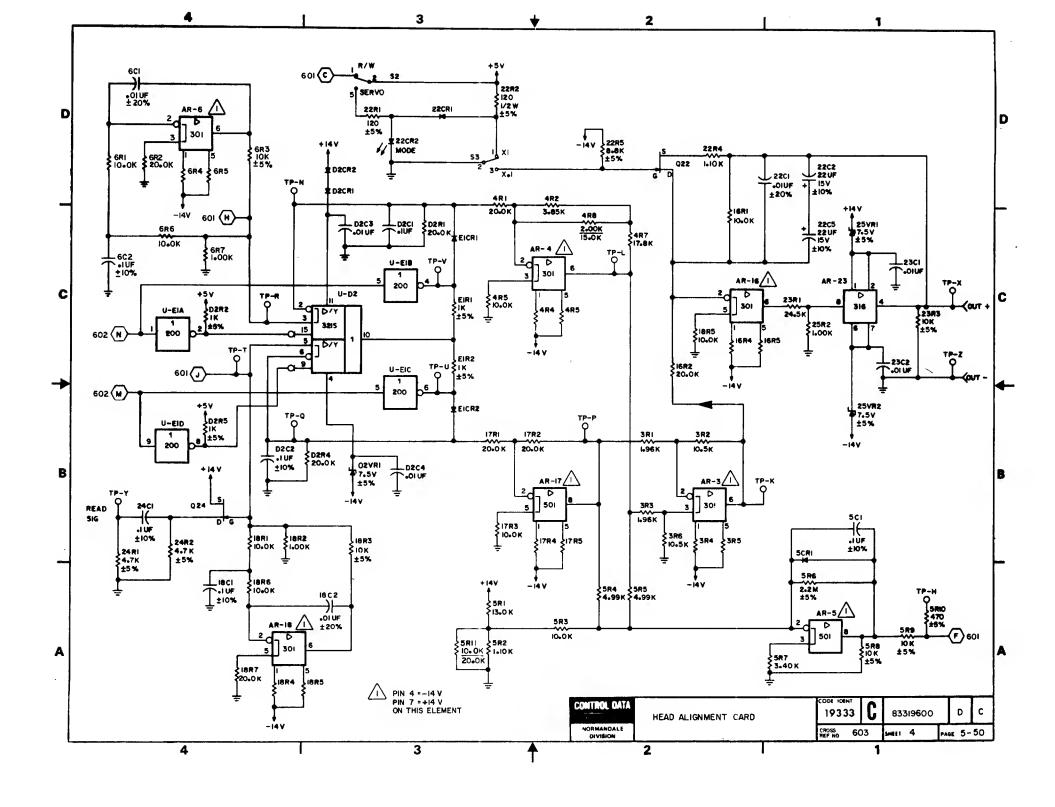


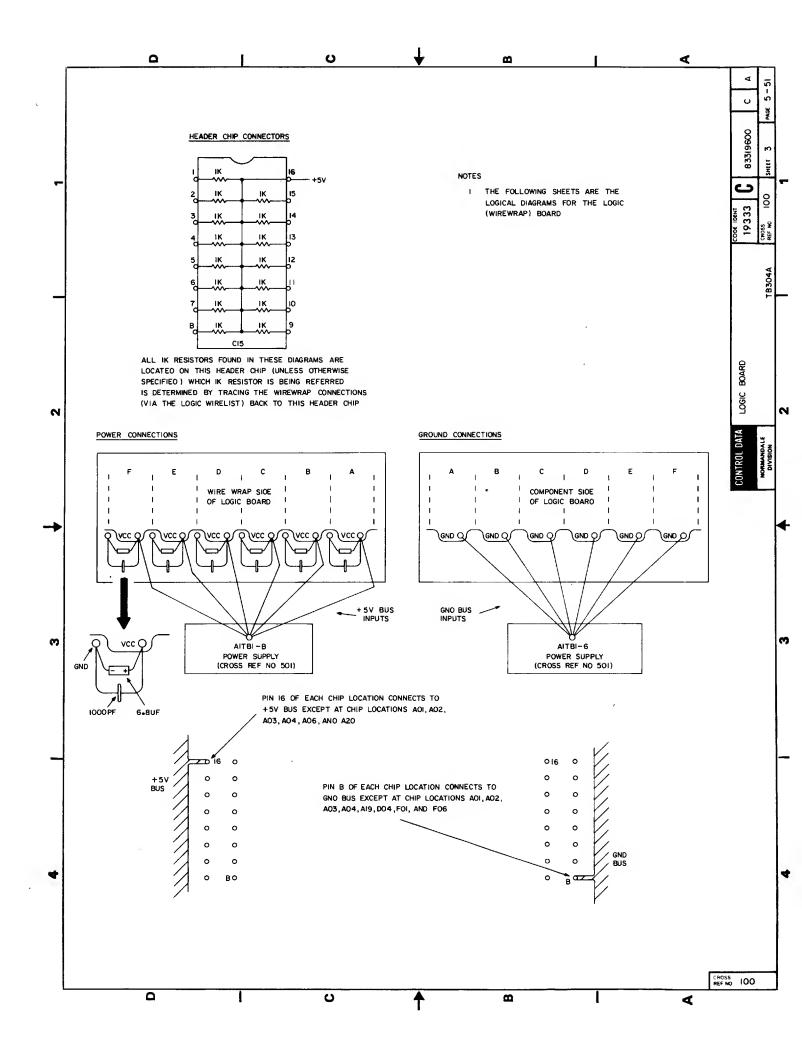


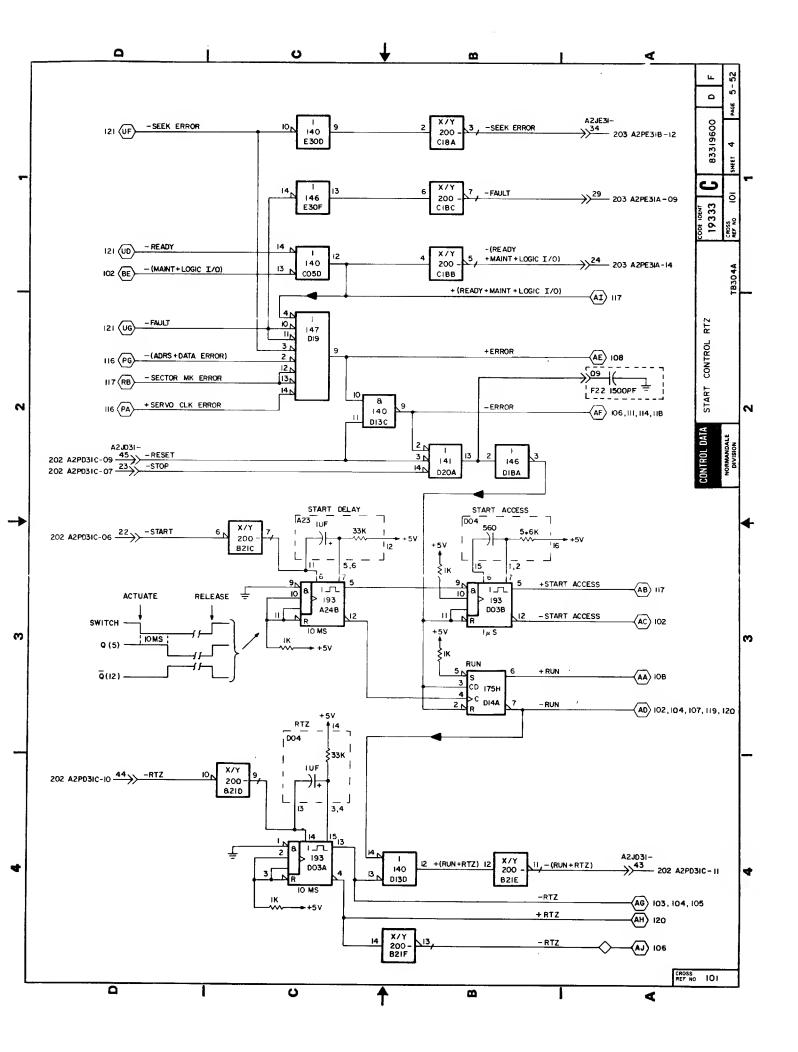


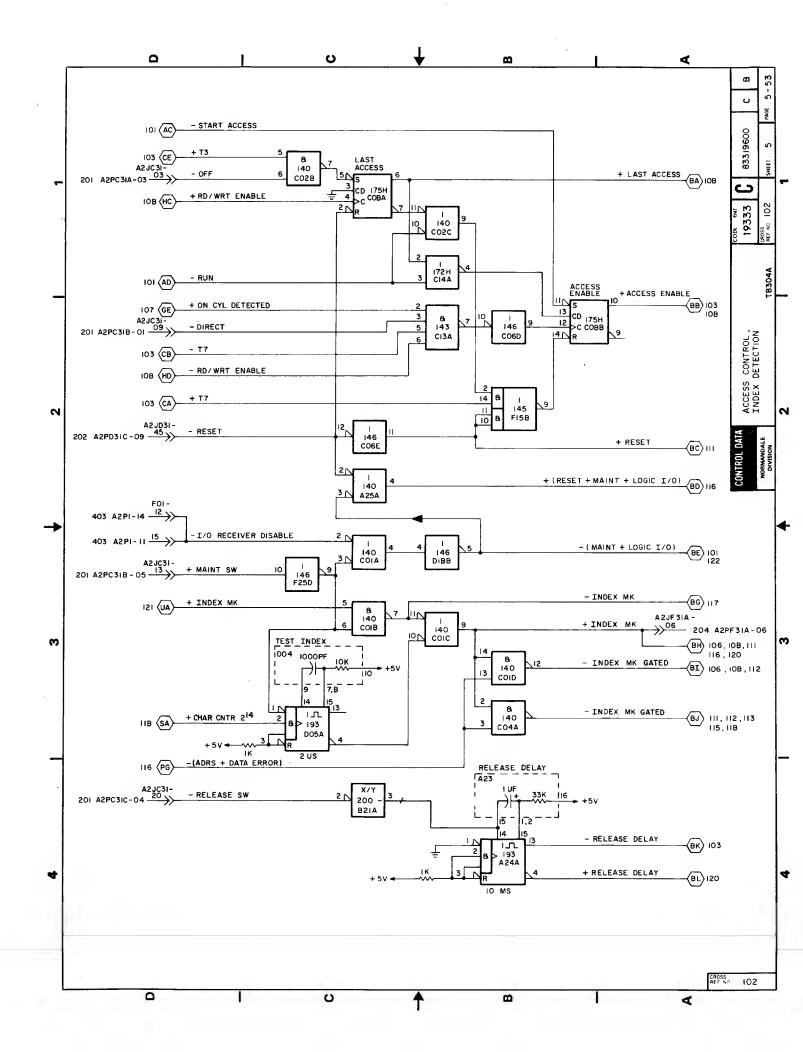
TB304A

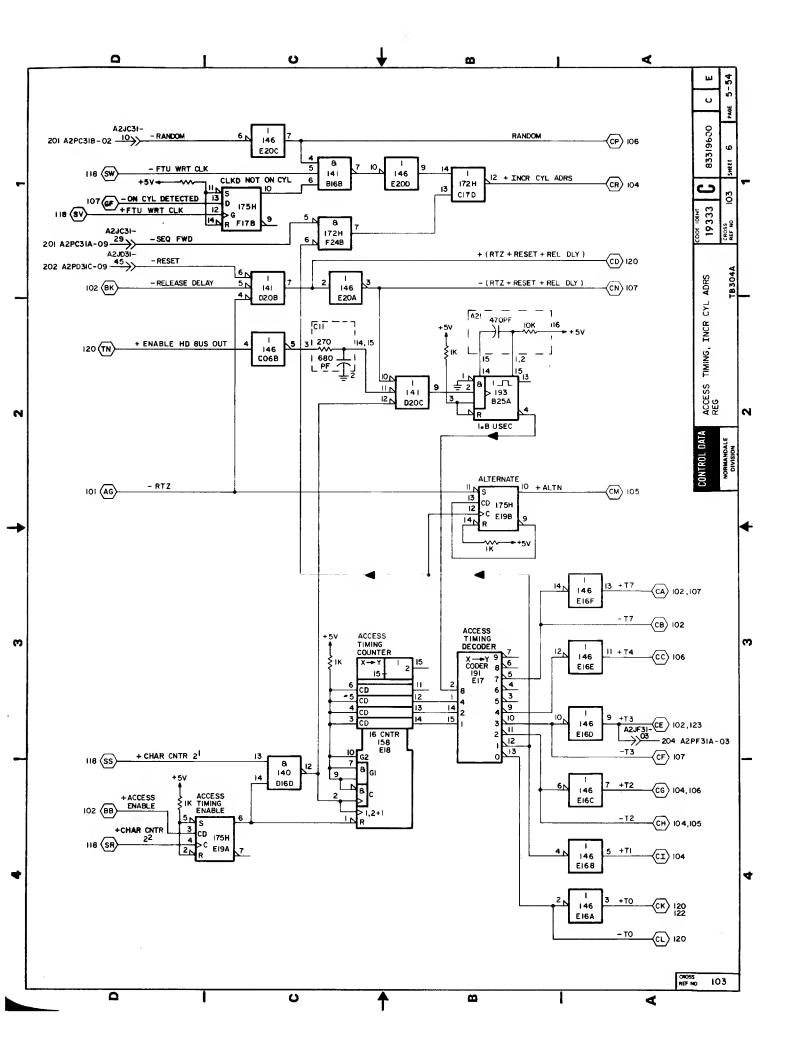
DIAGRAMS

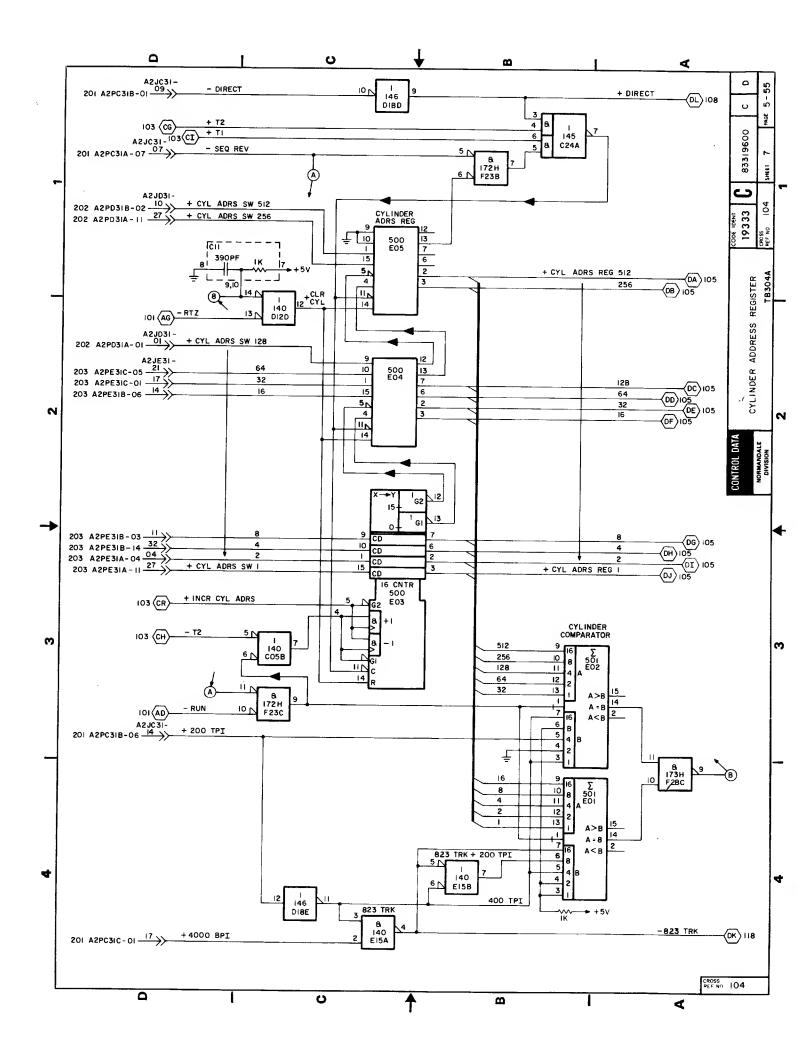


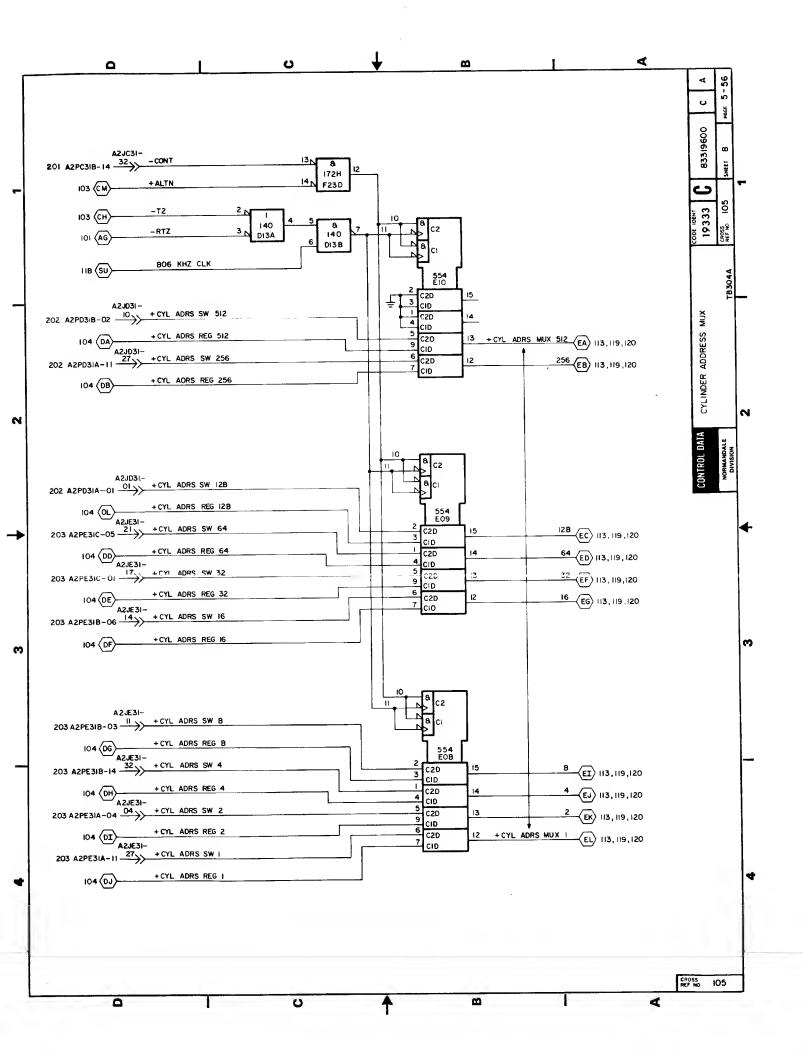


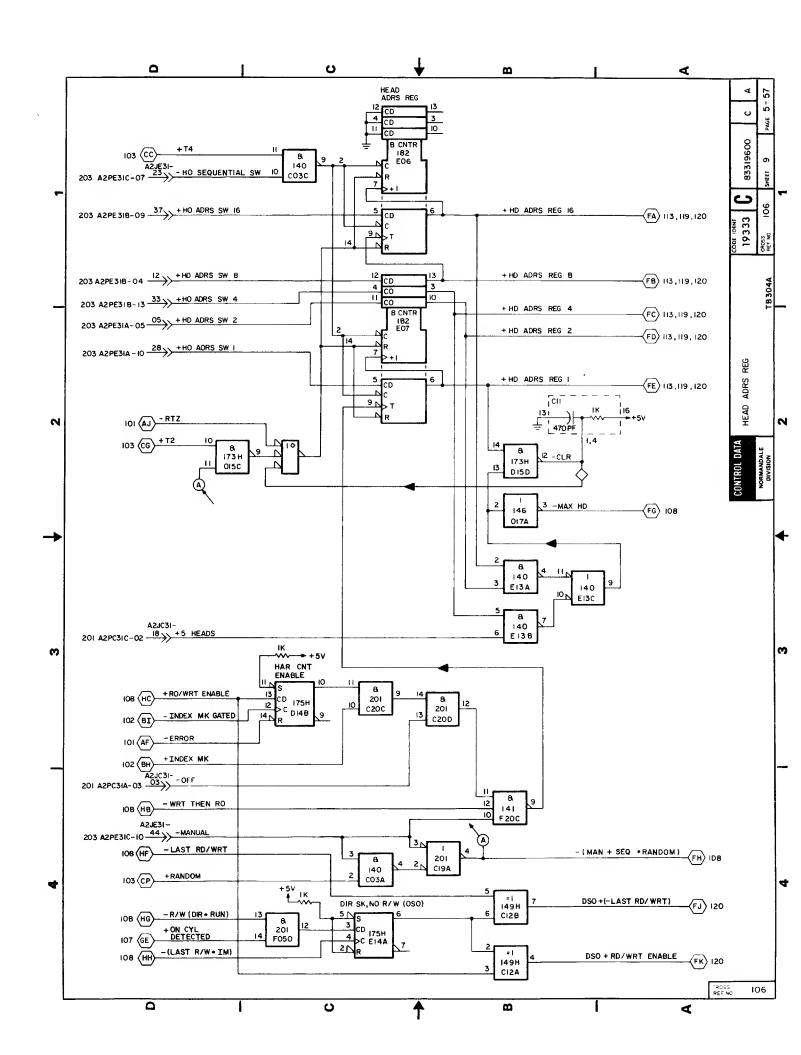


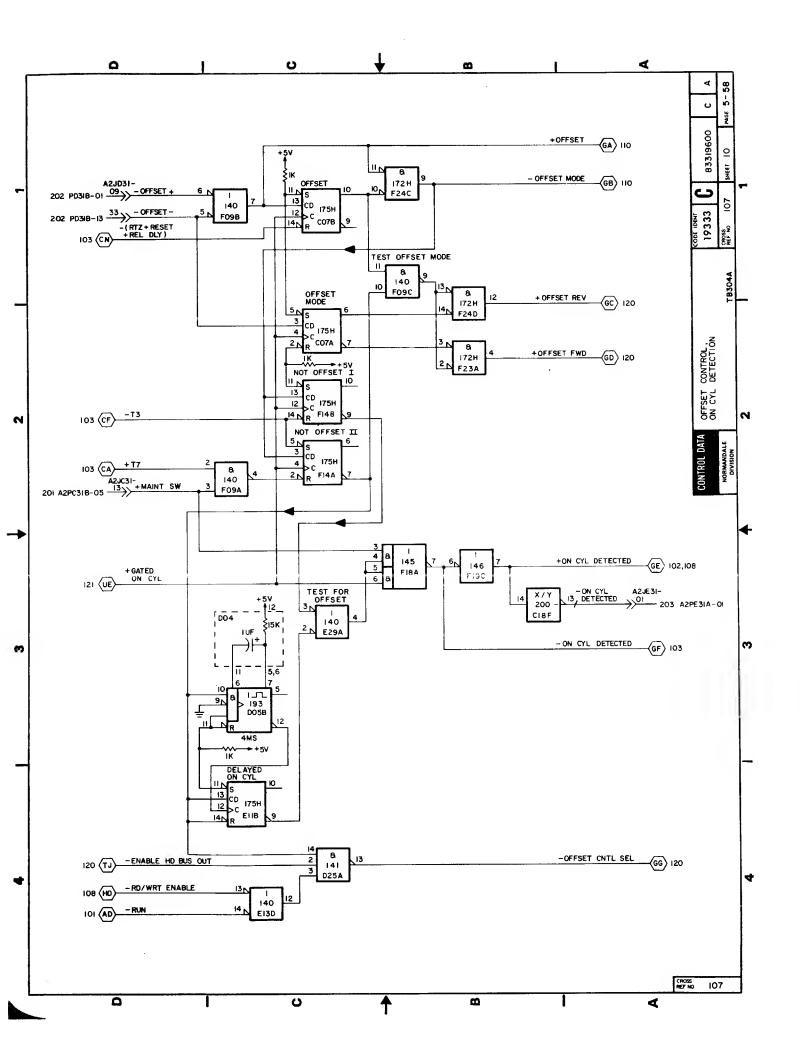


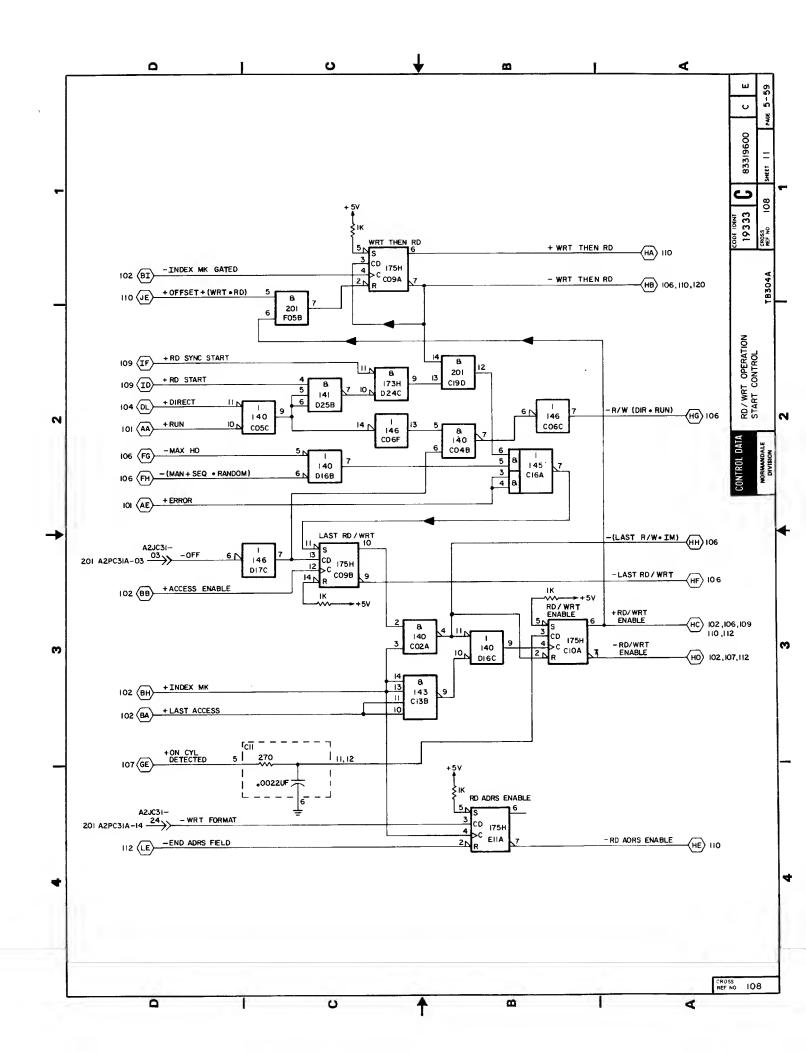


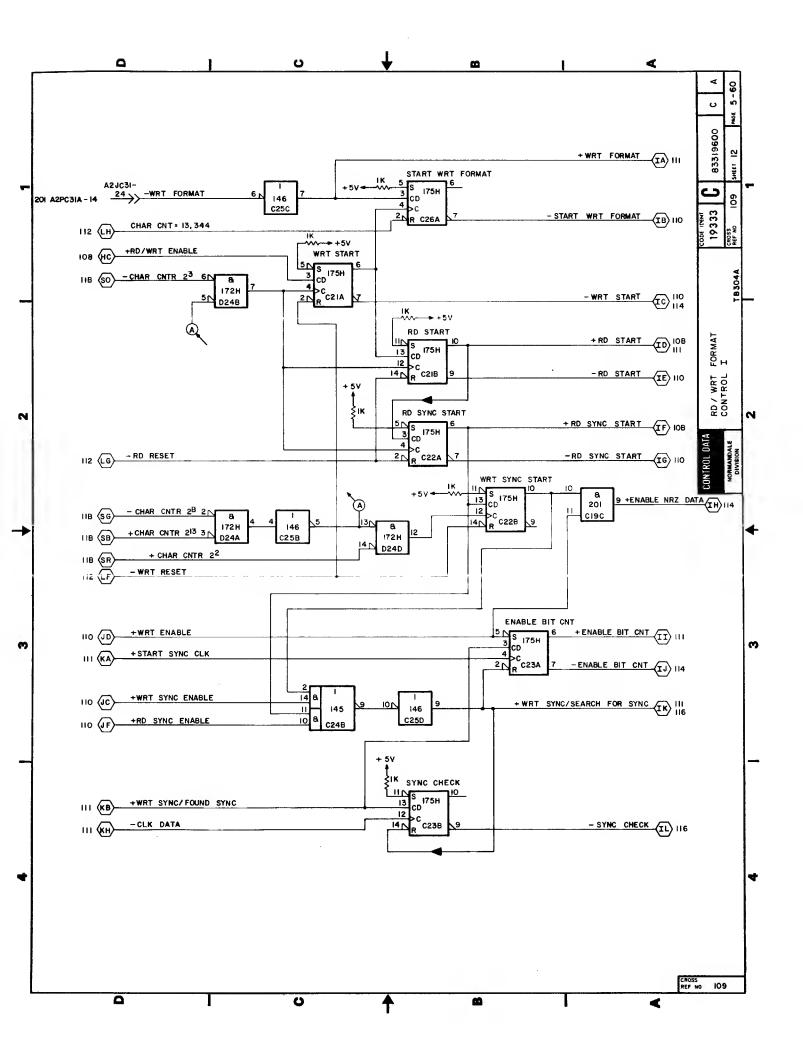


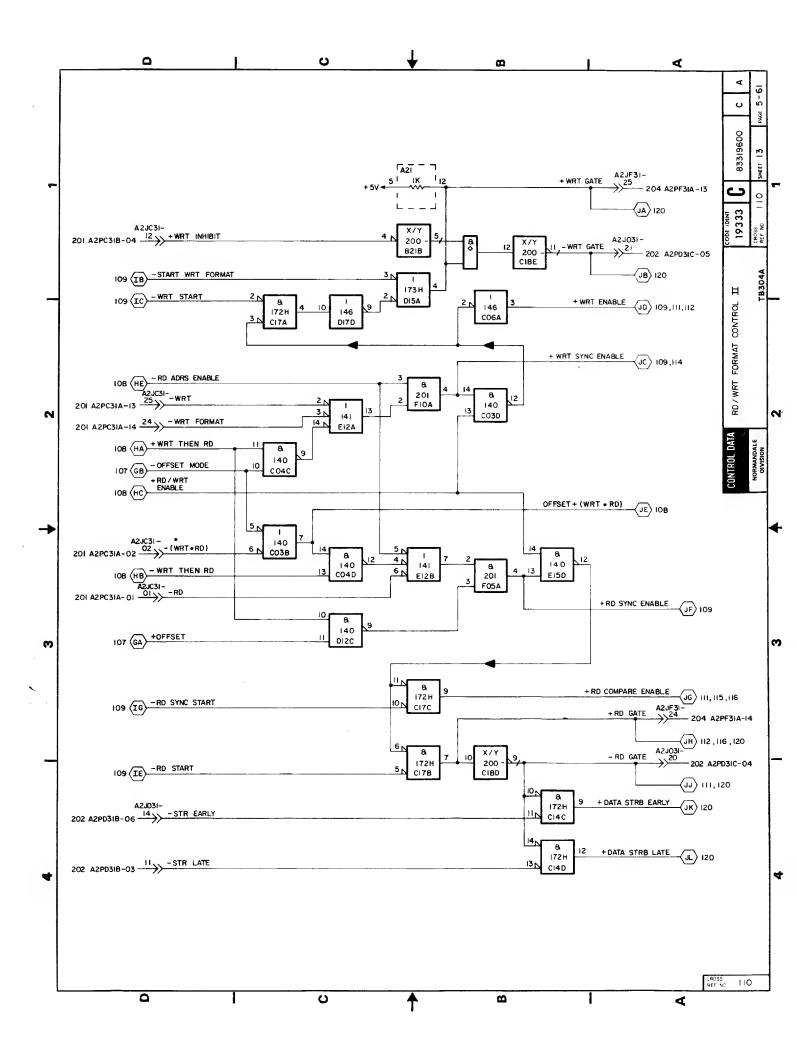


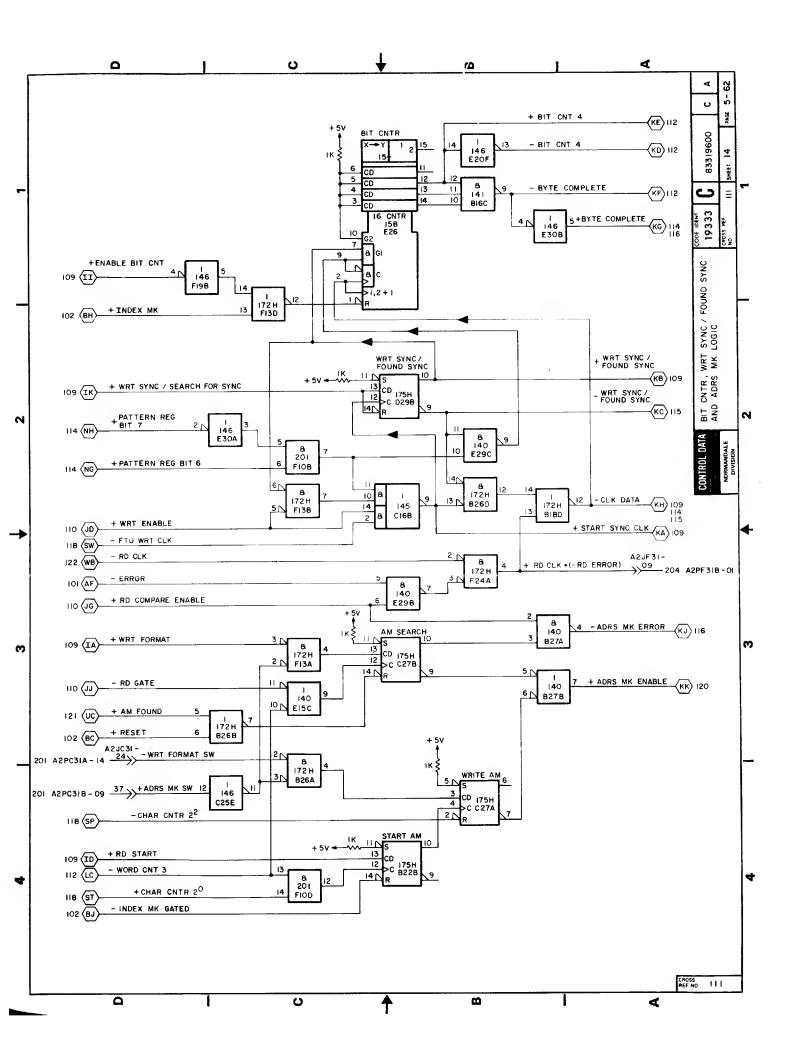


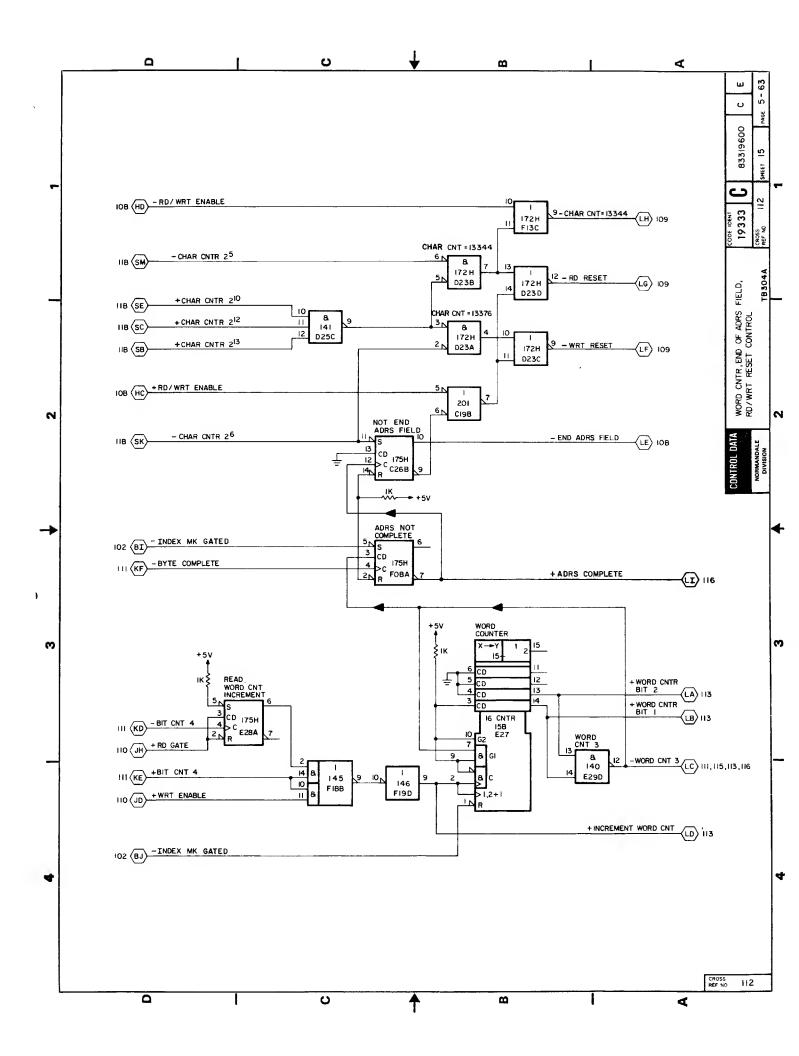


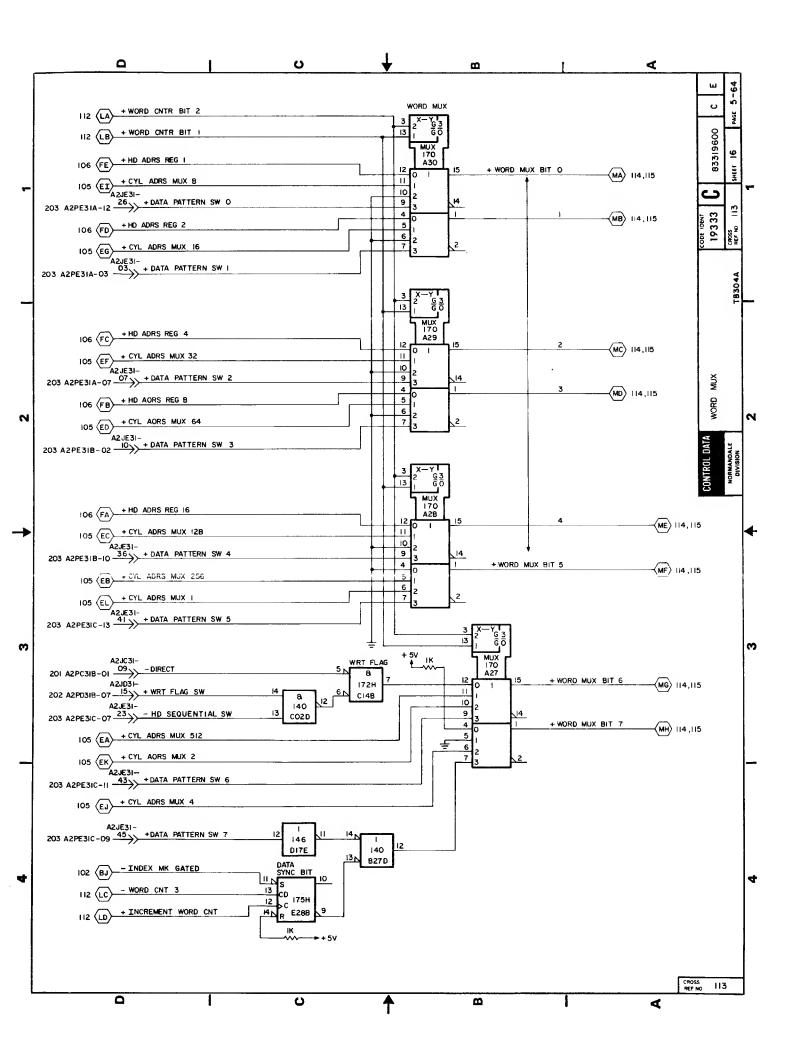


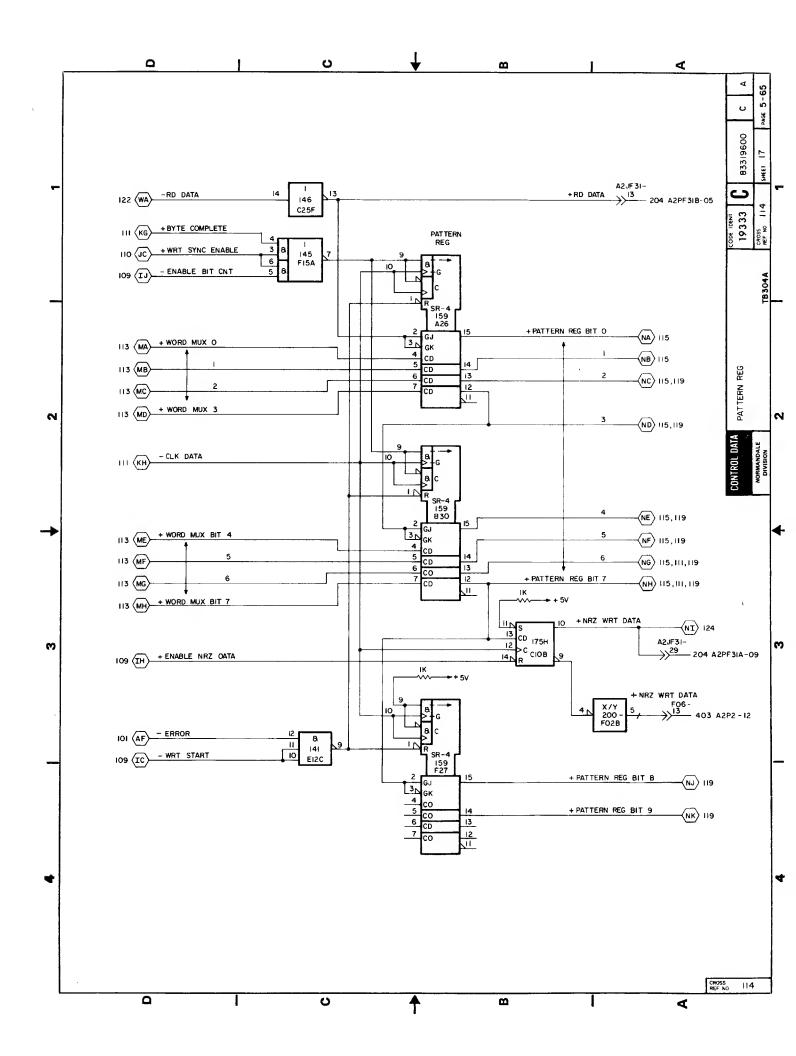


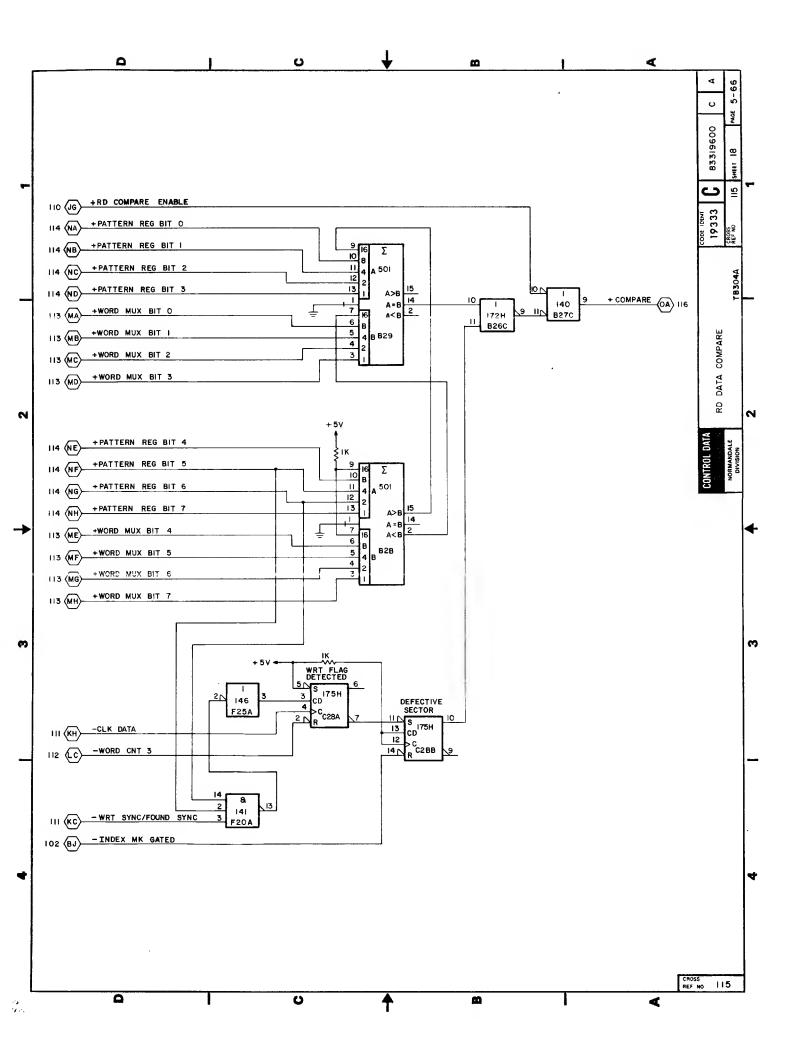


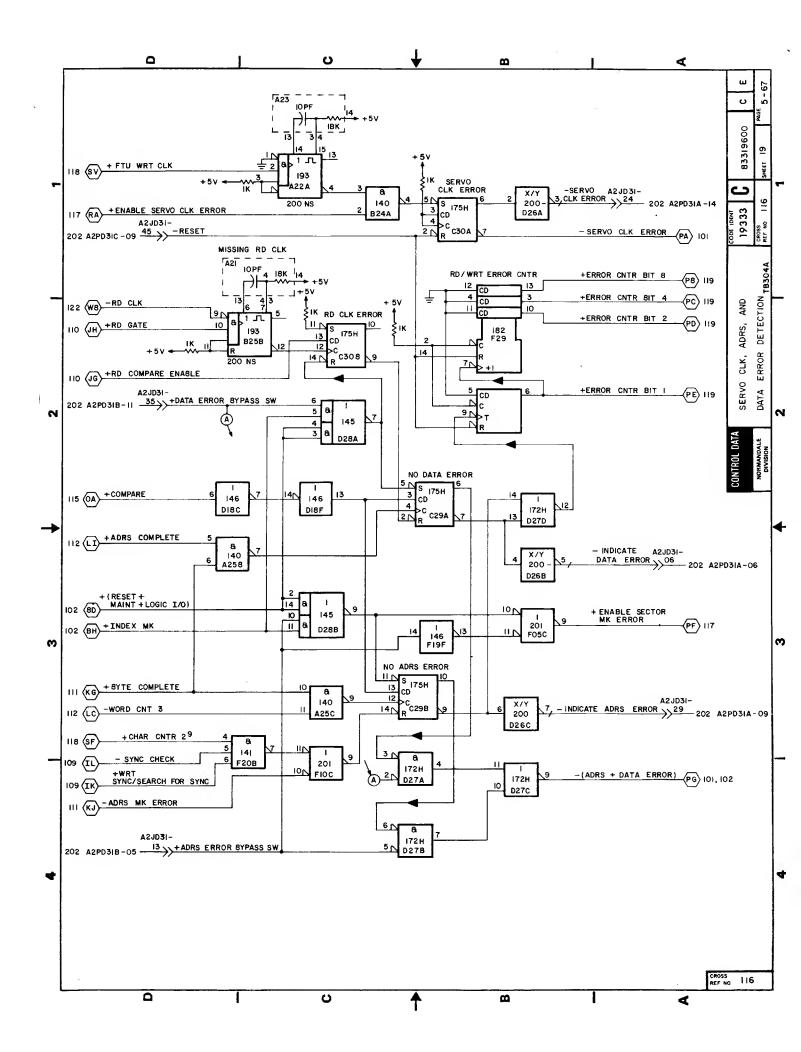


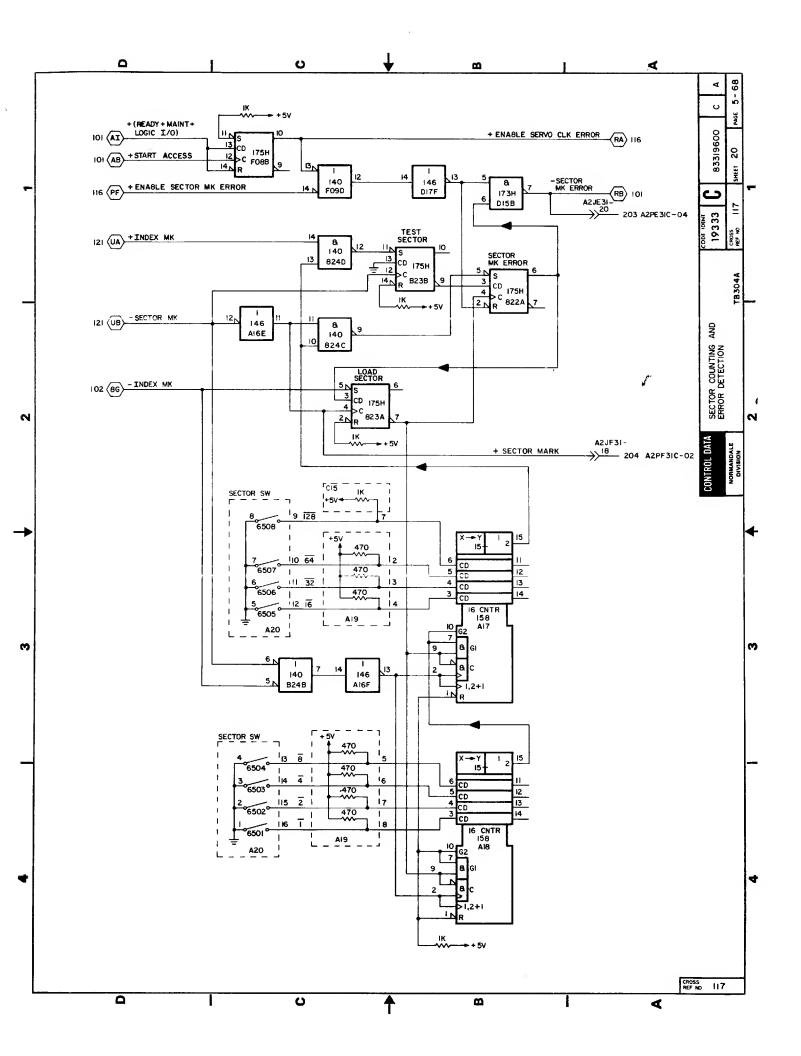


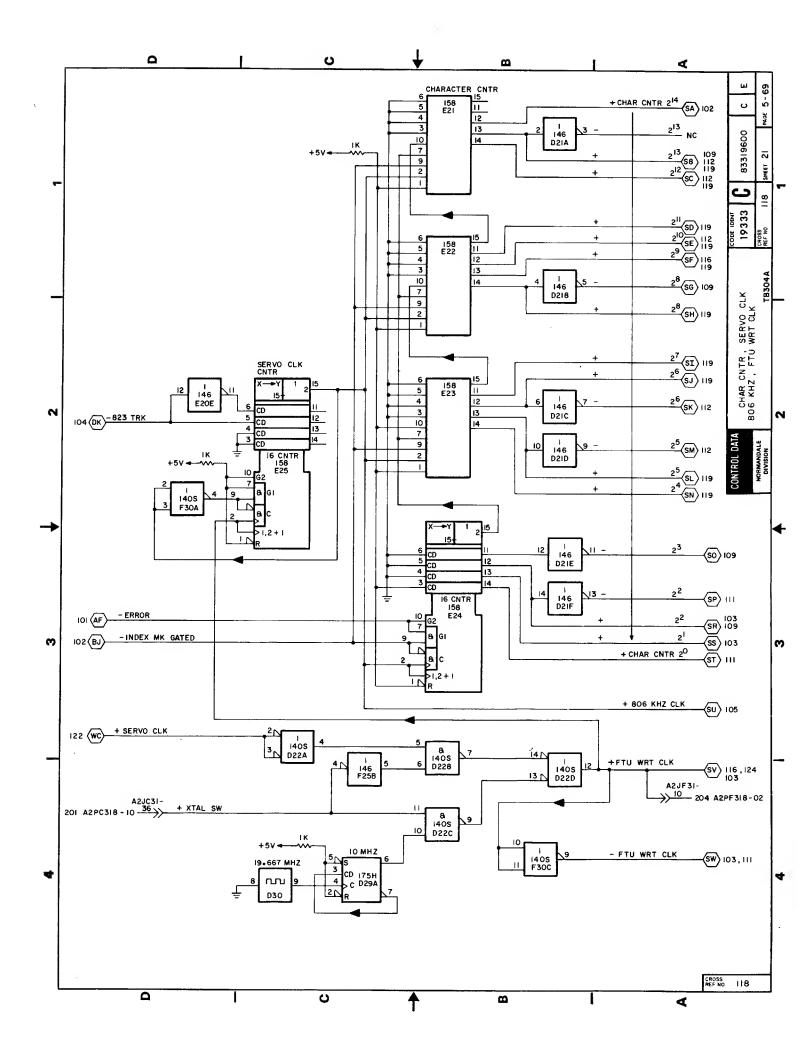


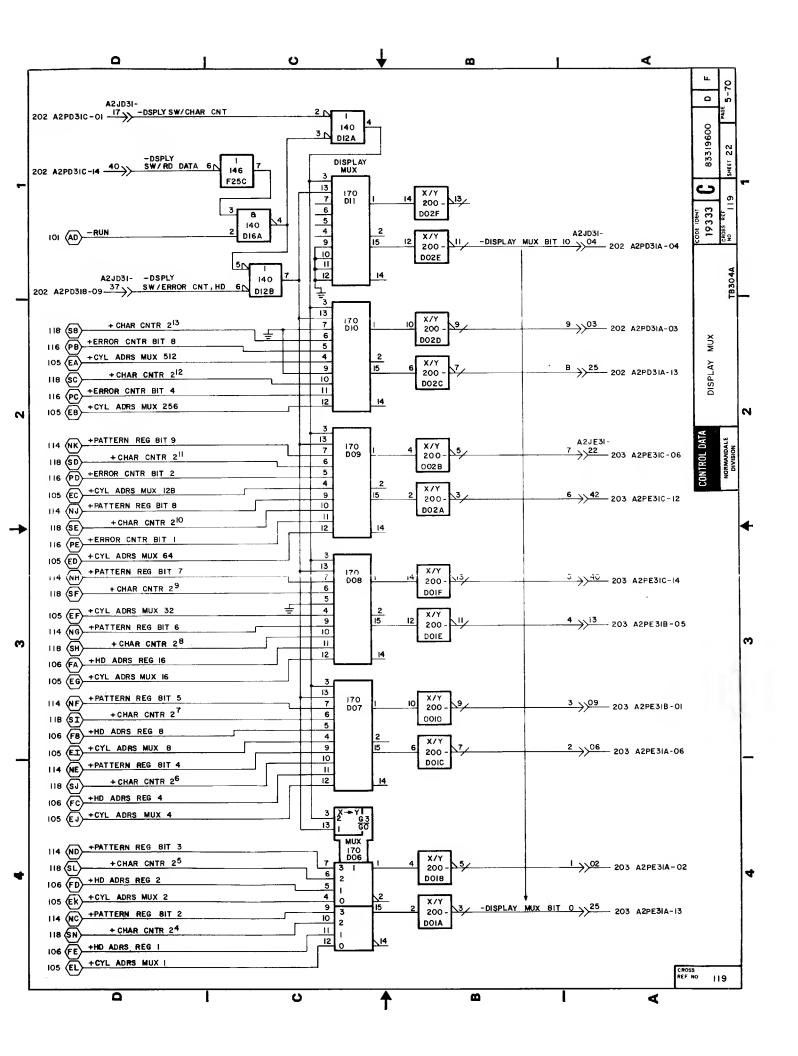


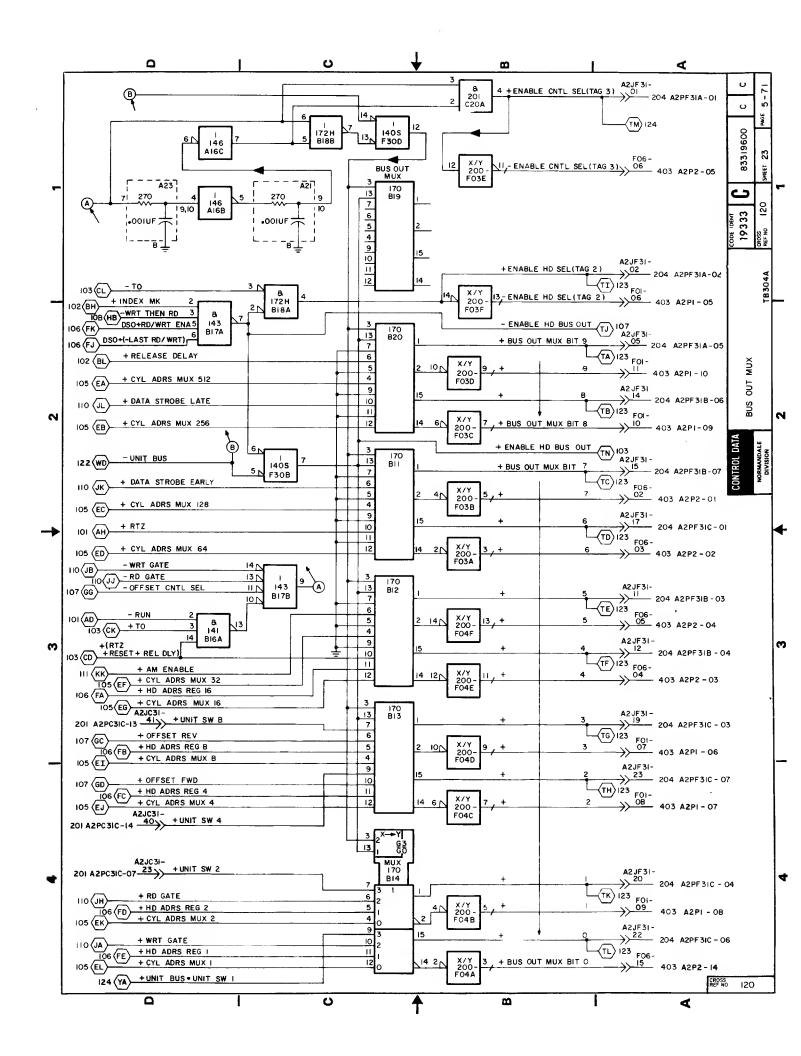


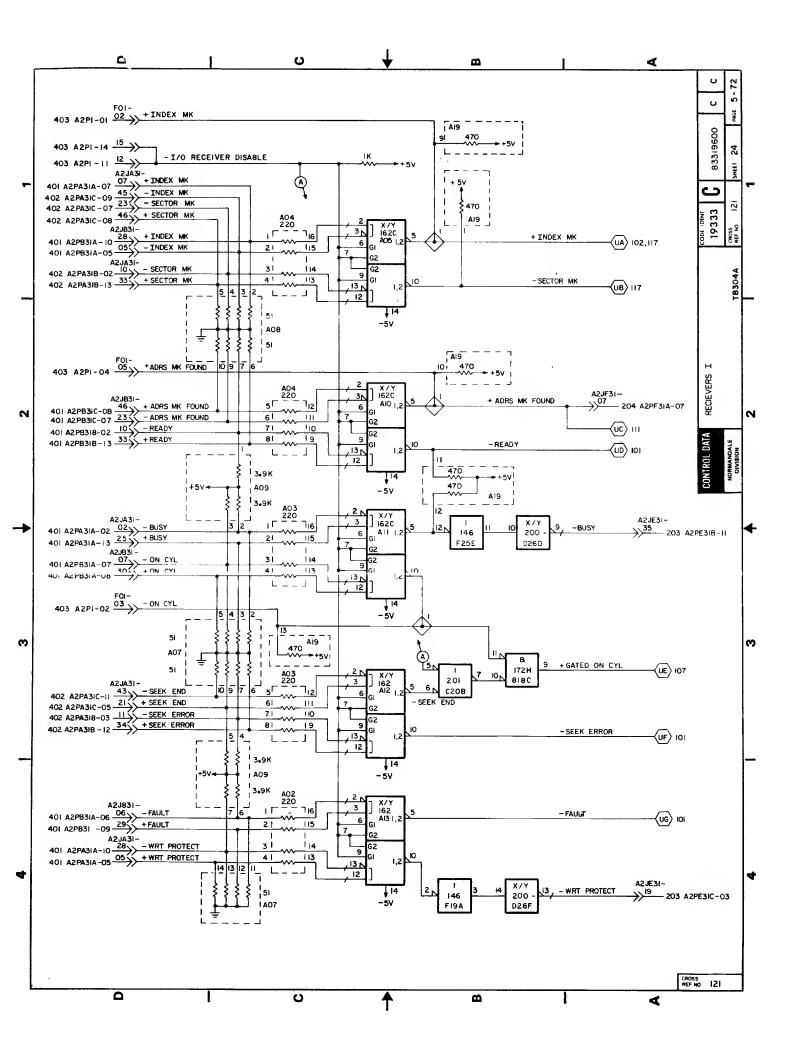


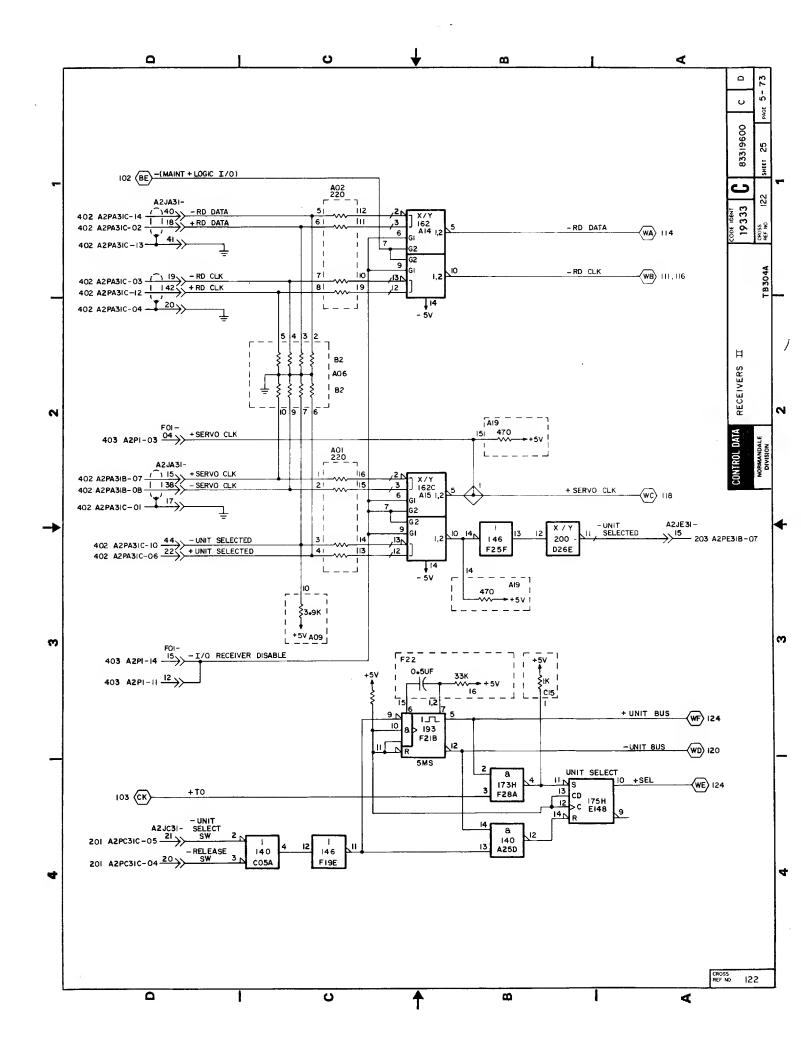


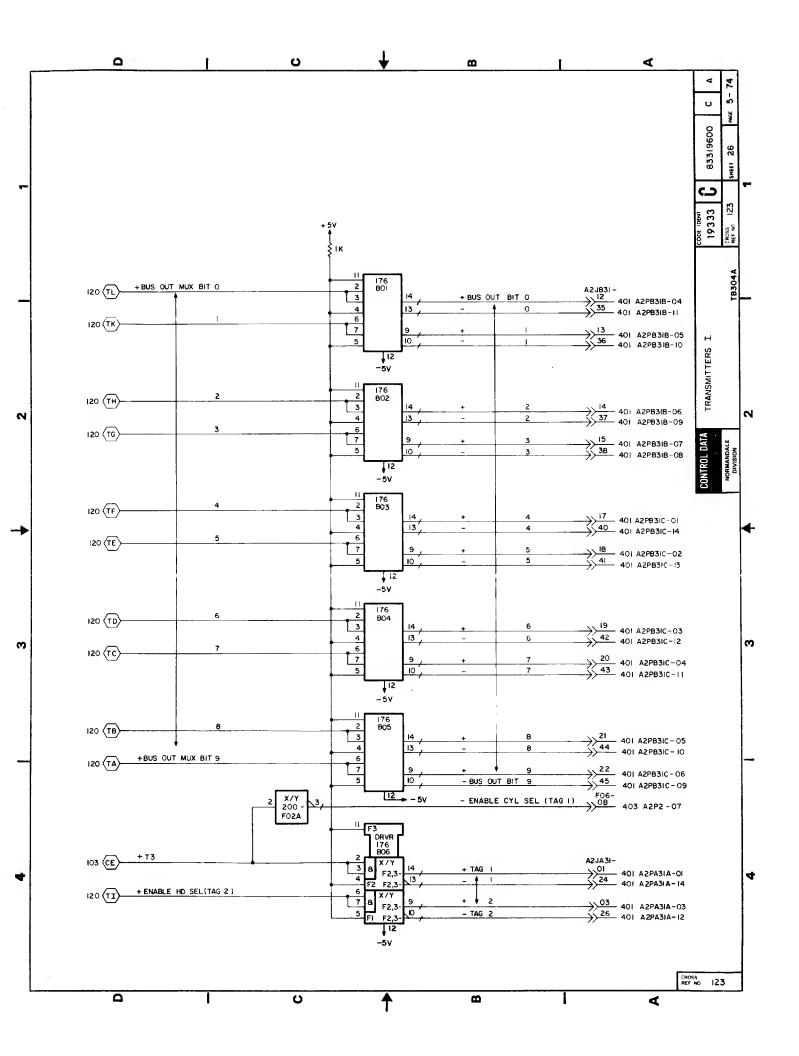


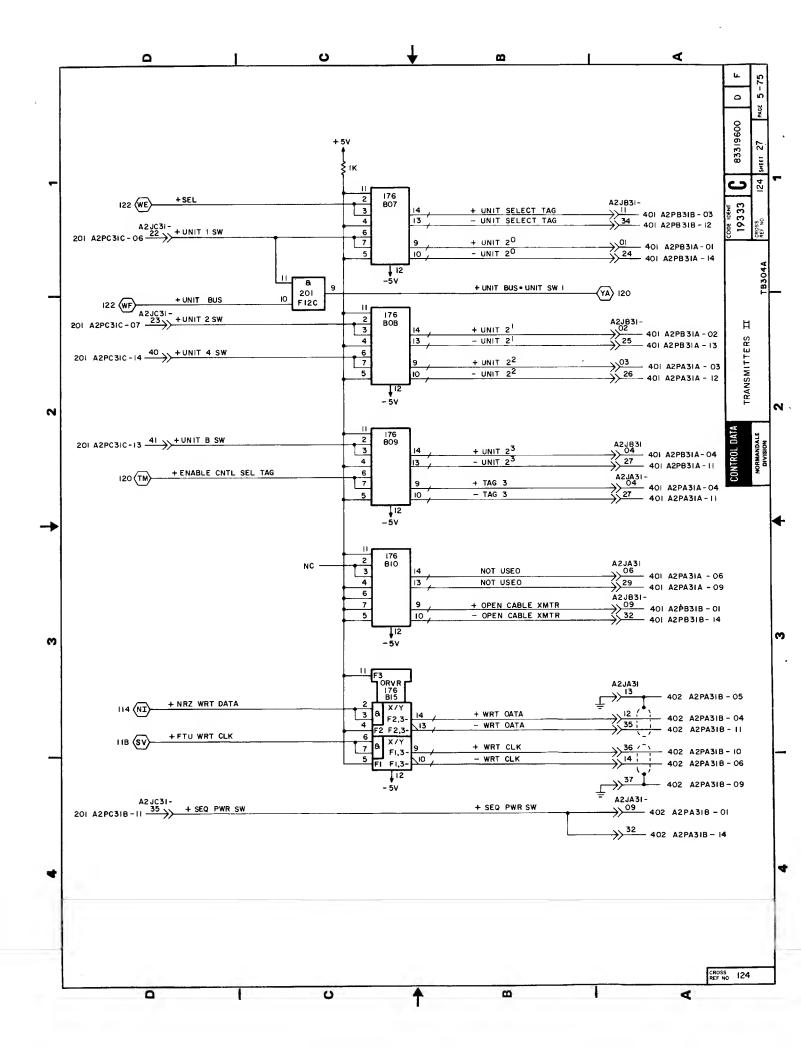


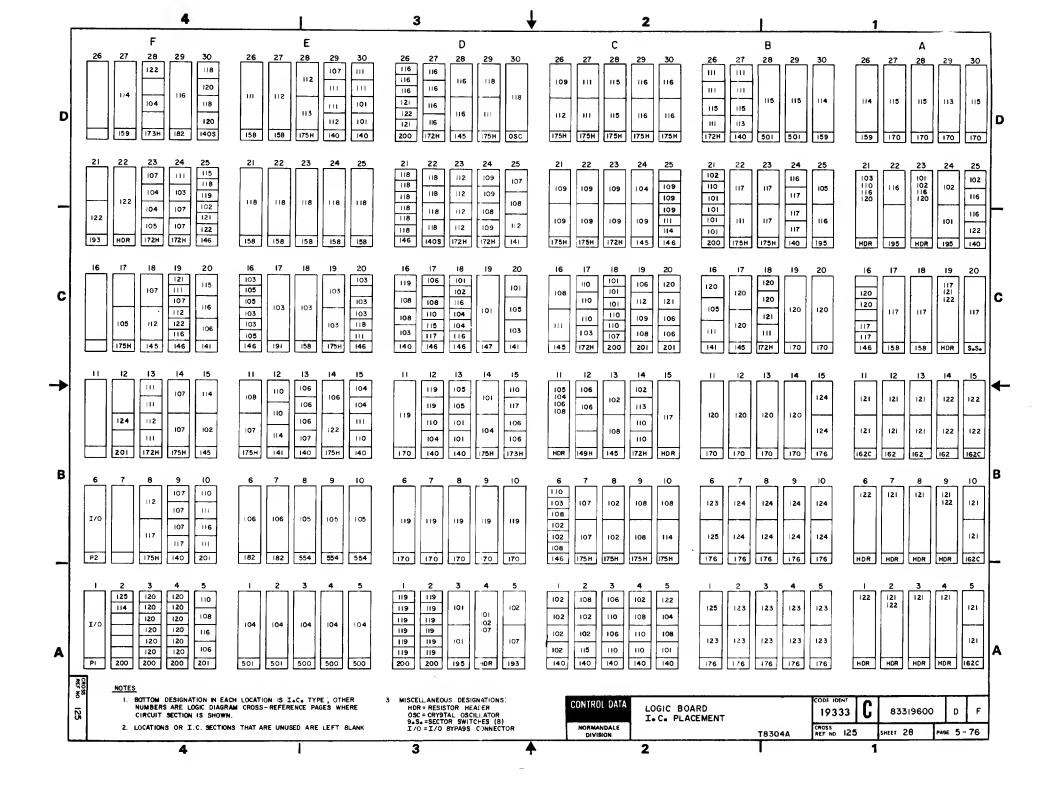


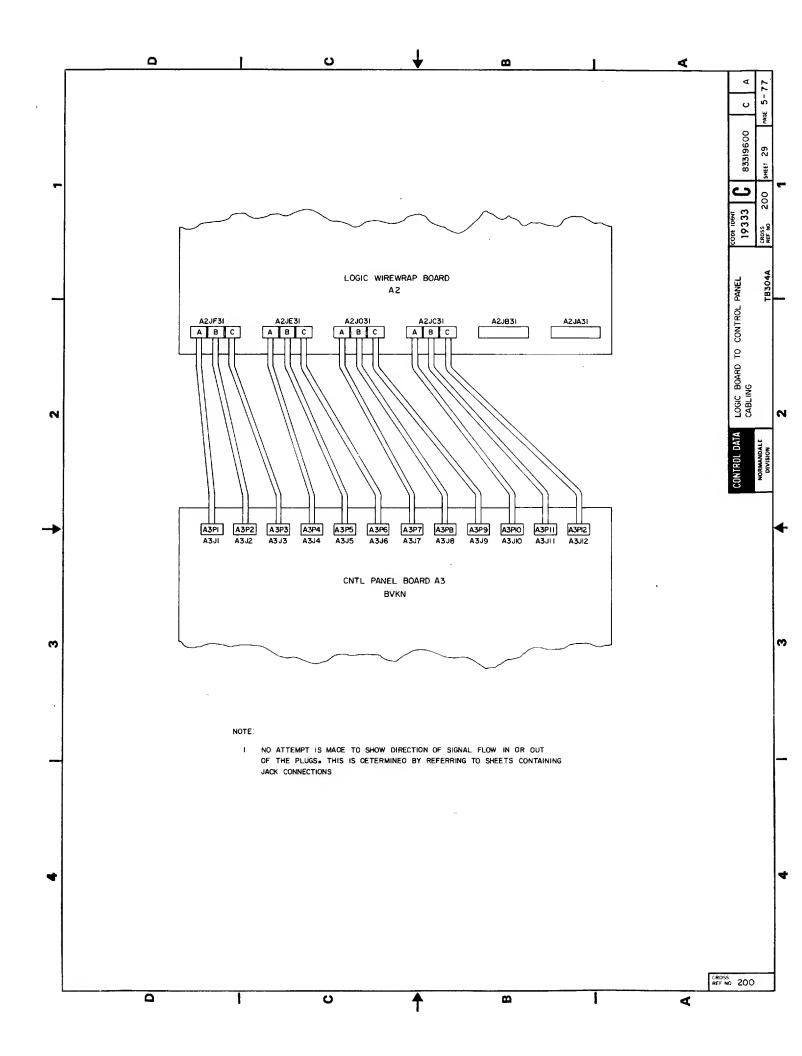


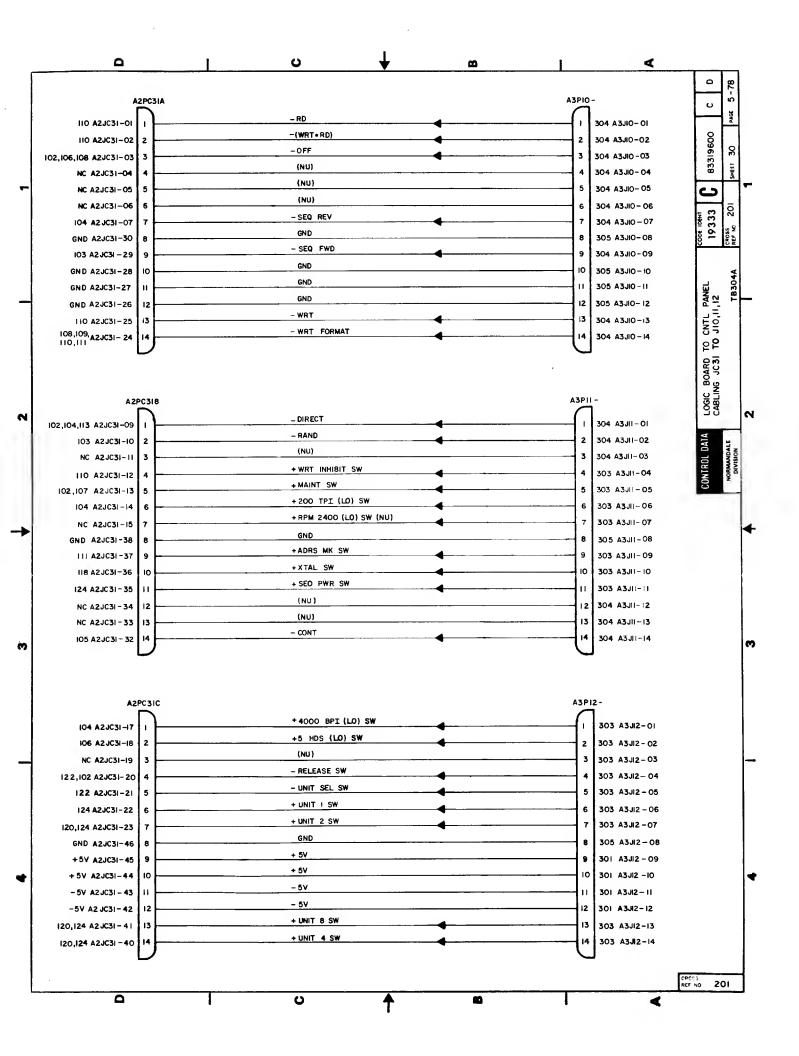


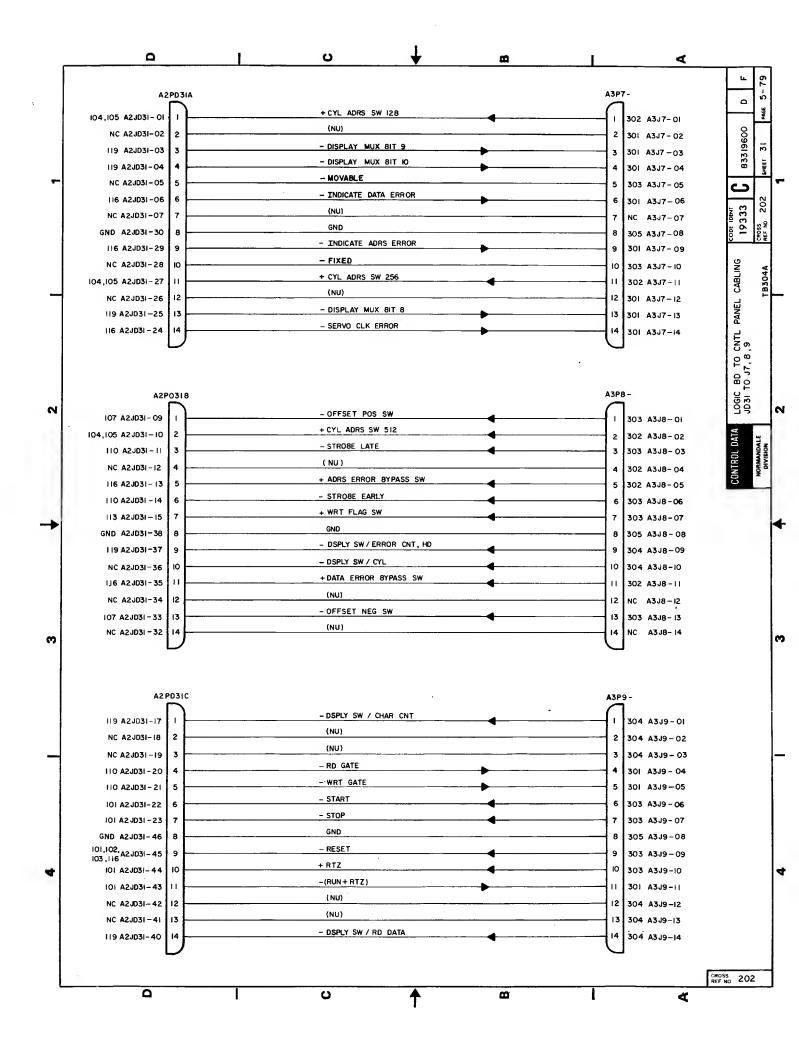


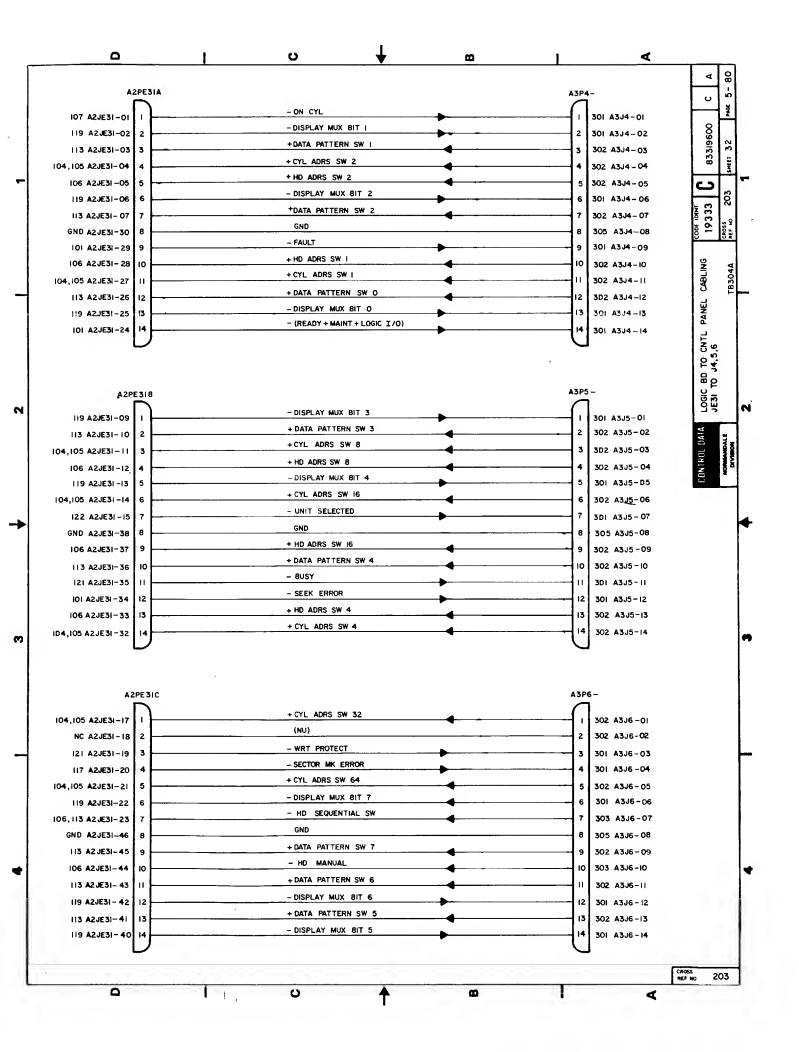


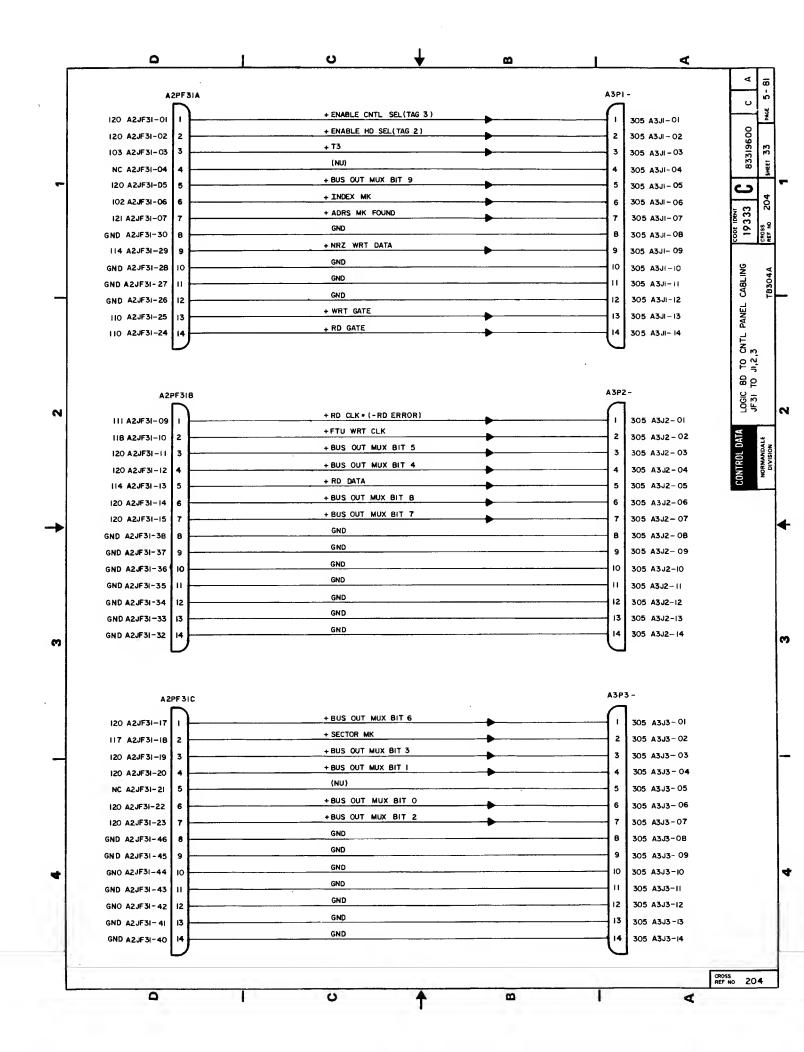


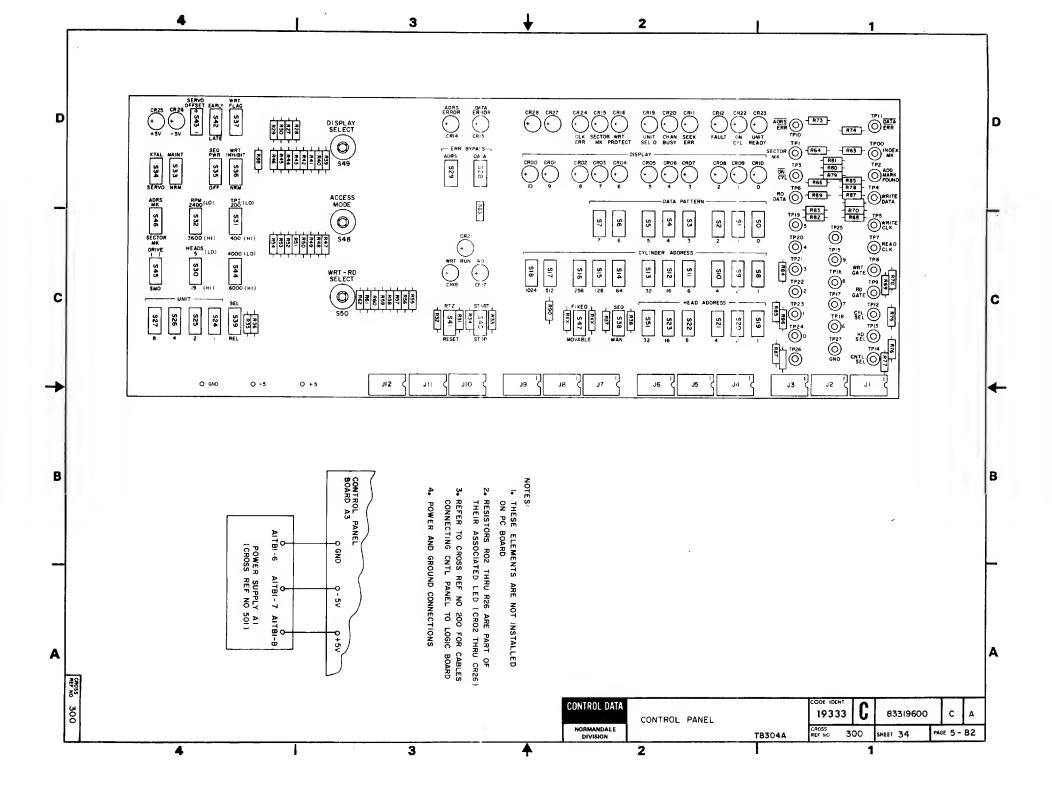


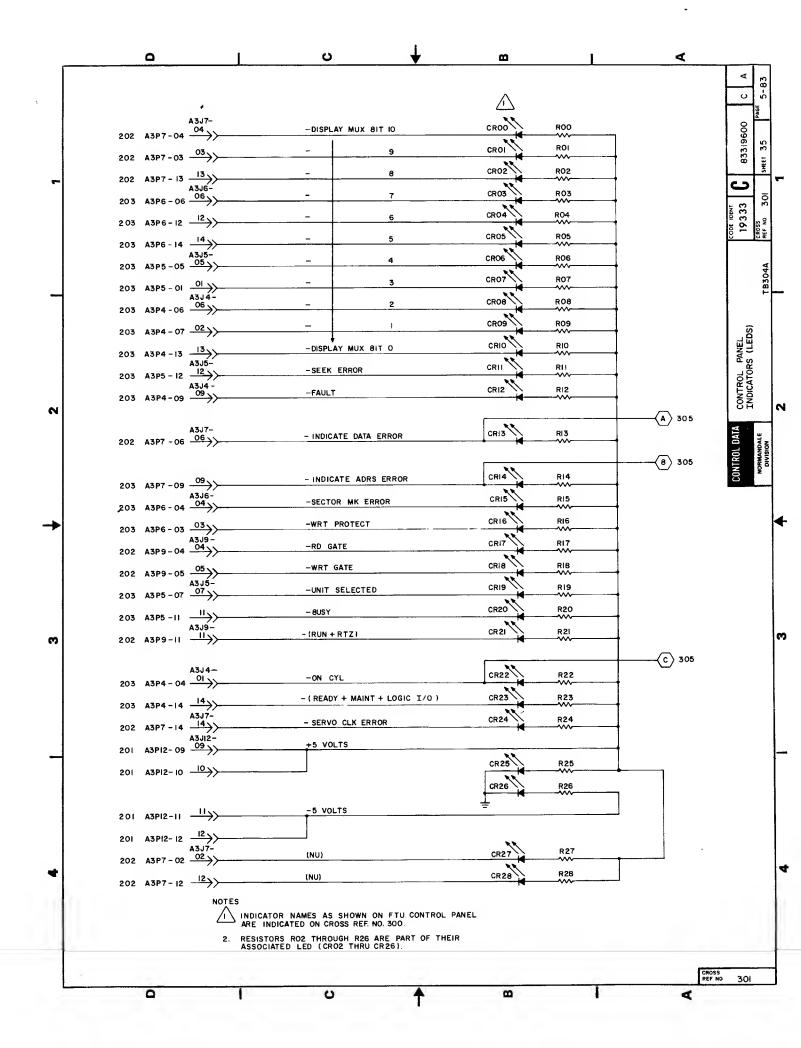


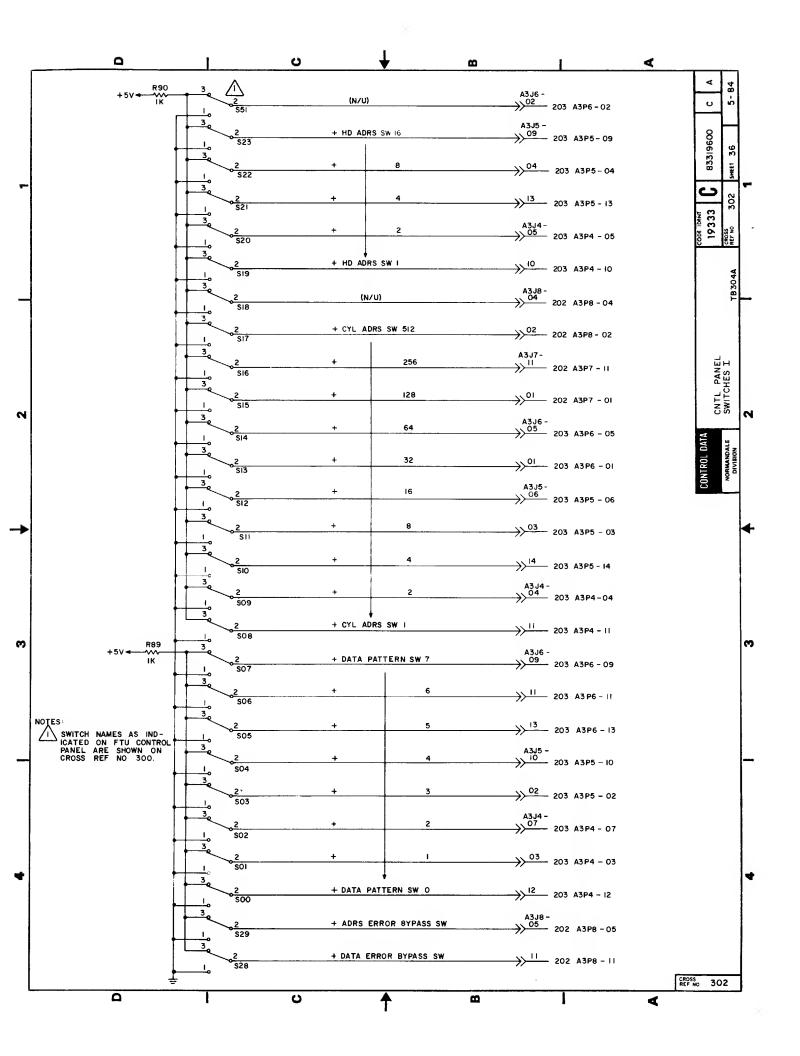


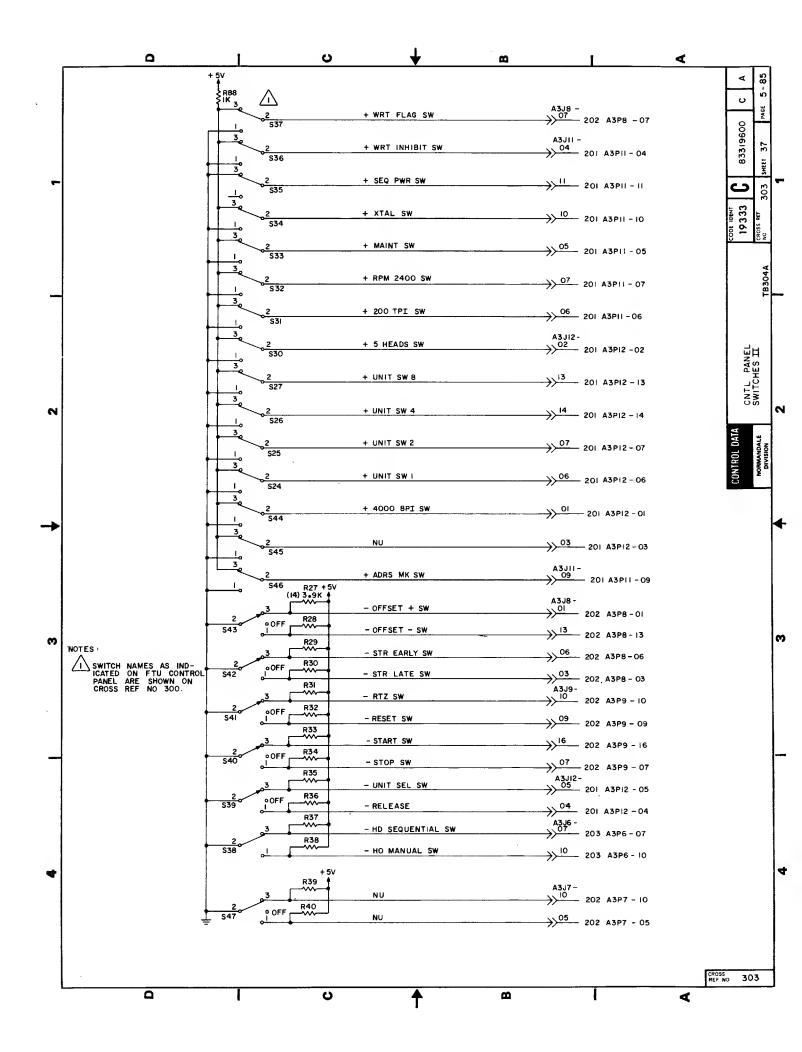


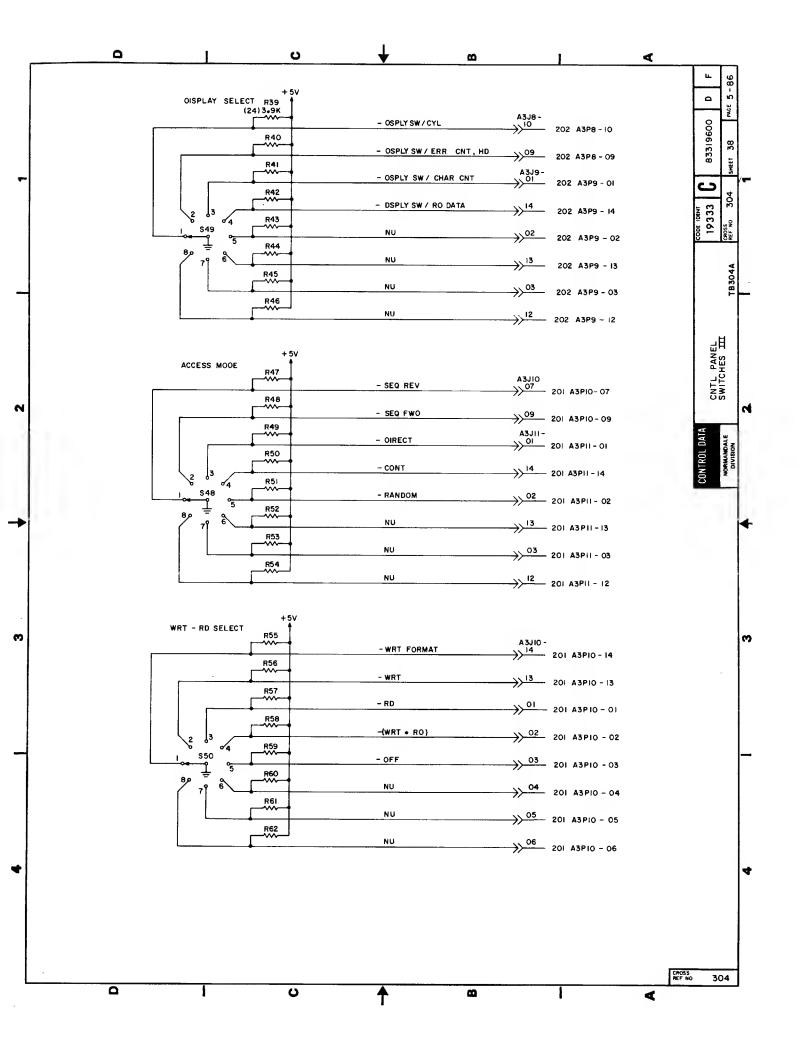


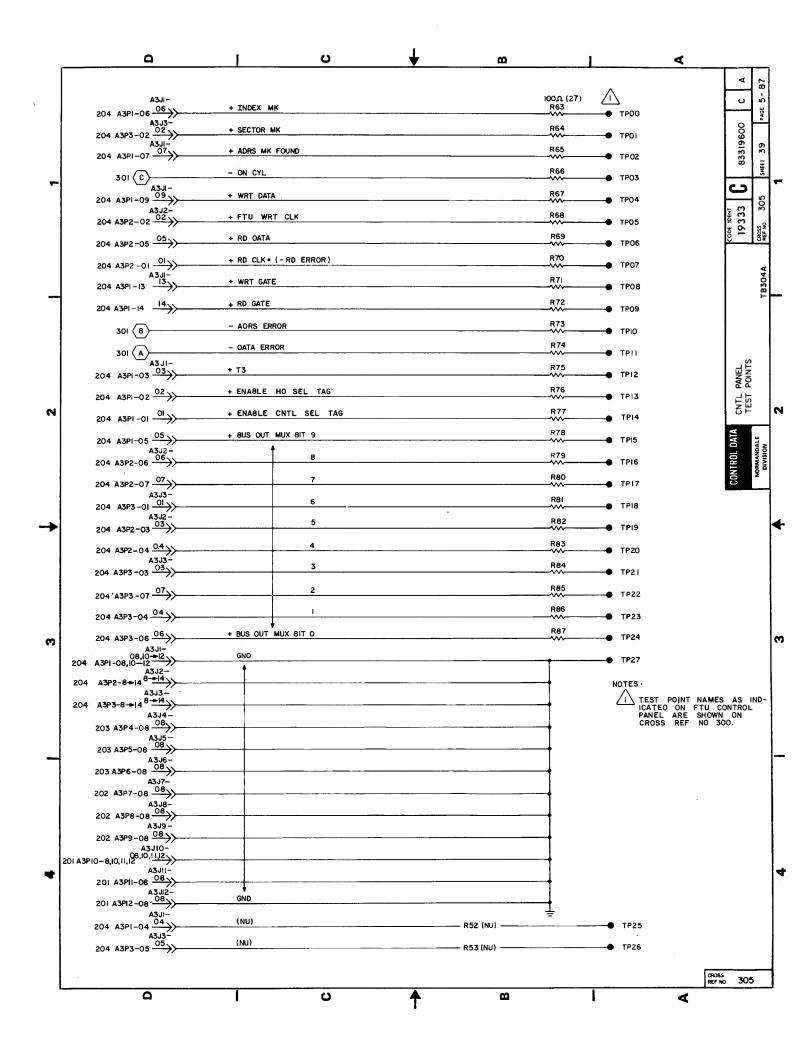


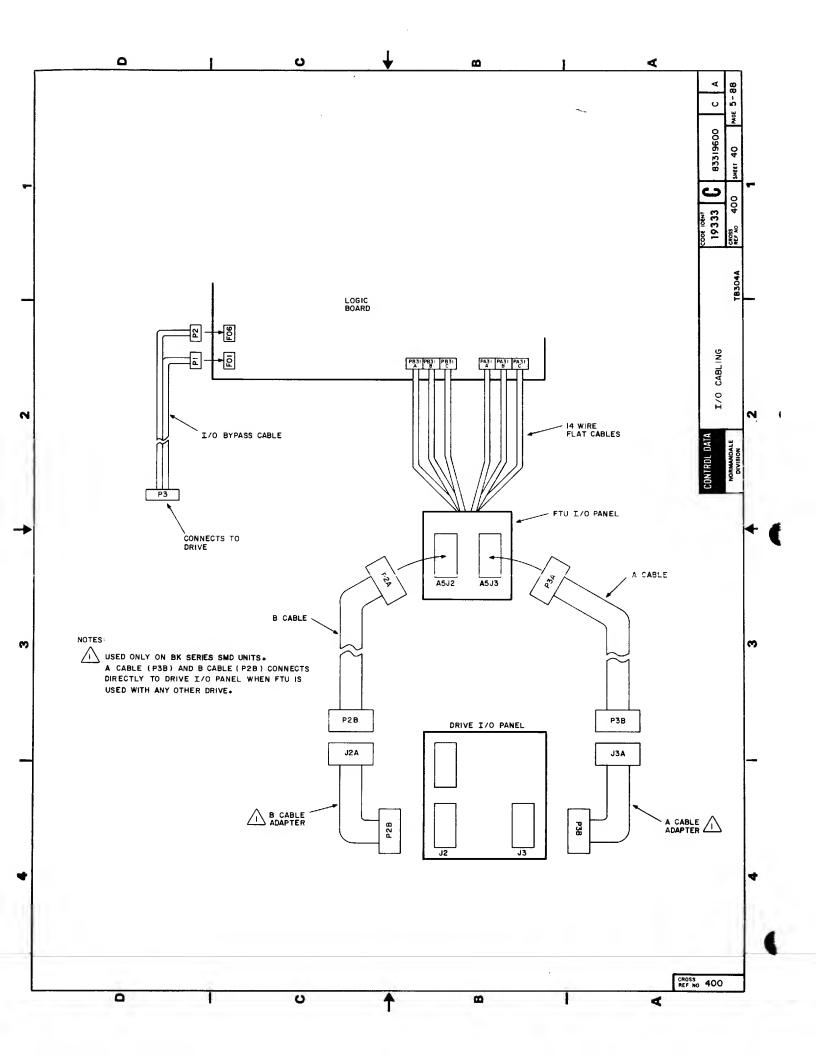


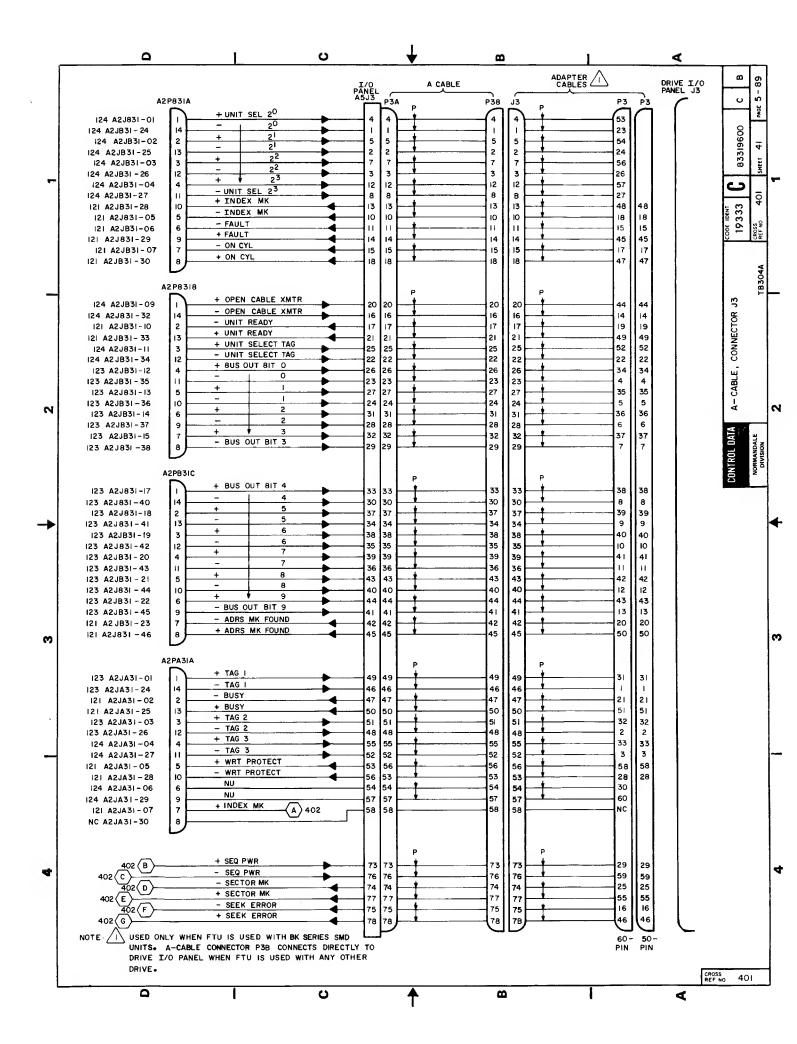


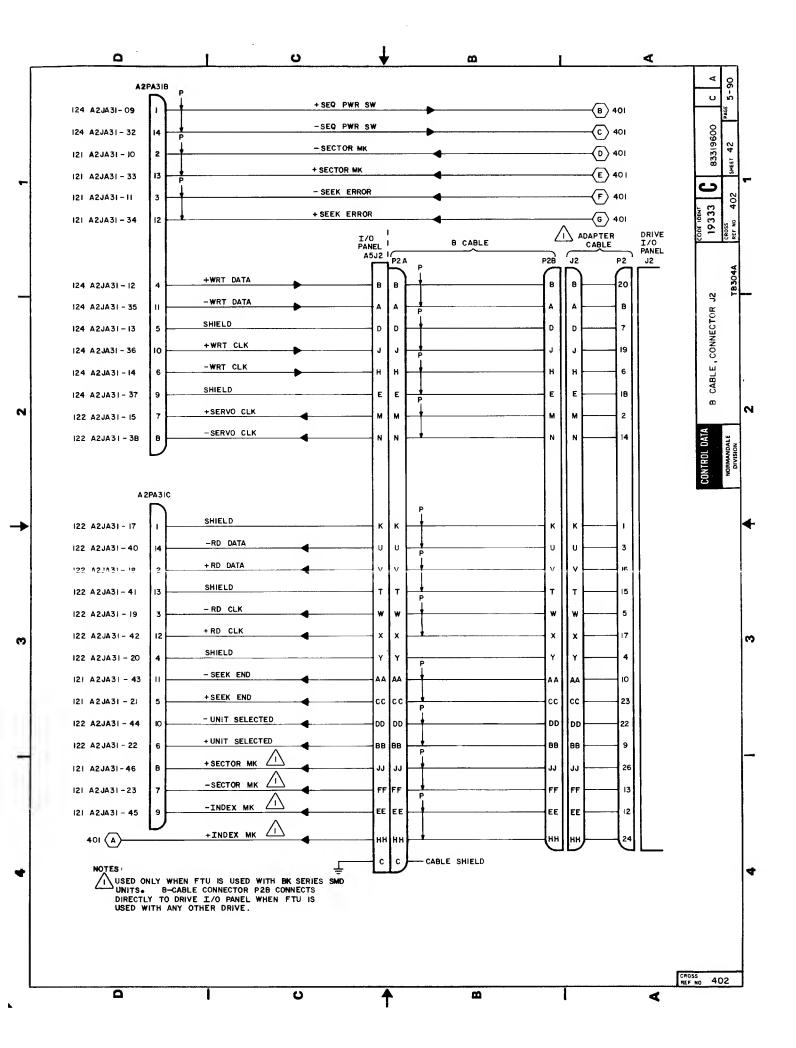


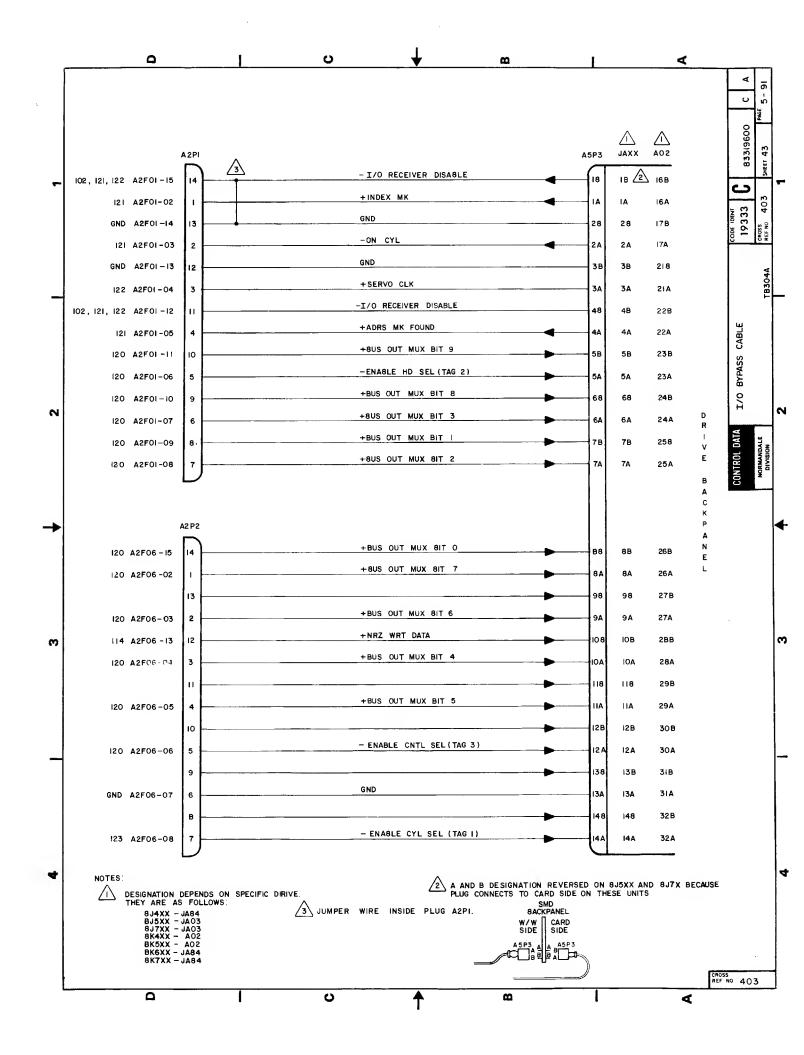












SECTION 6

WIRE LISTS

INTRODUCTION

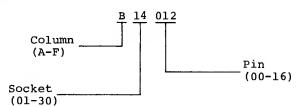
This section contains the Logic-Board wire wrap lists for the TB304B/C and the TB304A. The two lists are identified by yellow divider sheets. The power and control-panel harnesses, as well as the various I/O cables, are fully documented in the diagrams section.

The wire wrap lists are double-ended. That is, each wire is presented twice. Each end of the wire appears once as an origin, and again as a Destination. For example:

ORIGIN	DESTINATION	$\frac{\mathbf{z}}{}$
A04013	C29002	1
•	•	
C29002	A04013	1

Each list is arranged alphanumerically by origin, A01001 being the first entry and

JF31029 the last. The location coordinates (Columns A-F, sockets 01-03) and pin numbers are interpreted as shown below.



The pin numbers are those for the 16-pin IC sockets, not for the IC chips themselves. See CR 011 for more information on this matter.

The Z column shows the position of the wire on the wire-wrap pin. Level 1 is the wrap closest to the board surface. Only two wraps are present on any pin.

TB304B/C

WIRE LIST

TITLE LOGIC BOARD WIRI (REF: 83249903)	E WRAP (TB	304B/C)		WL	DOCUMENT NO		O. of 33	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL.	OI	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	EVEL
	A01001 A01002 A01002 A01003 A01003 A01004 A01015 A01016 A02001 A02002 A02003 A02004 A02005 A02005 A02006 A02006 A02007 A02007 A02008 A02007 A02008 A02008 A02010 A02011 A02012 A02015 A02016 A02006 A02006 A02006 A02006 A02006 A02007 A02008	JA31015 A06010 JA31038 A06009 JA31044 A06007 JA31022 A06006 A15012 A15013 A15002 JB31006 A07012 JA31028 A07013 JA31005 A07014 JA31040 JA31047 A06005 A14012 A14013 A14003 A14007 A13012 A13013 A13003 A13002 JA31012 A13013 A13003 A13002 JA31025 A07002 JA31025 A07002 JA31025 A07003 JB31007 A07004 JB31030 A07004 JB31030 JB31007 A07004 JB31030 JB31007 A07004 JB31030 JB31007 A07004 JB31030 JB31007 JB31030 JB31007 JB31030 JB31007 JB31030 JB31007 JB31030 JB31007 JB31030	2 1 2 1 2 1 1 1 1 1 1 1 2 1 2 1 2 1 2 1			A03008 A03009 A03010 A03011 A03012 A03013 A03014 A03015 A03016 A04001 A04002 A04003 A04003 A04004 A04005 A04005 A04006 A04007 A05007	A10003 A10002 A05012 A05013 A05002 A04016 A04015 A19009 A05007 A05009 A05006 JA31020 A10006 A05007 A16012 A04013 A04014	1 1 1 1 1 1 1 2 1 2 1 2 1 2 1 2 1 1 1 1

OR NUMBER ORIGIN DESTI- Z NATION LEVEL	
	OR NUMBER ORIGIN DESTI-
A13014 A14014 2 A13015 A13016 1 A13015 A13016 1 A14002 A02012 1 A14003 A02011 1 A14005 C25014 1 A14006 A14009 2 A14006 A15009 1 A14007 A25003 2 A14007 C05013 1 A14009 A13006 1 A14009 A13006 2 A14010 B25009 2 A14012 A02009 1 A14013 A02010 1 A14014 A15014 1 A14016 A14016 1 A15002 A01016 1 A15002 A01016 1 A15003 A01015 1 A15006 A15007 1 A15006 A15007 2 A15007 A15006 1 A15009 A14006 2 A15007 A15009 2 A15010 A19014 1 A15014 A15014 1 A15014 A15014 1 A15006 A1009 2 A15007 A15009 2 A15000 A14006 1 A15009 A14006 1 A15009 A15007 2 A15010 A19014 1 A15015 A15016 1 A15016 A15015 1 A15016 A15015 1 A15016 A15015 1 A16016 A15015 1 A16004 A23010 2 A16007 B18005 1 A16018 A24007 1 A16018 A16016 1 A16016 A16015 1	SIGNAL NAME ORIGIN DESTI- NATION LEV

TITLE LOGIC BOARD WIR	E WRAP			WL	DOCUMENT NO.	SHEET NO). 4	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	LEVEL
LOGIC BOARD WIR SIGNAL NAME OR NUMBER	A19015 A20001 A20002 A20003 A20004 A20005 A20006 A20006 A20007 A20007 A20007 A20007 A200011 A20012 A20013 A20014 A20015 A20016 A21001 A20015 A20016 A21001 A21001 A21002 A21003 A21004 A21005 A21006 A21007 A21009 A21007 A21009 A21001 A21011 A21011 A21015 A21016 A22001 A22002 A22007 A22002 A22007	D22002 A20007 A20003 A20004 A20005 A20006 A20006 A20007 A20006 A20007 A17006 A19007 A19008 A19007 A19008 A21001 B25015 A21004 A22006 A2007 A19008 A21001 B25017 A21014 A22006 A21010 A16006 A21010 A2007 A21011 B25014 A22000	LEVEL 2 1 1 2 2 1 1 1 2 2 1 1 1 1 1 1 1 1 1	S O	IGNAL NAME	A22009 A22010 A22011 A22012 A22014 A22015 A23002 A23002 A23004 A23005 A23006 A23006 A23006 A23010 A23010 A23011 A24011 A24011 A24011 A24011	4 DESTI-NATION JC3101: E1601: A1700] F1401: A2301: A2300: A2300: A2300: A2300: A2300: A2300: A2300: A2300: A2301: A2400:	D Z LEVEL 3 2 3 1 3 1 5 2 1 1 5 2 6 1 7 2 8 1 8 1 9 1 9 1 9 1 9 1 9 1 9 1
	A21016 A22001 A22002 A22002 A22003 A22003 A22004	A21014 A2200A E25002 B15006 A27004 A24010 B24003	1 1 2 1 2 1			A24011 A24012 A24013 A24014 A24014 A24015 A25002	A24002 D14004 D20006 B21007 A23016 A230002	2 2 1 5 1 5 1 5 2 2 2 2 2 2 2 2
	A22006 A22007 A22009	A21006 A21011 A22001	2			A25003 A25003 A25004	D1800= A1400 D28002	2

TITLE LOGIC BOARD WIR	E WRAP (T	B304B/C)		WL	DOCUMENT NO.	SHEET NO	D . 5	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVEL 2	OF	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	. ₹
TOEN TIPICATION	A25005 A25006 A25007 A25009 A25010 A25011 A25011 A25012 A25013 A25014 A25015 A26001 A26002 A26003 A26004 A26005 A26006 A26007	JE31043 B14004 B20004 C14007 E29014 A28013 B30006	1	101	ENTIFICATION	A28004 A28005 A28006 A28007 A28008 A28009 A28010 A28013 A28013 A28015 A29001 A29003 A29004 A29007 A29008 A29007 A29008 A29007 A29008 A29009 A29011 A29013 A29013 A29013 A29013 A29013 A29013 A29013 A29015 A30006 A30007 A30008 A30008 A30008 A30007 A30008 A30008 A30008 A30009 A30010 A30011 A30012 A30015 B01002 B01002 B01003 B01004 B01005 B01005 B01005 B01006	A2800A B20012 B14012 JE31041 A28004 A28010 B11004 B12011 A27013 B30004 A26007 A2803 A30003 B11012 A29006 A29006 A29010 JE31010 A29006 A29010 JE310107 A29008 B12004 B13011 A28013 A30013 A30006 A29006 A29010 JE310107 A29008 B12004 B13011 A28013 A30008 B14005 B12004 B130008 JE31003 JE31003 B14015 B01003 JF31022 B14015 B01007	1 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1

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B06004 B0701] 2 B08010 JB31026 1 B06005 2 B06005 B06001 2 B08012 B07012 2 B06006 JF31002 1 B08013 JF31025 1 B06007 B18004 1 B08014 JF31002 1 B06007 B18004 1 B08015 B08016 1 B08016 B08016 1 B06007 B18004 1 B08016 B08016 1 B08016 B08016 1 B06007 B06006 2 B08015 B08016 1 B08016 B08016 B08016 1 B08016 B08016
B07007 B07003 2 B09007 C20004 1 B07003 B07002 2 B09007 R09006 2 R07004 B08011 B09009 JA31004 B09005 B07005 B07004 C31022 B09011 B09009 JA31004 B07005 B07011 B09001 B09005 B09011 B09005 B09012 B08012 B09012 B08012 B09012 B08012 B09012 B08012 B09012 B08012 B09014 B09015 B09015 B09015 B09016 B0901

TITLE	, tabab (mp3	04P/C)		WL	DOCUMENT NO.	SHEET NO	8	REV.
LOGIC BOARD WIRE	WRAP (TB3	1			lonal Nane	<u> </u>	T	
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVEL .	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	LEVEL
	011004	12011				B13011	D07011	,
	B11004 B11005	A28011 B11007			-	813011	A29012	2
	B11006 B11007	C14009 B11005			-	813012 813012	D07012 A27006	
	811007	B11005				B13013	B14013	2
	811008 811008	B11007 B11009				813013 813014	B12013 F04006	
·	B11009	811008	1		•	B13015	802003	1
	B11009 B11010	B11011 D03004	2			B14001 B14002	B01007 F04004	
	B11010	B21n14	- 11		,	B14003	B19003	11_
	811011 811012	B11009 D09012				B14003 B14004	B13n07	
	811012	A29005				B14004	A27010	1
	B11013 B11013	B12013 C06004				B14005 B14005	D06005	
	B11013	F03002	1			B14006	C18010	2
	B11015	B04003				B14006 B14007	825010 808003	
	B12001 B12002	B03007 F04014				814009	F12009	1
	B12003	B13003				814010 814010	UF31025	
	B12003 B12004	B11003 D08004				B14011	D06011	2
	812004	A29011				B14011 B14012	A30012 D06012	
	812005 B12006	<u>B12007</u> B27007				B14012	A28006	
	812007	B12005				B14013 B14013	B19013 B13013	
	B12007 B12008	B1200A B12007	- 11			B14014	F04002	
	B1200A	812009				814015 815002	B01003	
	812009 B12010	B12008 B16014				B15002	C10010 B15003	2
	B12011	D08011				R15003	B15002 JF31029	
	812011 812012	A28n12 C10800	11			B15004	B15005	
	B12012	A30005	11			R15004 B15005	81000	
	812013 812013	B13013 B11013				B15005	B15011 B15004	1
	B12014	F04012				B15006	A22002 B15007	
	B12015 B13001	803n03 802007				B15007	B15006	1
	813002	F04n1n	1			B15007 B15009	JF31016 JA31036	
	R13003	B14003 B12003				B15010	JA31034	
	B13004	D07004				815011 815011	C15014 B15n0s	
	B13004 B13005	A30011 D07005	11			815012	A11014	
	B13005	A29004	- 11			815012 815013	B10012 JA31035	
	813006 B13007	F24012 B09003				815014	JA31012	
	B13009	B08007				815015 815016	B15016 B15019	
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LOGIC BOARD WIF	RE WRAP (T	B304B/C)		WL DOCUMENT NO.		9	D
OR NUMBER	ORIGIN	DESTI- NATION	Z LEVEL	SIGNAL NAME OR NUMBER	ORIGIN	DESTI- NATION	E VE
SIGNAL NAME	B16002 B16002 B16003 B16003 B16004 B16005 B16006 B16009 B16010 B16011 B16012 B16013 B16014 B16014 B16015 B16016 B17002 B17002 B17003 B17005 B17006 B17007 B17007 B17011 B17011 B17013 B17014 B17014 B17015 B17016 B17016 B17016 B17017 B17018 B17018 B17018 B17018 B17019 B18002 B18002 B18002 B18002 B18002 B18002 B18002	C02010 C14003 E16003 F28003 C03002 C16002 F17010 E20010 E30004 E26014 E26013 E20012 B16016 B16016 B16015 C13013 C20010 C19014 F20012 C12004 C12007 B18002 B18006 B16013 C14014 JD31020 C18011 JD31020 C18011 JD31021 B17016 B17016 B17016 B17016 B17017 D25002 E16002 B06007	LEVEL	SIGNAL NAME	B19003 B19013 B19013 B20001 B20002 B20003 B20003 B20006 B20006 B20007 B20007 B20007 B20009 B20009 B20009 B20010 B20011 B20012 B20013 B20013 B20014 B20015 B21002 B21002 B21002 B21003 B21004 B21005 B21006 B21007 B21006 B21007 B21009 B210109 B21011 B21012	B14003 B20013 B14013 B14013 B14013 B15007 F03010 F30012 B19003 D10004 A27011 B20007 A24004 B20009 B20008 B20008 B20008 B20009	E LEVE
	818004 818005 818006 818006 818007 818009 818011 818011 818012	806007 A16007 C20002 B17009 C20003 F30013 C07004 C20007 A19013 F01003	1 1 2 1 2 1 1 1 2 1 1 2	 	_		1 2 2 1 1 2 1 2 1
	818013 818014 818015 818016 819003	JF31009 B26012 B18016 B18015 B20003	1 1 1	-	B22010 B22011 B22011 B22012 B22013	C27004 C23011 B23014 F10012 C22003	

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TITLE LOGIC BOARD WIRE	WRAP (TB3	04B/C)		WL DOCUMENT NO.	SHEET NO	11	REV.
SIGNAL NAME OR NUMBER	ORIGIN	DESTI- NATION	EVEL		ORIGIN	DESTI- NATION	₹ LEVEL
IDENTIFICATION	828010 828011 828012 828012 828013 828013 828013 828013 829001 829003 829004 829005 829006 829007 829007 829012 829012 829012 829013 829014 830001 830002 830002 830002 830003 830004 830005 830005 830006 830006 830007 830007 830007 830007 830007 830007 830007 830007 830007 830007 830007 830007 830007 830007 830007 830005	D07009 B30014 D07007 B30013 D08009 B30012 C10013 B29009 B29001 B29001 B28002 B29001 B28015 A26014 A26015 A26016 A26001 A26001 B28002 B29011 B28010 B28010 B28010 B28010 B28010 B28010 B28010 B28010 B28005 A27015 B28004 A27001 B28003 F15007 A26009 A26010 C28004 A27001 B28003 F15007 A26009 A26010 C28004 B28011 B28004 B28011 B28004 B28011 B28004 B28011 B28004 B28011 B28004 B28011 B28011 B28004 B28011 B28004 B28011	2 1 2 1 2 1 2 2 2 2 2 2 1 1 1 1 1 2 1 2	IDENTIFICATION	C01006 C01007 C01009 C01010 C01011 C01012 C01013 C01014 C01014 C01015 C01016 C02002 C02003 C02003 C02004 C02005 C02006 C02006 C02007 C02009 C02010 C02011 C02012 C02013 C02013 C02013 C02014 C02015 C02016 C03002 C03003 C03004 C03005 C03006 C03007 C03009 C03007 C03009 C03010 C03011 C03012 C03011 C03015 C03016 C03015	CO100: CO101: CO101: CO101: CO101: CO100: B2300: CO900: CO100: CO101: CO901: CO101: CO901: CO400: D1301: B1600: J23100: C0800: C1400: J53102: C0800: C1400: C14	3 1 1 1 4 1 5 2 3 1 9 1 3 2 1 1 1 1 2 2 3 1 9 1 1 1 2 2 3 1 9 1 1 1 2 2 1 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 1 3 1 4 2 1 1 1 1 1 2 1 2 1 3 1 4 1 4 1 4 1 5 1 6 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7

TITLE	(mp	2045 (a)		WL	DOCUMENT NO.	SHEET NO		REV.
LOGIC BOARD WIRE	E WRAP (TB	304B/C)	T a				12	D
OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	LEVEL
				,				
	<u> </u>	C13014			_	C07003	F09005	2
	C04003 C04004	C01013 B22014				C07004	B18009	1
	C04005	C06013	1		_	C07004 C07005	C07012 C07011	2
	C04006 C04007	C09013 C06006			_	C07005	C07002	_1_
	C04009	E12014				C07006 C07007	F24014 F23003	1
	C04010 C04011	C03005 C09006			_	C07010	F09011	i
	C04011	D12010			-	C07011 C07011	C15013 C07005	2
	C04012	E12004	1		_	C07012	C07004	5
	C04013 C04013	C09003 C19014	31			C07012	F14004	1
	C04014	C03007	1			C07013 C07013	F24011 D12011	<u>2</u> 1
	C04014 C04015	F05005 C04016				C07014	F26011	2
	C04015	C04015	11			C07014 C07015	E11011 C07016	1
	C05002	JC31021	1		_	C07016	C07015	1
	C05003 C05004	B21002 F19012			_	C08002	D20003	<u>2</u> 1
	C05005	D13002				C08003	C08008	i
	C05006 C05007	E02001 E03004	1			C08004	C10006	2
	C0500A	C31046	2			C08004 C08005	C02007	1
	C05009 C05010	D25005 D14006			-	C08006	C13010	2_
	C05010	F26011	<u>1</u>			C08006 C08007	C13011 C02011	1
	C05011 C05012	C24003			-	C08008	C08003	1
	C05012	C18n04 A14n07			-	C08010 C08011	C09012 D03012	<u></u>
	<u>C05014</u>	A19011	2			C08012	C06009	_i_
	C05015 C05016	C05016 C05015	11			C08013	C14004	1
	C0600S	C03012	1			C08014 C08015	F15009 C08016	1 1
	C06002	C17003 C16014	2		-	C08016	C08015	1
	<u> </u>	B11013	- 11			C09002	F05n07 C09n07	1
	C06005 C06006	C11003			-	C09003	C04013	2
	C06008	<u>C04007</u> F05013			-	C09004 C09004	C01012 D14012	
	<u> </u>	C08012				C09005	C22011	_i
	C06010 C06011	C13007 B26006				C09005 C09006	E26003 C04011	2
	C06012	JD31n45	1		-	C09007	C09003	1
	<u> </u>	C04002 C04005			-	C09009	C12005	1
	C06014	D25006	_1_			C09010 C09011	C02n02 C16n07	1
	C06015 C06016	C06016 C06015			-	C09012	C08n1n	1
	C07002	C07005			-	C09012 C09013	E19003 D17007	<u>2</u> 1
	C07002	C09014			-	C09013	C04006	2
	C07003	JD31n33	1			C09014	C07002	2
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TITLE LOGIC BOARD WIRE	E WRAP (TB	304B/C)		WL	DOCUMENT NO.	SHEET NO	D. 13	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	Of	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
IDENTIFICATION	C09014 C09015 C09016 C10007 C10003 C10004 C10007 C10009 C10010 C10011 C10012 C10013 C10014 C10015 C10016 C11001 C11001 C11002 C110016 C11006 C11006 C11007 C11008 C11008 C11008 C11008 C110108 C11011 C11012 C11013 C11014 C11015 C11016 C11016 C11017 C11008 C11008 C11018 C11019 C11009 C12009 C12019	C10011 C09016 C09015 C02004 C11012 D16009 C10011 C08004 C13006 F02004 B15002 C09014 C10005 C23012 E26002 B28013 D08003 C19009 C10016 C10015 C11004 B21013 C11004 B21013 C11006 C06005 E06014 C11001 F19007 C18014 C11008 C11008 C11016 C11017 C11011 C10003 C11018 C11008 C11011 C11007 C12006 C19005	1 1 1 2 2 1 1 1 1 1 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 2 1 1 1 2 1 1 2 1 1 1 2 1 1 1 1 2 1	- ID	ENTIFICATION	C12010 C12011 C12011 C12016 C13002 C13003 C13003 C13006 C13006 C13006 C13007 C13009 C13010 C13011 C13011 C13013 C13014 C13015 C13016 C14003 C14003 C14003 C14005 C14005 C14006 C14007 C14007 C14009 C14010 C14010 C14011 C14012 C14013 C14014 C14015 C14016 C15002 C15006 C15007 C15010 C15010 C15013 C15013 C15013	E20004 018012 E15006 C12016 C12016 C12016 C12016 C14005 D18010 E16014 C10007 E13013 C06010 C08006 C14002 C13014 B17002 C13013 C13016 C13015 C13011 B16002 E13014 C13016 C13017 C14010 B17013 C14016 C	2 2 1 1 1 2 1 2 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 1 2 1

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TITLE LOGIC BOARD WIRE WRAP (TB3	04B/C)	WL DOCUMENT NO.	SHEET NO	D. 15	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	DESTI- # LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
C21013 C21014 C21015 C21016 C22002 C22003 C22003 C22003 C22005 C22006 C22006 C22006 C22007 C22010 C22011 C22011 C22011 C22012 C22013 C22014 C22014 C22015 C22016 C22006 C23002 C23003 C23003 C23004 C23004 C23004 C23007 C23009 C23001 C24003 C24003 C24003	C26004 2 D23012 1 C22002 2 C21016 1 C21015 1 C21014 2 C21010 1 B22013 2 C21004 1 C15010 1 C22011 2 C22011 2 C22013 1 C24011 2 C17010 1 C24002 1 C22005 2 C09005 1 D24012 1 C22006 1 D24012 1 C22006 1 D24011 2 D23009 1 C21002 2 C22016 1 C22015 1 C25009 1 C23014 2 C23013 2 E26007 1 C16009 1 D29012 2 C19011 1 F18011 2 F19004 1 F15005 1 F20005 1 C21011 2 B22011 1 C28004 2 C10012 1 D29010 1 C23003 2 C23002 2 D29014 1 C23003 2 C23002 2 D29014 1 C23003 1 C23003 2 C23002 2 D29014 1 C23016 1 C23016 1 C23017 2 E16007 1 D29010 2 D18009 1 C23010 2 D18009 1 C33010 2		C24006 C24007 C24009 C24010 C24011 C24014 C24015 C24016 C25002 C25003 C25006 C25006 C25006 C25007 C25010 C25011 C25012 C25013 C25014 C25015 C25016 C25016 C26003 C26003 C26004 C26005 C26007 C26007 C26010 C26011 C26012 C26012 C26013 C26016 C27007 C26016 C27007 C27003 C27004 C27007 C27009 C27010 C27011 C27011 C27011	E16005 E05011 C25010 E15013 C22006 C03014 C24016 C24015 JD31028 E13010 D24004 D24005 B26002 E12003 C26003 C23002 C24009 B26003 JC31037 A26002 A14005 C25016 C25016 C25016 C25016 C25017 F13003 C21013 C27011 C26014 D15003 C21013 C27011 C26014 D15003 C26008 C25005 D29002 C26016 C26005 D29002 C26016 C26015 D21013 B26004 B27006 B27005 C26005 E15009	1 1 1 1 2 2 1 2 1 1 1 2 1 1 1 1 1 1 1 1

TITLE (TD204D (G)	DOCUMENT NO. SHEET NO.	REV.
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OR NUMBER ORIGIN DESTI- Z IDENTIFICATION LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION ORIGIN DESTI- NATION	Z LEVEL
SIGNAL NAME	SIGNAL NAME ORIGIN DESTI-	D Z
C29003 C29013 2 C29004 A25007 1	D01010 D07001 D01011 JE31013	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

ITLE LOGIC BOARD WIR	E WRAP (TB3	04B/C)		WL	DOCUMENT NO.	SHEET NO). 17	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL	0	IGNAL NAME R NUMBER PENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	D03007 D03008 D03009 D03010 D03010 D03013 D03013 D03013 D03013 D03015 D04001 D04002 D04003 D04004 D04005 D04005 D04006 D04006 D04007 D04008 D04008 D04008 D04010 D04011 D04012 D04013 D04014 D04014 D04015 D04015 D04016 D05001 D05010 D05011 D05011 D05011 D05015 D05011 D05015 D05015 D05011 D05015 D05011 D05015 D05015 D05011 D05015 D05015 D05015 D05015 D05015 D05015 D05011 D05011 D05015	D04002 D03003 D03003 D14002 C08011 D12013 D13013 D04004 D04002 D04004 D04003 D03007 D04006 D05007 D04006 D05007 D04006 D05007 D04006 D05007 D04006 D04010 D04011 D04012 D05006 D04010 D04014 B21009 D03014 D04012 D05006 D04010 D04011 D04012 D05006 D04010 D04011 D04012 D05006 D04010 D04011 D04012 D05006 D04010 D04011 D04011 D04012 D05006 D04010 D04011 D04008 D05008 E1013 D05008 E101009	1			D06003 D06004 D06004 D06005 D06006 D06007 D06009 D06010 D06011 D06012 D06013 D06013 D06013 D06015 D07001 D07003 D07003 D07004 D07005 D07005 D07005 D07007 D07007 D07011 D0	D12004 D07003 E08013 B14004 E07010 B29013 B29012 E23014 F16013 B14012 D12007 D07013 D01002 D01010 D06003 D08003 E08015 B13004 E06009 B13005 E23011 B28011 F20002 B28012 D21006 D21006 E07003 B13011 E08014 H13012 D06013 D01006 D01014 D07003 D09003 E09013 B10006 E07003 E09013 B10006 E07003 E09013	1 1 2 2 1 1 2 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1

LOGIC BOARD WIRE WRAP (TB304B/C)	TITLE				1 tare	DOCUMENT NO.	SHEET NO).	REV.
OR NUMBER DRIGIN DESTI EVEL OR NUMBER DESTINATION LEVEL	LOGIC BOARD WIR	E WRAP (TB3	04B/C)		WL	DOGGINZINI ING.	J. S. LET ING		D
	OR NUMBER	ORIGIN			0	R NUMBER	ORIGIN		Z LEVEL
	OR NUMBER	D08012 D08013 D08013 D08013 D08015 D09001 D09003 D09004 D09005 D09007 D09009 D09010 D09011 D09012 D09013 D09013 D09013 D09013 D09015 D10001 D10005 D10006 D10006 D10007 D10008 D10008 D10009 D10012 D10013 D10015 D10009 D10010 D10017 D10008 D10005 D10006 D10007 D10008 D10007 D10008 D10009 D10010 D11001 D11001 D11001 D11001 D11001 D11001 D11010	B12012 D07013 D09013 D09013 D01012 D02004 D08003 D10003 E09015 F27014 F27015 D25010 F29007 E09014 B11012 D08013 D10013 D02002 D02010 D09003 E10013 B20004 F29013 B20004 F29013 D25012 D10008 D10007 D10008 D25011 F29003 E10013 D02006 E10009 F28005 E05002 D11006 D11006 D11007 D11006 D11007 D11007 D11007 D11007 D11010	LEVEL 2 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 1 1 2 1 1 1 1 1 2 1	0	R NUMBER	011013 011015 012002 012003 012004 012005 012005 012006 012007 012009 012010 012011 012013 012014 012015 012013 012014 012015 013002 013003 013004 013005 013006 013007 013009 013010 013010 013011 01	D11008 E10007 JD31017 D12005 D06003 D16004 D12003 JD31037 D06013 F05003 C04011 C07013 E05014 D03013 D20004 F21004 D12015 E16006 C05005 D20004 E19011 D13005 D13000 F30003 E10011 D14014 D19009 C16003 D20006 F29014 B21012 D03013 D14007 C02010 D13016 D13015 D14003 D03011 D18003 D14007 C02010 D13016 D13015 C05010 D13014 C05010	LEVEL 1 1 1 2 2 1 2 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 2 1 1 2 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1

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LOGIC BOARD WIR	E WRAP (TB3	04B/C)		WL	DOCUMENT NO	. SHEET N	19	REV.
OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	OF	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	EVI
	D14013 D14014 D14014 D14015 D14016 D15002 D15003 D15004 D15005 D15006 D15007 D15009 D15010 D15011 D15011 D15011 D15012 D15013 D15014 D15015 D15016 D16002 D16003 D16004 D16005 D16006 D16007 D16009 D16010 D16011 D16011 D16011 D16011 D16015 D16016 D17002 D17003 D17006 D17007 D17009 D17010 D17011 D17012 D17013 D17014 D17015 D17015 D17016 D17017 D17019 D17017 D17019 D17010 D17011 D17011 D17015 D17016 D17017 D17016 D17017 D17019 D17016 D17017 D17019 D17010 D17011 D17015 D17016 D18003	E15014 D13009 D20002 D14016 D14015 D17009 C26007 C18012 D17013 B22002 B23003 D19013 E07014 D15012 C24004 C19004 D15015 D15013 D15016 D15015 F23010 F25007 D12005 D17003 D15011 C16005 C10004 C13009 C02004 E14004 E18002 E24013 E18001 D16016 D16015 F16014 D16015 F16014 D16005 C20013 D15002 C17004 B27014 JE31045 D15005 F09012 D17016 D	1 2 1 1 1 1 2 2 1 1 1 1 1 1 1 1 1 1 1 1		E54018	D18004 D18005 D18006 D18007 D18009 D18010 D18011 D18012 D18013 D18014 D18015 D19002 D19003 D19003 D19004 D19009 D19010 D19010 D19011 D19011 D19011 D19012 D19013 D19013 D19014 D19015 D19016 D20002 D20003 D20004 D20004 D20004 D20005 D20006 D20006 D20010 D20011 D20012 D20013 D20011 D20012 D20013 D20014 D20015 D20016 D20015 D20016 D20015 D20016 D20017 D20017 D20018	C01004 A25003 B27009 D18014 C24003 C13003 E15003 JC31014 C12011 C29013 D18007 D18016 D18015 D27009 C01013 A12010 E30010 F08013 D13010 A13005 D19011 D19010 E30014 D19010 E30014 D19010 E30014 D19013 JE31020 D19012 C30007 D19016 D19015 D14014 E12017 C08002 D20006 D12013 D13003 A24013 D20003 D13011 E20002 E20003 C11015 E18002 D18002 F22009 JD30016 D20015 E21013 D24003	1 1 1 1 1 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 2 1 1 1 1 2 2 2 1 1 1 1 2 2 2 1 1 1 1 2 2 2 1 1 1 1 2 2 2 1 1 1 1 2 2 2 1 1 1 1 1 2 2 2 1

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	D21004 D21005 D21006 D21006 D21007 D21010 D21010 D21010 D21011 D21012 D21014 D21014 D21015 D21016 D22002 D22002 D22003 D22004 D22005 D22006 D22007 D22006 D22007 D22009 D22011 D22011 D22012 D22011 D22012 D22013 D22014 D22015 D23006 D23007 D23007 D23007 D23009 D23009 D23009 D23009 D23001 D23011 D23012 D23013 D23009 D23010 D23011 D23012 D23013 D23014 D23015 D23016 D23007	E22014 D08010 D24002 E23013 D07010 D23006 E23013 D06006 E24011 C27002 E19004 D24016 D21016 A19016 D22003 D22006 D2300 D2301	LEVEL 1	0	R NUMBER	D24003 D24003 D24005 D24005 D24005 D24006 D24007 D24010 D24011 D24012 D24013 D24014 D24015 D24016 D25002 D25002 D25003 D25006 D25005 D25006 D25006 D25006 D25010 D25011 D25013 D25015 D26007 D26007 D26007 D26007 D26007 D26007 D26010 D26011 D26011 D26015 D26016 D26010		LEVEL 2 2 1 4 1 5 1 3 2 1 1 2 1 3 1 7 1 7 3 2 2 1 6 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7

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SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVE
SIGNAL NAME OR NUMBER	T	DESTI-	LEVEL 1 3 1 3 1 3 1 3 1 3 1 3 1 3 1 3 1 3 1	SIGNAL NAME OR NUMBER	D29016 D30009 D30015 D30016 D31030 D31038 D31038 D31038 D31036 E01001 E01001 E01003 E01004 E01005 E01006 E01007 E01007 E01009 E01010 E01011 E01001	DESTI-	Z LEVE

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	E08015	D07004	<u>1</u>		E11014	E11013	
	E09001	E04010			E11015 E11016	Ellal6 Ellal5	1 1
	E09002	E04009 E04007			E12005	JC31025	1
	E09003	E02011			E12003	C25006	1
	E09004	E04006	- 11		E12003 E12004	E11003 C04012	2 1
	E09004 E09005	E02012 E04001	2		E12005	E11007	1
	E09006	E04015	2		E12005 E12006	F10003 JC31001	
	E09007 E09007	E04003 E01009			E12007	F05002	1 1
	E09009	E040.02			E12009	F27001	ì
	E09009	E02013	2		<u>E12010</u> E12011	E12011 C17002	<u> 1</u>
	E09010 E09010	E08010 E10010			E12011	E12010	1
	E09011	E10011	2		E12012	D20002	1
	E09011	E08011	1		<u> </u>	E24010 F10002	
	E09012 E09013	D08012 D08004	1		E12014	C04009	
	E09014	D09012	- 1		E12015	E12016	1
	E09015	D09004	<u> </u>		E12016 E13002	E12015 E15002	2
	E10005	E10002	1		E13002	E13011	ī
	E10002	E10003	2		F13003	E15006	2
	F10003 E10003	E10002 E10004	<u> </u>		E13004 E13005	<u> </u>	
	E10003	E10004	1		E13005	F26006	1
	E10004	E1000A	2		F13006	F17n03	
	E10005 E10006	E05001 E05015	2		<u>E13007</u> F13009	F16004 F17005	1
	E10007	D11015	i		E1301n	C25003	1
	E10008	E10004	2		E13011 E13011	E13002 E02004	1 2
	E10009 E10010	D11001 E09010	$-\frac{1}{1}$		F13012	D25003	1
	E10011	D13007	i		E13013	C13006	_2_
	E10011	E09011	2		E13013 E13014	F13n1n C14n03	1
	E10012 F10013	D10012 D10004	1		F13014	F23n1n	s
	E11002	C26010	1		E13015	E13016	
	E11003	E12003			F13016 E14002	E13015 E23001	
	E11004 E11004	D28011 F13013			E14002	E14005	2
	E11005	C15n11	_2		F14003 E14004	F05012	
	E11005 E11007	E18003 E12005			E14004 E14005	D16011 E14002	
	E11007	E29002			E14005	E25005	1
	E11011	C07014	1		<u>F14006</u> F14010	<u>C12006</u> B07002	1
	E11011 E11012	F14011 D05012	2		E14011	F28004	<u>i</u>
	E11013	E11014			E14011	C15001	
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	E14013 E14014	E14012 A25012		-	E17012 E17013	E16004 E16002	1
	E14015	E14016		_	E17014	E18013	i
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	E15002	E20004 E13002	1 2	_	E18001 E18001	E19006 D16014	2
	E15003	D18011	2	_	E18002	D16012	1
	E15003	E02007	1		E18002	D20012	5
	E15004 E15005	E02006 E20005	1		E18003 E18003	E11005 E18004	2
	E15005	F26003	- 11	nece .	E18004	E18003	2
	E15006	C12011	1		E18004 E18005	E18005	1
	F15006 F15007	E20012	2	-	E18005	E18004 E18006	2
	E15009	C27012	- 11		E18006	E18005	2.
	E15010	F10013	1		E18004 E18007	E18007	1
	E15010 E15011	7D31020	2	-	£18007	E18006 E18009	2
	E15012	C17011	_i	_	E18009	E18007	2
	E15013	F05004	1		E18009 F18010	E18010 E18009	1
	E15013 E15014	C24010 D14013	2	-	E18010	E19014	5
	E15014	F05006	i!		F18012	E17001	1
	E15015	E15016	1		E18013 E18014	E17014	1 1
	E15016 E16002	E15015 E17013		_	E19002	E17015 E19005	2
	F16002	818003	I t	-	E19002	E25001	1
	E16003	B16003	1		E19003 E19004	C09012 E24012	2
	E16004 E16004	E17012 E19012	2	-	E19004	D21014	5
	F16005	C24006	1	-	£19005	E19014	1
	E16006 E16006	E17011 D13002	1 2		E19005 E19006	E19002 E18001	2
	F16007	C24004	<u></u>	-	F19009	E19n13	- i
	E16009	JF31003		-	E19010	F23014	1
	F16010 F16010	E17010 F14014	1 2		E19011 E19012	D13n03 E16004	5
	E16011	C03011	1	-	E19012	F24006	1
	E16012	E17009		-	E19013	E19009	1
	E16013 F16013	F15014 A22010	1 2		F19014 E19014	E18010 E19005	2
	E16014	E17005		-	E19015	E19016	1
	<u> E16014</u>	C13005		-	E19016	E19015	
	E16015 · E16016	E16014 E16015			E20002	D20007 816014	1
	E17001	E18012			E20003	D2011n	1
	E17002	B25004	_1		E20004 E20004	C12010 E15002	
	F17005 E17005	E16014 F12006	11		E20005	E15007 E15005	1
	E17009	E16012		,	E20006	JC31010	1
	E17010	E16010			E20007 E20009	C03002 C17014	
	E17011	E16006	1		620009	C17014	1
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TITLE LOGIC BOARD WIRE W	RAP (TB	304B/C)		WL	DOCUMENT NO.	SHEET NO	25	REV.
SIGNAL NAME OR NUMBER OF IDENTIFICATION	RIGIN	DESTI- NATION	₹ LEVEL	O	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	E20010 E20011 E20012 E20013 E20014 E20015 E20016 E21001 E21002 E21003 E21004 E21005 E21006 E21007 E21007 E21010 E21010 E21010 E21011 E22001 E22002 E22003 E22004 E22004 E22005 E22007 E22007 E22007 E22007 E22007 E22007 E22010 E22011 E22017	B16007 E02005 E15007 E28004 F18014 B16015 E20016 E20015 E26006 E22001 E22002 F30002 E21005 E21006 E21005 E21006 E21007 E21006 E22009 E22007 E21007 E22006 E22008 E23007 E21007 E21007 E21007 E21007 E21007 E21007 E22006 E22008 E23007 E21007 E21007 E21007 E21007 E21007 E21007 E22006 E22008	1			E23003 E23004 E23004 E23005 E23006 E23007 E23007 E23009 E23009 E23010 E23010 E23011 E23013 E23014 E23015 E24001 E24002 E24003 E24003 E24004 E24005 E24006 E24006 E24007 F24007 E24007 E24007 E24007 E24009 E24010 E24011 E24012 E24011 E24012 E24013 E24011 E25001 E25002 E25003 E25004 E25005 E25005	E23004 E23003 E23006 E23006 E23006 E23006 E24009 E24001 E24001 E24001 E24001 E24001 E24001 E24001 E24001 E24006 E25006 E2	1 2 2 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1

TITLE LOGIC BOARD WIRE	WRAP (TB	304B/C)		WL	DOCUMENT NO.	SHEET NO). 26	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	0	GNAL NAME R NUMBER	ORIGIN	DESTI- NATION	Z LEVEL
IDENTIFICATION	E25006 E25007 E25007 E25008 E25009 E25010 E25010 E25015 E26001 E26002 E26003 E26003 E26004 E26006 E26006 E26007 E26007 E26007 E26007 E26010 E26017 E26010 E27001 E27001 E27002 E27003 E27003 E27004 E27005 E27006 E27007 E27008 E27009 E27010	E25003 E25001 E25010 E25010 E25010 E25010 E25004 F30004 E25007 E24003 E24003 E24003 E26004 E26004 E26006 E26006 E26006 E26006 E27010	2 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1		ENTIFICATION	E28005 E28005 E28006 E28009 E28011 E28012 E28013 E28014 E28014 E28015 E28016 E29007 E29003 E29004 E29007 E29010 E29010 E29011 E29011 E29011 E29011 E29011 E29012 E29013 E29014 E29016 E30002 F30002 F30002 F30006 E30006 E30006 E30007 E30006 E30007 E30007 E30009 E30010 E30010 E30016 E31030 E31038 E31038 E31046 F31046	E28014 E27003 F18002 B27013 E23009 E27001 E27002 A25011 E23010 E28015 E11009 F14009 F18005 E24007 B27002 F24003 E24009 F10007	2 1 1 1 1 2 2 2 1 1 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 1 2 1 1 1 2 1

TITLE	LOGIC BO	ARD WIRE WR	AP (TB3041	B/C)	WL	DOCUMENT NO.	SHEET NO). 27	REV.
	SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	0	IGNAL NAME OR NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
		F01004 F01005 F01006 F01007 F01008 F01009 F01010 F01011 F01012 F01013 F01013 F01014 F01015 F01015 F02002 F02003 F02004 F02005 F02005 F02006 F02006 F02007 F03007 F03007 F03007 F03007 F03011 F03011 F03011 F03011 F03012 F03017 F0	D22003 JF31007 F03013 F04009 F04007 F04007 F04007 F03009 F01015 F01014 F02008 F06003 F06008 C10009 F06013 F01013 F02016 F02015 B1014 F06003 B11002 F06006 B20014 F01011 B20002 F06006 JF31001 F01016 F03015 B14014 F06015 B14002 F06006 JF31001 F01006 JF31001 F01006 JF31001 F01006 F03015 B14014 F06015 B14002 F06004 B13002 F06004 F03015 B14002 F06006 F04015				F05002 F05003 F05004 F05005 F05006 F05009 F05010 F05012 F05013 F05014 F05015 F05016 F06007 F06003 F06004 F06005 F06007 F06008 F06007 F06008 F06007 F06008 F06013 F06013 F08013 F08010 F08011 F08015 F08011 F08011 F08011 F08011 F08011 F08011 F08011 F08013 F08013 F08013 F08014 F08016 F08017 F08011 F08017 F08011 F0	E12007 D12009 E15013 C04014 E15014 C07002 F31046 F09014 C29011 F19013 E14003 C06007 C13002 F05016 F05015 F03003 F04011 F04013 F02005 F04003 F27009 F08011 E27007 F10013 E30004 D14012 C26012 F09013 F08016 F08016 F08016 F08017 F15014 JC31013 F15014 JC31013 F14002 C07003 JD31009 F24011 F24010 F24010 F24010 F24010 F24010 F24010	1

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OR NUMBER IDENTIFICATION	F09012 F09013 F09013 F09014 F09015 F09016 F10006 F10006 F10007 F10009 F10013 F10013 F10013 F10013 F10014 F10015 F10016 F12002 F12003 F12004 F12006 F12009 F12010 F12011 F12015 F12016 F13002 F13003 F13004 F13005 F13007 F13007 F13009 F13010 F13011 F13012 F13013 F13014 F13015 F13007 F13009 F13009 F13000 F13010 F13011 F13012 F13013 F13014 F13015 F13016 F14002 F14003 F14003 F14003 F14004 F14005	DESTI-NATION D17014 F08010 B24002 F05009 F09016 F09015 E12013 E12005 F15003 E30003 D08009 F20014 E29010 C29014 B27004 F20007 B22012 F08003 E15010 E24014 F10016 F10015 F16003 JD31005 F26010 E17005 B14009 F21005 B14009 F210005 B14009 F210005 B140009 F210005 B140009 F210005 B140009 F2100009 F21000		01		F14007 F14009 F14011 F14013 F14013 F14013 F14014 F14016 F15002 F15003 F15003 F150004 F15006 F15007 F15000 F15010 F16003 F16004 F16005 F16006 F16007 F16008 F16007 F16008 F16009 F16010 F16010 F16010 F16010 F16010 F16010 F16010 F16010 F16010 F160010 F16001	F09010 E29003 E11011 A22012 F24009 F14003 E16010 F14016 F14016 F14015 C02009 F10004 F15006 E30005 A25010 C23007 F15003 C03014 B30009 C08014 B26006 F15011 F15010 E16013 F09002 F15016 F15015 F16006 F15017 F15010 E10017 E10007 F15010	

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	F17010	B1600			F20012	B17003	
	F17011 F17011	F2100:	• 11		F20013 F20014	F25002	
	F17012	F17014 F3001			F20014	F10006 F20016	<u>2</u>
	F17013	F1900	- 1		F20016	F20015	
	F17014	F1701	- 11		F21001	F28n07	_
	F17015 F17016	F17016			F21001 F21002	C11010 C00002	
	F18002	E2800	. 11		F21002	F21003	
	F18003	F0900	3 2		F21003	F21002	2
	F18003	F2501			F21003	F17011	
	F18004 F18005	F18004 E29004	31		F21004 F21005	D12n14 F28002	1
	F18005	F18n0			F21005	F12010	5
	F18006	F1400			F21006	F22015	1
	F18007	F1900			F21007	F22002	2
	F18009 F18010	F1901:			F21009	F19011 A25013	
	F18010	F1801	4 2		F21010	F21011	
	F18011	C23n0	- 11		F21010	E14012	1
	F18011 F18014	F13004			F21011 F21011	C15006 F21010	
	F18014	E2001	11		F21017	F30014	
	F18015	F1801	6 1		F21014	F22n13	
	F18016	F1801			F21015	F22004	
	F19002 F19003	A1301 D2601			F22001 F22002	F22007 F22001	
	F19004	C2300			F22002	F21007	
	F19005	F1301			F22003	F22004	1
	F19006 F19006	F1800 F1701			F22004 F22004	F22n03 F21n15	
	F19007	C1100			P22004	F 21 015	
	F19009	E2700	2 1	PE54029	F2200 7	F22008	1
	F19010 F19011	F1800 F2100		PE54029	F22008	F22007	1
	F19012	C0500		PE54018 PE54029	F22009 F22010	D20013 F26012	2
	F19013	F0501		FEJ4029	F22013	F21012	
	F19014	D2801			F27014	F22016	
	F19015 F19016	F1901 F1901			F22015	F21006	
	F20002	D0700			F22016	F22014	
	F20003	E2901	1 2		F23002 F23002	F09009 F24013	
	<u>F20003</u> F20004	B2601			F23003	C07007	1
	F20004 F20004	E2201 D0800			F23004	813010	_
^	F20005	C2300			F23005 F23005	JC31007 F23011	
	F20006	D2901			F23006	E05013	
	F20007 F20009	F1001 E0700			F23007	C24nn5	1
	F20009	JE3104			F23009	E01001	
	F20010	C1900	3 2		F2301n F2301n	E13014 D16002	
	F20011	C2001	ا ج		F23011	F23005	2
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					F23013 F23014	JC31032 E19010	
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TITLE LOGIC BOARD WIRE	WRAP (TE	304B/C)		WL	DOCUMENT NO	SHEET NO) . 30	REV.
SIGNAL NAME OR NUMBER	ORIGIN	DESTI- NATION	Z LEVEL	01	GNAL NAME R NUMBER	ORIGIN	DESTI-	J ₹ LEVEL
PE54029	F23016 F24002 F24003 F24004 F24005 F24006 F24007 F24009 F24011 F24011 F24011 F24012 F24013 F24014 F24016 F25002 F25003 F25006 F25007 F25009 F25010 F25011 F25011 F25012 F25013 F25014 F25015 F26007 F2	F23015 B25009 E29007 JF31009 JC31029 E19012 C17013 F14013 F09011 F09007 C07013 B13006 F23002 C07006 F24016 F24016 F24013 C28003 D22011 D22006 JD31040 D16003 D05001 F18003 D25011 F18003 D25016 F25016	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		ENTIFICATION	F27010 F27014 F27015 F28002 F28003 F28004 F28005 F28006 F28007 F28009 F28010 F28011 F28015 F28016 F29002 F29003 F29004 F29005 F29007 F29007 F29007 F29007 F29007 F29010 F29011 F29011 F29011 F29011 F29012 F29010 F29010 F30002 F30002 F30002 F30003 F30004 F30005 F30006 F30007 F30009 F30010 F30011 F30011 F30011	E26002 D09007 D09009 F21005 B16003 E14011 F17006 D11003 F26012 F28007 E01014 E02014 F28016 F28016 F28016 F29004 F29008 F29008 F29008 F29008 F29008 F29008 F29008 F29008 F29011 D27012 D09005 F29011 D27012 D19005 F29016 F29016 F29016 F29016 F29016 F29017 D10005 D13011 C30002 F29016 F29016 F29017 D10005 D13011 C30002 F29016 F29016 F29016 F29016 F29016 F29017 D10005 D130002 F30003 F30003 F30003 F30003 F30003 F30003 F30003 F30003	1 1 1 1 2 1 1 1 2 1 1 1 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1

TITLE LOGIC BOARD WIR	E WRAP (TB	304B/C)		WL	DOCUMENT NO.	SHEET NO	O. 31	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	F30013 F30014 F30014 F30015 F30016 F31026 F31027 F31028 F31028 F31030 F31032 F31033 F31033 F31034 F31035 F31036 F31036 F31037 F31038 F31037 F31038 F31040 F31041 F31041 F31042 F31042 F31044 F31045 F31045 F31045 F31045 F31046 JA31001 JA31002 JA31006 JA31007 JA31009 JA31009 JA31009 JA31009 JA31011 JA31011 JA31011 JA31011 JA31011 JA31011 JA31011	A02004 B10014 A08002 JC31035 JA31032 A04003 A03007 B15014	1 2 1 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1 1 1 2 2 2 1			JA31014 JA31015 JA31017 JA31018 JA31019 JA31020 JA31020 JA31022 JA31023 JA31024 JA31025 JA31026 JA31027 JA31028 JA31036 JA31037 JA31038 JA31036 JA31037 JA31038 JA31040 JA31041 JA31041 JA31041 JA31041 JA31041 JA31042 JA31044 JA31045 JA31040 JA31041 JA31045 JA310101 JA310101 JA31011 JA31011 JA31011 JA31011 JA31011 JA31011 JA31011 JA31011 JA31012 JA31013 JA31011 JA31011 JA31011 JA31011 JA31012 JA31013 JA31014 JA31015 JA31017 JA31018 JA31019	B15010 A01001 JA31037 JA31041 A02006 A02007 JA31041 A05008 A03006 A01004 A08004 B06010 B09010 A02008 A03008 B15013 A01002 A02008 A03008 A01003 A01002 A02008 A03008 B15013 A01002 A02008 A03008 B15013 A01002 A02008 A03008 A03008 A04008 B09014 B08009 B09014 B01009 B09014	

JR31022 B05009 1 JD31003 D02009 JR31023 A04006 1 JD31004 D02011 JR31024 B07010 1 JD31005 F12003 JB31025 B08013 1 JD31006 D26005	D
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TITLE LOGIC BOARD WIRE	E WRAP (TB	304B/C)		WL	DOCUMENT NO	SHEET N	O . 33	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL E	(SIGNAL NAME OR NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	JF31028 JE31032 JE31033 JE31034 JF31035 JE31036 JF31036 JF31041 JE31042 JE31044 JE31045 JF31007 JF31007 JF31007 JF31007 JF31007 JF31010 JF31010 JF31011 JF31011 JF31011 JF31017 JF31018 JF31029 JF31029	E07005 C18007 E03010 E07004 C18003 D26009 A28009 E06005 D01013 A28007 D02003 A27009 F20010 D17012 F03012 B06006 F03014 E16009 C02005 B05006 F13013 B26005 F01005 F24004 B18013 B15007 F30011 B03006 B03002 A26003 B05002 A26003 B05002 B04006 B04006 B04002 B23004 B01006 B01002 B02003 B14010 B15003	1					

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SIGNAL NAME		DESTI-	Z	SIGNAL NAME		DESTI-	T
OR NUMBER IDENTIFICATION	ORIGIN	NATION	LEVEL	OR NUMBER IDENTIFICATION	ORIGIN	NATION	L
	401001	JA31015	- 11		A03008	A07006 A12012	
	A01001 A01002				A03010	A12013	
	A01002	A06009			A03011	F0051A	
	A01003	JA31n44	li li		A03012 A03013	A12002 A11012	
	A01003 A01004	<u>A06007</u> JA31022			A03014	A11013	
	A01004	A06006	2		A03015	A11003	
	A01013	A15012	11		A03016 A04001	A11002 JB31028	
	A01014 A01015	A15013 A15003			A04001	408002	
	401016	A15002	1		A04002	JB31005	
	10050A 10050A	JB31n06 A07011			A04007 A04003	A08003 JA31010	
	402001	J831029			A04003	A08004	
	20020A	A07012	- 11		A04004	JA31n33	
	FOUSDA	JA31028			A04004 A04005	<u>A08005</u> JB31046	
	<u> </u>	<u>A07013</u> JA31005			A04005	A08010	
	A02004	A07014			A04006 A04006	JB31023	
	A02005	JA3104n _A06002			A04005	A08009 JB31010	
	A02005 A02006	JA31018			A04007	A08007	
	A02006	A06003	11		A04008	JB31033	
	A02007	JA31019			A0400R A04009	A08006 A10012	
	<u> </u>	<u>A06004</u> JA31042			A 0 4 0 1 n	A10013	
	80008	A06005	. 2		A04011 A04012	A10003	
	402009 402010	A14012 A14013	- 11		404013	A10002 A05012	
	402010	A14003			A04014	A05013	
	402012	A14002	. 1		A04015 A04016	F0050A 405002	
	A02013 A02014	A13012 A13013	ll ll		A05002	A04016	
	A02015	A13003			A05 <u>00</u> 3	A04015	
	A02016	A13002			A05005 A05006	A19009 A05007	
	A03001 A03001	JA31nn2 A07002	11		A05007	A05009	
	403002	JA31025			A05007	A05006	
	A03002	A07003			A 05 0 0 R A 05 0 0 9	120006 10006	
	E0080A	JB31007 A07004			A05009	A05007	
	A03004	JB3103n	1		A05010	A16012	
	A03004	A07005			A05012 A05013	A04013 A04014	
	A03005 A03005	JA31n43 A07010			A05014	A10014	
	A03006	JA31021	1		A05015	A05016	
	A03006	A07009			A05016 A06002	A05015 A02005	
	A03007 A03007	JA31n11 <u>A07007</u>			A06003	40200k	
	AUJOUA	JA31034			A <u>0</u> 6004	402007	
					A06005	A0700A	
			11				

TITLE LOGIC BOARD WIR	E WRAP (TB	304A)		WL	DOCUMENT NO.	SHEET NO).	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	0	GNAL NAME R NUMBER FNIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
IDENTIFICATION	A06006 A06007 A06007 A06007 A06007 A06007 A06007 A07002 A07002 A07003 A07004 A07005 A07006 A07007 A07007 A07007 A07007 A07011 A07011 A07011 A07011 A07013 A07013 A07013 A07013 A07014 A08002 A08003 A08003 A08003 A08003 A080006 A08007 A090004 A09007 A090007	A01004 A01003 A09010 A01007 A01001 A03001 A09002 A03003 A09003 A03004 A03006 A09006 A09006 A09006 A09006 A09006 A09007 A09006 A09007 A09006 A04001 JA31046 A04007 A04008 JA31046 A04008 A04007 A07009	2 2 1 2 2 1 2 2 1 2 2 1 2 2 1 2 2 1 2 2 1 2 2 1 2 2 1 2 1 2 2 1 2 1 1 2 1		ENTIFICATION	A10009 A10010 A10012 A10013 A10014 A10014 A10015 A10016 A11007 A11007 A11007 A11007 A11009 A11000 A11012 A11013 A11014 A11014 A11015 A11016 A12002 A12007 A12007 A12007 A12009 A12010 A1	A10007 A19011 A04009 A04010 A05014 A10015 A03016 A03015 A19012 A11007 A12009 A11007 A12009 A11007 A19013 A03014 A12014 B15016 A11015 A03011 C20006 A12007 A13009 A12007 A13009 A12007 A13009 A12007 A13009 A12006 A11016 A11016 A11016 A12017 A13009 A12006 A12007 A13009 A12006 A11006 A11016 A02016	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

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OR NUMBER IDENTIFICATION	ORIGIN	NATION	LEVEL	OR NUMBER	ORIGIN	NATION	LEV
			1		'		
	A13014	A14014			A17005	A19002	
	A13014 A13015	A12014 A13016	11		A17006 A17006	A20009 C15007	
	A13016	A1301	1		A17007	A1801=	; 1
	A14002 A14003	A02012			A17007 A17009	A17010 B22004	2
	A14005	C25014	4 1		A17009	A18000	1
	A14006 A14006	A14009 A1500			A1701n A17015	A17007 B24010	
	A14007	A25003	7 2		A18001	A18007	1
	A14007 A14009	C05017			A18001 A18002	A18010 A17002	
	A14009	A14006	5 2		A18003	A19008	2
	A14010 A14012	825009 A02009			A18004 A18005	A19007 A19006	
	A14013	A02010	0 1		A18006	A19005	2
	A14014 A14014	A15014 A13014			A18007 A18007	A24003 A18001	
	A14015	A14016	5 1	•	A18009	A17009	1
	A14016 A15002	A1401= A0101a			A18010 A18010	A 18001 A 17001	
	A15003	A0101	1		A18015	A17007	1
	A15005 A15006	A19015 A15007			A19001 A19001	A16012 B24006	
	A15006	A10009	2		A19002	A20010	_1
	A15007 A15007	A15006			A19002 A19003	A17009 A20011	
	A15009	A14006	4 1		A19003	A17n04	. 2
	A15009 A15010	A15007 A19014			A19004 A19004	A20012 A17003	
	A15012	A01013	3 1		A19005	A20013	1
	A15013 A15014	A01014 A10014			A19005 A19006	A18006 A20014	
	A15014	A14014	4 1		A19006	A1800=	2
	A15015 A15016	A15016 A1501			<u> </u>	A20015 A18004	
	A16004	A23010	n 2		A1900B	A20016	1
	A16005 A16006	A21007 A21009			A19008 A19009	A18003 A05005	
	A16007	B1800	5 1		A19009	Culnns	2
	A16011 A16017	824011 A05010			A19010 A19010	A10005	
	A16012	A19001			A19011	A10010	
	A16013 A16014	A17002 B24007	- 11		A19011 A19012	C05014 A11005	
	A16015	A16016	· II		A19012	F25012	2
	<u>A</u> 16016 A17001	A16015 A18010			A19013 A19013	Allolo B18011	
	A17002	A16013			A19014	A15010	1
	A17002 A17003	A18002 A19004			A19014 A19015	F25014	
	A17004	A1900			A19015	022002	
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SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	01	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	LEVEL
5	A20001 A20003 A20003 A20003 A20004 A20006 A20006 A20006 A20007 A20007 A20007 A20007 A20001 A20011 A20012 A20016 A21001 A21001 A21002 A21003 A21004 A21004 A21009 A21010 A21011 A21011 A21012 A21013 A21014 A21016 A21016 A21017 A21017 A21017 A21018 A22008 A22008 A22008	A20002 A20003 A20003 A20005 A20006 A20006 A20006 A20007 A20006 A20007 A17006 A19006 A19007 A21007 A21011 A16006 A21007 A21011 A16006 A21007 A21011 A21007 A2	1			A23002 A23003 A23004 A23006 A23006 A23006 A23007 A23010 A23011 A23012 A23014 A23015 A23014 A23015 A23016 A24001 A24002 A24003 A24004 A24007 A24008 A24009 A24010 A24011 A24011 A24011 A24011 A24011 A24011 A24010 A25000	A2300 A2401 A2300 A2300 A2300 A2300 A2300 A2400 C2000 A2301 A2301 A2301 A2301 A2301 A2301 A2401 A2400 A2401 A2501 A2501 A2501 A2501	5 4 1 1 2 1 2 1 2 1 2 1 2 1 2 1 2 2 2 1 2 1 2 2 2 1 2 1 2 2 2 2 1 2 2 2 2 1 2

TITLE LOGIC BOARD WIR	E WRAP (TB	304A)		WL	DOCUMENT NO	SHEET NO) .	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	0	I IGNAL NAME R NUMBER PENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
	A25011 A25012 A25013 A25014 A25014 A25016 A25016 A26002 A26003 A26004 A26004 A26005 A26006 A26007 A26007 A26007 A26009 A26010 A26010 A26010 A26011 A27001 A27001 A27003 A27004 A27004 A27005 A27006 A27007 A26009 A27001 A27008 A27008 A27009 A27011 A27012 A27013 A27013 A27013 A27015 A28001 A28003 A28004 A28007 A28008 A28007 A28008	C2B002 E28013 E14014 F21009 F30005 A25016 A25016 A25013 A26003 A26002 JF31013 A30015 B29006 A30001 B29006 A29001 B29001 B29003 B30002 B29012 B29013 A28003 A27008 B13012 B27012 A27005 JE31043 B12004 A28013 B20004 A28013 B20004 A28013 B20004 A28013 B20004 A28013 B30006 B30006 B30006 B30006 B30006 B30006 B30006 B30006 B29012 B29014 A28013 B30006 B30006 B30006 B30006 B30006 B30006 B30006 B30006 B30006	2			A28009 A28010 A28011 A28013 A28015 A29001 A29003 A29004 A29005 A29006 A29007 A29008 A29009 A29010 A29013 A30006 A30007 A30008 A30009 A30010 A30010 A30010 A30010 B01002 B01003 B01004 B01005 B01006 B01007	JE31036 A28006 B11002 B12011 A27013 A29013 B30004 A26007 A28006 A29006 A29006 A29006 A29006 A29006 A29006 A29003 B13001 A26006 A29003 B14005 B12012 A30006 A30006 A29003 B14005 B12012 B1002 B1002 B1003 B1004 B1004 B1004 B01005 B02011 B01011 B01004 B01006 B02011 B01006 B01007 JF31020 B01006 B01006 B01007 JF31020 B01006	

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SIGNAL NAME	WRAP (IB	JU4A)	т п		10000	6		A
OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	801011 801012 801012 801013 801014 801015 801016 802002 802003 802004 802004 802005 802005 802006 802007 802007 802007 802007 802007 802011 802011 802011 802012 802013 802014 802015 802016 803002 803003 803003 803003 803003 803003 803006 803006 803007 803006 803007 803007 803007 803007 803007 803007 803011 803011 803011 803011 803011 803015 803015 803015 803016 803015	B01004 B02005 B01012 B03012 JB31037 JB31014 B02016				B04002 B04003 B04004 B04004 B04005 B04006 B04006 B04007 B04007 B04007 B04001 B04011 B04012 B04012 B04016 B04016 B04016 B05002 B05003 B05003 B05003 B05003 B05001 B05001 B05011 B05011 B05012 B05011 B05012 B05013 B05011 B05011 B05012 B05013 B05016 B05006 B05006 B05006 B05006 B05006 B05006 B05007 B05011 B05011 B05012 B05013 B05016 B05006	JF31017 B11015 B04002 B04005 B05011 B04004 B04007 JF31015 B11001 B04005 B05012 JB31042 JB31044 JB31022 JB31045 JB31045 JB31045 JB31045 JB31045 JB31045 JB31044 JB31025 JB31045 JB31045 JB31045 JB31045 JB31045 JB31045 JB31044 JB31045 JB31046	1 2 2 1 1 2 1 1 1 2 1 1 2 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1 1 1 1 1 2 1 1 1 1 1 2 1 1 1 1 1 2 1 1 1 1 1 1 2 1 1 1 1 1 1 2 1 1 1 1 1 1 2 1

TITLE LOGIC BOARD WIR	E WRAP (TB3	04A)		WL	DOCUMENT NO	SHEET NO) .	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL) o	IGNAL NAME R NUMBER PENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
	## 1000	B18004 B06006 JA31003 JA31026 B06005 B01011 B07012 B01012 JA31024 JA31001 B06016 B06016 B07002 B08011 B07006 F12011 JB31024 B07007 B07006 F12011 JB31024 B07007 B08003 B080012 JB31034 JB31011 B07016 B08012 B08003 B08003 B08004 B08004 B08004 B08006 B08004 B08006 B08004 B08006	2 1 1 2 1 1 1 1 1 1 2 2 1 1 2 2 1 1 1 2 2 1 1 1 2 1 1 2 1 1 2 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 2 1 1 2 1 2 1 1 2 2 2 1 2 1 2 2 1 2 1 2 1 2 1 2			808014 808015 808016 809002 809003 809003 809004 809004 809006 809006 809006 809006 809007 810007	JB31 002 B08015 B08015 B08016 B09002 B13007 B10011 B09005 B09004 B09011 B09006 JA31004 JA31027 B09006 JA31004 JA31027 B09006 B10015 B10015 B10016 B10017 C15010 B10007 C15010 B10006 JB31009 J	1 1 2 2 1 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1

TITLE LOGIC BOARD WIRE	WRAP (TB	304A)		WL	DOCUMENT NO.	SHEET NC).	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	.EVEL	Of	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
	81100A R11009 R11010 R11010 R110112 R11013 R11013 R11013 R11013 R11014 R12005 R12004 R12005 R12006 R12006 R12007 R12007 R12007 R12008 R12009 R12009 R12011 R12011 R12011 R12012 R12012 R12013 R12014 R12015 R13001 R13001 R13004 R13005 R13006 R13007 R13007 R13007 R13007 R13007 R13007 R13007 R13007 R13011 R13011 R13011 R13011 R13011 R13013 R13013 R13013 R13013 R13013 R13013	#2700 #1200 #1200 #1200 #1200 #1200 #1200 #1200 #1601 #1000 #1301 #1101 #1101 #0300 #0401 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400 #1400	R 1 1 2 4 1 4 2 9 2 2 5 1 3 2 4 1 2 1 3 1 7 1 3 1 7 1 7 1 5 1 8 2 7 2 9 1 8 1 1 2 2 1 3 1 7 1 1 3 2 1 1 7 1 7 1 5 1 8 2 7 2 9 1 8 1 9 1 1 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7			#13014 #13015 #14001 #14002 #14003 #14004 #14005 #14006 #14006 #14006 #14006 #14007 #14010 #14010 #14011 #14012 #14012 #14012 #14013 #14013 #14014 #14015 #15002 #15003 #15004 #15006 #15006 #15007 #15007 #15007 #15009 #15010 #15011 #15012 #15012 #15013 #15014 #15015 #15016 #16002 #16003 #16003 #16003 #16003 #16003 #16003 #16005	F0400 B0200 B0100 F0400 B1900 B1900 A2701 D0600 A3000 C1801 B2501 B0800 F1200 A2101 A3001 D0601 A3001 D1501 A3001 B1500 C1001 B1500 B1500 C1001 B1500 C1001 B1500 C1001 B1500 C1001 C1	

TITLE LOGIC BOARD WIR	E WRAP (TB3	04A)		WL	DOCUMENT NO	SHEET NO). 	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	EVEL
	B16006 B16007 B16009 B16010 B16011 B16012 B16013 B16014 B16015 B16016 B17002 B17003 B17005 B17006 B17007 B17009 B17007 B17013 B17013 B17013 B17014 B17013 B17014 B17013 B17014 B17015 B17016 B18002 B18003 B18004 B18005 B18007 B18009 B18011 B18011 B18011 B18011 B18011 B18013 B18014 B19013 B19013 B19003 B19003 B19003 B19003 B190002	F19006 F17010 E20012 E30002 E26013 E20014 B17016 E20002 B12010 C12002 C12003 C12003 C12003 C12003 C12003 B18003 C14014 JD31020 C18011 JD31020 C18011 JD31020 C18011 JD31020 C18011 JD31020 C18011 JD31020 B16003 B17006 C20007 A19013 F01003 A26010 B18016	0 1			B20003 B20004 B20004 B20005 B20007 B20007 B20008 B20009 B20009 B200013 B20013 B20013 B20013 B20013 B20014 B20015 B21005 B21005 B21005 B21005 B21005 B21006 B21011 B21012 B21013 B21014 B21015 B21016 B21017 B21017 B21018 B22018	F30012 B19003 D10004 A27011 B20005 B20006 B20006 B20006 B20007 B20007 B20007 B20007 B19013 F03006 B05003 JC31012 C14014 JC31012 A21012 A23011 D04013 J031043 J031044 J	2 2 1 1 2 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1

TITLE LOGIC BOARD WIRE	WRAP (TB3	104A)		WL	DOCUMENT NO.	SHEET NO).	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	Oi	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	B23002 B23003 B23003 B23003 B23004 B23005 B23007 B23006 B23001 B23012 B23013 B23014 B23014 B23014 B23015 B23016 B24007 B24006 B24006 B24007 B24007 B24011 B25001 B25000 B25000 B25000 B25000 B25000 B25000 B25001 B25001 B25011 B25011	828007 825011 D15006 822006 824011 JF31018 C01011 824005 822004 823018 824018 823016 823016 823015 F09013 A22004 C30005 B23017 A17015 B24011 B24010 B23017 A16011 B23004 B23018 A16011 B23004 B23016 B23005 A19001 B23017 A16010 B23017 A16011 B23004 B23018 B24018 B24018 B25008 B25018 A27004 E17007 A21018 A21018 A21018 A21018 A21018 A21018 A21018 A21018				825015 R26002 R26002 R26003 R26003 R26004 R26005 R26006 R26006 R26006 R26007 R26011 R26012 R26013 R26014 R26015 R26015 R27002 R27002 R27002 R27003 R27004 R27006 R27007 R27009 R27010 R27010 R27011 R27017 R27010 R27011 R27017 R27018 R27013 R27014 R27016 R27017 R27019 R27017 R27019 R27017 R27019 R27011 R27017 R27019 R27011 R27017 R27019 R27010 R27010 R27010 R27011 R27017 R27010 R27011 R27017 R27019 R27011 R27017 R27019 R27011 R27011 R27012 R27013 R27014 R27015 R27016 R28001 R28001 R28001 R28001 R28007 R28007 R28009 R28007 R28009 R28011 R28011 R28011 R28011	A21002 JC31026 C25011 F13002 C27003 A19010 JF31007 C06011 F15014 B29014 C28010 B18014 D29012 F20003 B26019 B26019 C27007 B12006 C27010 F10010 C27007 B12006 C30013 B27002 B26009 A27007 B280007 B280007 B280007 B30007	

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	E WRAP (TB3	T	т т		GNAL NAME	1 11	1	T
OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVEL	01	R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	LEVEL
LOGIC BOARD WIR SIGNAL NAME OR NUMBER		DESTI-NATION D08009 B30012 C10013 B29009 B29008 A26004 B28005 A26004 B28001 B28001 A26015 A26014 A26013 D06009 B30003 D06009 B30003 D06009 B30003 B26010 A26011 A26012 B28004 A27015 B28004 A27015 B28004 A27016 B28005 A27016 B28005 A27016 B28006 A28001 B28006 A28001 B28006 A28001 B28006 A27016 B2		01	GNAL NAME R NUMBER	11	DESTI-NATION C01007 B23005 C09004 D19002 C04003 C01016 C01015 C09010 C01014 C04002 D16011 C10002 JF31003 C20013 C08005 F15002 D13014 B16002 C08007 C14006 JE31023 C03010 J031015 C02016 C02016 E20007 B16004 C19003	A Z LEVEL 1 2 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1

TITLE LOGIC BOARD WIRE	WRAP (TB	304A)		WL	DOCUMENT NO.	SHEET NO).	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	0	IGNAL NAME OR NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
	C04006 C04007 C04009 C04010 C04011 C04013 C04013 C04014 C04014 C04014 C04016 C05007 C05008 C05007 C05008 C05007 C05011 C05012 C05013 C05014 C05015 C05016 C06007 C06009 C0	D14006 C24003 C18004 A14007 A19011 C05015 C05015 C07012 C17003 C16014 B11013 C04007 F05013 C08012 C13007 B26006 J031045 C08012 C04007 C				C07006 C07007 C07010 C07011 C07011 C07012 C07013 C07013 C07014 C07015 C07016 C08002 C08003 C08004 C08006 C08006 C08006 C08006 C08006 C08010 C08011 C08011 C08012 C08016 C08016 C08016 C09007 C0	F24014 F23003 F09011 C15013 C07005 C07004 F14004 F24011 D12011 D20010 C07016 C07016 C07016 C07016 C03013 C08008 C10006 C03013 C08009 C13010 C08016 C08016 F05007 C09007 C04013 C01012 D14011 C02011 C08016 F05007 C09007 C04013 C01012 C0	1

LE LOGIC BOARD WIRE	BOARD WIRE WRAP (TB304A)			WL	DOCUMENT NO.	SHEET NO	O .	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	C	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	LEVE
IDENTIFICATION	C10002 C10003 C10003 C10005 C10005 C10005 C10005 C10006 C10007 C10001 C10011 C10011 C10012 C10013 C10014 C10015 C10016 C11001 C1	C11001 F19007 C18014 C11008 C11006 C11016 C11010 C11009 C11012 C11011 C13010 C10003 C11015 C11014 D20011 C11007 C12006 C19005			JEN TIPICATION	C12005 C12006 C12006 C12007 C12015 C12016 C13002 C13003 C13003 C13006 C13007 C13009 C13010 C13011 C13011 C13013 C13014 C13014 C13014 C13014 C13015 C14003 C14003 C14004 C14005 C14005 C14006 C14010 C14011 C14012 C14013 C14014 C14016 C15007 C15010 C15010 C15011 C15013 C15013 C15013	C10002 E14000 C12016 C12016 C12016 C12016 C12016 C12016 C12016 C12016 C12016 C14016 C10006 C14002 C13016 C14016 C1	5 1 2 2 5 1 1 5 1 1 2 2 5 1 1 1 1 2 1 1 1 1

TITLE LOGIC BOARD WIRE WRAP (1	B304A)		WL	DOCUMENT NO.	SHEET NO),	REV.
SIGNAL NAME OR NUMBER ORIGIN IDENTIFICATION	DESTI- NATION	EVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	EVEL
C1501 C1501 C1501 C1501 C1600 C1600 C1600 C1600 C1600 C1600 C1600 C1600 C1601 C1601 C1601 C1601 C1601 C1700 C1700 C1700 C1700 C1700 C1700 C1700 C1701 C1800	5				C19002 C19003 C19003 C19004 C19005 C19006 C19007 C19009 C19010 C19011 C19012 C19014 C19015 C19016 C20002 C20003 C20003 C20004 C20005 C20006 C20007 C20001 C20013 C20013 C20013 C20013 C20013 C20013 C20016 C21007 C21003 C21004 C21005 C21005 C21006 C21007 C21007 C21009 C21010 C21011 C21011 C21012 C21012 C21011 C21012 C21012 C21012	D14n1 F20n1 C02n0 D1700 C20n0 C20n1 C22n1 C1200 D14n1 C21n1 C21n1 C21n1 C17n0 C17n0 C17n0 C22n0 C22n0	03133113311221121121121121121121111112111111

TITLE LOGIC BOARD WIRE WRAP (TB304A)	WL DOCUMENT NO. SHEET NO. 15 REV.
SIGNAL NAME OR NUMBER IDENTIFICATION ORIGIN DESTI- NATION LEVEL	SIGNAL NAME OR NUMBER ORIGIN DESTI- NATION LEVEL
C21013	C24005

TITLE LOGIC BOARD WIRE WRAP (TB304	A)		WL	DOCUMENT NO.	SHEET NO).	REV.
	ESTI-	Z LEVEL	OF	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
C27014 C27015 C27016 C28002 C28002 C28003 C28004 C28004 C28005 C28007 C28010 C28011 C28012 C28012 C28013 C28013 C28014 C28014 C28015 C28016 C29002 C29004 C29005 C29007 C29010 C29011 C29015 C29016 C30002 C30002 C30003 C30003 C30003 C30004 C30006 C30007 C300011 C30011 C30011 C30011 C30012 C30011	B26007 C27016 C27015 E15010 A25011 F25003 B30010 C23012 C30011 B26011 C28007 C28005 C28013 C28013 C28016 C28016 C28016 C28016 C28015 C30009 C29013 A25007 D28007 C30014 D27006 D28007 C30004 C29015 C29015 C29016 C29003 C30004 C30004 C30004 C30005 C30004 C28005 C30004 C28005 C30004 C28005 C30004	1			C30013 C30014 C30015 C30016 C31026 C31027 C31028 C31028 C31030 C31000 C31000 C31000 C31000 C31000 C31000 C31000 C31000 C31000 C3	B27010 C29009 C30016 C30016 C31026 C31026 C31026 C31026 C31036 D06001 JE31006 JE3100	

LOGIC BOARD WIRE	WRAP (TB	304A)		WL	DOCUMENT NO.	SHEET NO	·	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVEL	0	IGNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	EVE
	003008 003010 003010 003011 003013 003013 003013 003014 003015 004001 004002 004004 004005 004006 004007 004008 004009 004001 004011 004013 004014 004014 004015 004016 005001 005003 005003 005003 005001 005011 005012 005015 005017 005011 005017	D0500 D04000 D04000 D05001 D05001 D05001 D04012 B21000 D04012 D04012 D04012 D04013 D04013 D04013 D04014 D05000 E21013 D05010 D04014 D05000 E21013 D05010 D04014 D05000	5 1 3 2 3 1 2 1 1 1 3 2 4 2 2 1 1 1 7 2 4 1 3 1 5 2 6 1 7 2 6 1 7 1 6 1 6 1 6 1 6 1 6 1 7 2 8 1 8 2 9 1 9 1 9 1 9 1 9 1 9 1 9 1 9 1			D06003 D06004 D06004 D06005 D06005 D06007 D06007 D06009 D06010 D06011 D06012 D06012 D06013 D06013 D06015 D07001 D07003 D07004 D07005 D07006 D07007 D07007 D07007 D07011 D07011 D07012 D07013 D07013 D07015 D08001 D08003 D08004 D08004 D08004 D08007 D08009 D08007 D08009 D08011 D08011 D08012 D08012	D0700 E0801 B1400 E1300 B1400 D2101 B2901 E0700 D1501 E0801 B1401 D0700 D0101 D0700 C0101 D0600 B1300 E0801 B2801 F2000 B2801 B2801 F2000 B1300 E0801 B1300 E1300 B2801 E0801 B1301 E0801 E0901	3 1 2 3 2 5 1 1 0 2 2 2 3 2 1 1 2 2 2 3 3 1 1 2 2 3 1 1 2 3 1 1 2 2 3 1 1 1 2 3 1 1 1 2 3 1 1 1 1

LOGIC BOARD WIRE WRAP (TB304A)	WL	DOCUMENT NO.	SHEET NO),	REV.	
SIGNAL NAME OR NUMBER ORIGIN DESTI- LEV	EL	C	SIGNAL NAME OR NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
D08013				D12005 D12006 D12007 D12009 D12011 D12013 D12013 D12014 D12014 D12015 D12016 D13002 D13003 D13003 D13004 D13007 D13007 D13009 D13001 D13010 D13011 D1	D16004 D12003 JD31031 D11011 F05003 C04011 C07013 D20004 F28003 C11006 D20004 E19011 D13006 E19011 D13006 E19011 D13006 C16007 D20004 F29014 D14010 D14010 D13016 D	3 2 1 1 3 1 1 2 1 3 1 1 1 2 1 3 1 1 1 2 1 1 2 1 1 1 1

TITLE LOGIC BOARD WIRE	E WRAP (mp3	304A)		WL	DOCUMENT NO.	SHEET NO).	REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	OF	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL
	D14016 D15002 D15003 D15004 D15005 D15006 D15006 D15009 D15010 D15011 D15011 D15011 D15014 D15015 D15016 D16002 D16003 D16004 D16005 D16006 D16007 D16009 D16011 D16011 D16011 D16011 D16011 D16011 D16011 D16011 D16011 D16012 D16006 D17002 D17002 D17002 D17002 D17003 D17010 D17011 D17012 D17013 D17014 D17015 D16006 D18006	D15n13 D16n05 C20n13 C09n13 D15002			PE54018	D18007 D18009 D18010 D18011 D18012 D18012 D18013 D18014 D18015 D18016 D19002 D19003 D19004 D19004 D19001 D19011 D19011 D19011 D19012 D19013 D19013 D19013 D19014 D19016 D20002 D20003 D20004 D20006 D20006 D20006 D20006 D20010 D20011 D20011 D20011 D20011 D20013 D20014 D20015 D20016 D20015 D20016 D20016 D21004 D21004	D18014 C24003 C13003 E15006 JC31014 E02005 C29013 D18007 D18016 D18015 D27009 C01013 A12010 E30010 C18004 F08013 D13010 E30014 D19013 JE31020 D15007 D19012 C30007 D19012 C30007 D19012 C30007 D19012 C30007 D19012 C30007 D19015 E2002 D2006 D12013 D13003 A24013 D20003 C07014 C11015 E18002 D20016 D20015 E20013 D20016 D20015 E21013 D20016 D20015 E21013 D20016 D20016	1 1 1 1 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 1 2 2 1 1 1 1 2 2 1 1 1 1 1 2 2 1 1 1 1 1 1 2 2 1 1 1 1 1 1 1 2 2 1

TITLE LOGIC BOARD WIRE	LE LOGIC BOARD WIRE WRAP (TB304A)		WL	DOCUMENT NO.	SHEET NO.		REV.	
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL	0	GNAL NAME R NUMBER ENTIFICATION	ORIGIN	DESTI- NATION	EVEL
	021005 021006 021006 021007 021009 021010 021011 021011 021013 021014 021015 021016 022002 022003 022003 022003 022003 022003 022007 022009 022010 022011 022011 022011 022012 022013 022016 023002 023003 023004 023005 023005 023006 023007	D23010 D23010	2 1 2 1 2 1 3 1 5 1 1 5 1 1 5 1 1 5 1 1 5 1 1 5 1 1 5 1 1 5 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1			D24004 D24005 D24006 D24007 D24007 D24010 D24011 D24012 D24013 D24014 D24015 D24015 D25002 D25002 D25006 D25006 D25006 D25007 D25009 D25010 D25011 D2	C25004 C25006 C25006 C24015 C21012 C22012 C2	

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OR NUMBER IDENTIFICATION	ORIGIN	NATION	LEVEL	OR NUMBER IDENTIFICATI	ORIGIN	NATION L
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	D27005	D28010			<u> </u>	
	D27006	C29010			030016	
	<u> </u>	D27010			D31030 D31030	
	D27010	D27007			D31030	
	027011	027004			D31038	
	027012	F29009			031046	
	027013	C29007			E01001	F23009
	027013	D26004			E01001	
	N27014 N27014	C29009 D26006			E01003	
	027014	D27016			E01003 E01004	
	D27016	D27015			E01004	
	D28002	A25004	1		F01005	E02003
	028002	D28003			E01006	E15007
	D28003	028002			E01007	
	<u>028003</u> 028004	D28004 D28003			F01009	
	D28004	028014			E01010 E01011	
	028005	C20010			F01012	
	D28005	028011	1		F01013	
	D28006	027002			E01014	
	028007	C29005			E02001	
	D28009 D28010	C29011 D27009			E02001 E02003	_
	028010	F19014			E02003	
	028011	D2800			E02004	
	US8011	E11004			E02005	018012
	028014	D28004			<u> </u>	
	028015 028016	D28016 D28016			£02006 £02007	
	D29002	C26014			E 02007	
	029002	02900	2		F.02008	_
	029003	D29007			E02009	
	<u> </u>	D30000			E02010	
	D29005 D29005	D29002 D29011			F.02011 E02012	
	029006	D22010			E02013	
	029007	D2900			F02014	
	029009	E29011			E03001	
	029010	C23013			E03001	
	D29011 D29011	D29009 D14011			F03002 F03003	
	029012	C23004			E03004	
	029012	B2601			E03005	
	029013	D29014			E03006	E08004
	D29013	F20006			<u>E03007</u>	
	D29014 D29014	C23014 D2901			E03009	
	029015	02901			E03009 E03010	
	D29016	D2901			E03010	
	P000E0	D29004			E03011	
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TITLE LOGIC BOARD WIRE WRAP (TB304A)	WL DOCUMENT NO. SHEET NO. 22 REV. A
SIGNAL NAME OR NUMBER ORIGIN DESTI- NATION LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION SIGNAL NAME ORIGIN DESTI- NATION LEVEL
E03012 E04004 1 E03013 E04004 1 F03014 E04014 1 E03015 JE31027 1 E03015 E08006 2 E04001 JE31017 1 E04001 E09005 2 E04002 E09009 1 E04003 E09007 1 E04006 E09004 1 E04006 E09004 1 E04007 E09003 1 E04008 E31046 1 E04009 J031001 1 E04009 J031001 1 E04009 E09002 2 E04010 E09001 2 E04011 E05011 2 E04011 E05011 2 E04011 E05014 1 E04015 JE31014 1 E04015 JE31014 1 E04015 JE31014 1 E04015 JE31014 1 E04015 JE3006 2 E05006 E09006 2 E05001 F05006 2 E05001 F05006 2 E05001 E00007 1 E05000 E09007 2 E05001 E00007 2 E05001 E00006 2 E05001 E00006 2 E05001 E00006 2 E05001 E00007 1 E05000 E04010 1 E05000 E05000 1 E05000 E05000 2 E05011 E04011 2 E05015 E04010 2 E05015 E04010 2 E05015 E04010 2 E05011 E04011 2 E05015 E05010 2 E05011 E04011 2 E05015 E04010 2 E05011 E04011 2 E05015 E04010 2 E05007 E04006 1 E06007 E04006 1 E06007 E04006 1 E06007 E04006 1 E06007 E06006 1 E06007 E06006 1 E06009 E07013 2	E06009

TITLE LOGIC BOARD WIRE WRAP	(TB304A)		WL DOG	CUMENT NO.	SHEET NO) .	REV.
SIGNAL NAME OR NUMBER ORIGIN IDENTIFICATION	DESTI- NATION	Z LEVEL	OR NI	L NAME JMBER IFICATION	ORIGIN	DESTI- NATION	Z LEVEL
E09	0007 E0400 0007 E0100 0009 E0400 0009 E0400 0010 E1001 0011 E1001 0013 D0800 0014 D0901 0002 E1000 0002 E1000 0003 E1000 0004 E1000 0004 E1000 0005 E0500 0006 E0501 0007 E0500 0008 E1000 0009 E0500 0009 E0500 0011 D1300 0011 D1300 0011 D1300 0012 D1001 0013 D1001 0014 E1101 1005 E1501 1007 E1200 1008 E1000 1009 E2900 1001 E1100 1011 E1100 1012 D0501 1013 E1101 1014 E1101 1015 E1101 1016 E1101 1017 E1200 1018 E1101 1019 E2900 1010 E1101 1011 E1100 1012 D0501 1013 E1101 1014 E1101 1015 E1101 1016 E1101 1017 E1200 1018 E1101 1019 E2900 2000 E2500 2000 E2500 2000 E1100 2000 E0500 2000 E1000 2000 E1000 2000 E1000 2000 E10	9 2 2 1 3 2 0 2 0 1 1 1 2 1 4 1 2 1 4 1 2 1 4 1 2 1 4 1 2 1 4 1 2 1 1 1 1			E12005 E12006 E12007 E12009 E12001 E12011 E12012 E12013 E12014 E12015 E12016 E13002 E13003 E13004 E13005 E13006 E13007 E13007 E13007 E13007 E13007 E13007 E13007 E13007 E13010 E13011 E13012 E13013 E13014 E13014 E13015 E13016 E14002 E14002 E14003 E14004 E14006 E14006 E14006 E14007 E1	E11007 F10003 JC31001 F05002 F27001 E12010 C17002 E12010 F10002 C04009 E12016 E12015 E06007 D06005 E13011 E07010 D06005 E13011 JC31018 E13010 D17002 E13004 D25003 C13006 F13010 C14003 F23010 E14005 F05012 D16011 E14005 F05012 D16011 E14005 F05012 C12006 E13016 E14016 E13017 E15006 E14012 E14016 E14012 E14016 E14016 E14016 E14016 E14017 E15006	2 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 1 1 2 1 1 1 2 1 1 1 2 1 1 1 1 2 1 1 2 1 1 1 2 1 1 1 2 1 2 1 1 2 1 2 1 1 2 1 2 1 1 2 1 2 1 2 1 1 2 2 1 2 1 2 1 2 1 2 1 2 2 2 2 2 2 2 2 2 2 1 1 1 2

TITLE LOGIC BOARD WIRE WRAP (TB304A)	WL DOCUMENT NO. SHEET NO.	REV.
SIGNAL NAME OR NUMBER ORIGIN DESTI- NATION LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION ORIGIN NATION	Z LEVEL
E15005 E15004 1 E15006 E20012 2 F15006 D18011 1 E15006 E15003 2 F15007 C27012 1 E15010 F10013 1 E15010 C28002 2 E15011 J031020 1 E15012 C17011 1 E15013 F05004 1 F15013 F05004 1 F15013 C24010 2 E15014 F05006 1 E15015 E15014 D14013 2 E15014 F05006 1 E15015 E15014 1 E16002 B18003 2 F16002 B18003 2 F16003 B16003 1 E16004 E17012 1 E16004 E17012 1 E16006 E17011 1 E16006 E17011 1 E16007 C24004 1 E16009 JF31003 1 E16010 F14014 2 E16010 F14014 2 E16011 E17010 1 E16010 F14014 2 E16011 E17009 1 E16012 E17009 1 E16013 E16015 1 E16014 E17005 1 E16015 E16014 1 E16016 E17009 1 E16017 C24004 1 E16019 F15014 1 E16010 F14014 2 E16010 F14014 2 E16011 E17009 1 E16011 E17009 1 E16012 E17009 1 E16013 E16015 1 E17010 E16016 1 E17010 E16010 1 E17011 E16010 1 E17012 E16004 1 E17013 E16002 1 E17013 E16002 1 E17014 E18013 1 E17015 E16014 1 E17016 E16010 1 E17017 E16004 1 E17018 E16010 1 E17019 E16010 1 E17010 E16010 1	E18004 E18005 E18005 E18004 E18005 E18006 E18006 E18007 E18007 E18006 E18007 E18009 E18009 E18007 E18009 E18010 E18010 E18010 E18010 E19014 E18012 E17001 E18013 E17014 E18014 E17015 E19002 E19005 E19004 E24012 E19004 E24012 E19004 E19014 E19010 E19014 E19010 E19014 E19010 E19016 E19010 E19017 E19010 F23014 E19011 E16004 E19010 F23014 E19010 E19010 E19010 F23014 E19011 E16004 E19011 E16004 E19012 E16004 E19013 E19016 E19014 E18016 E19016 E19016 E20002 D20001 E20009 C17014 E20001 E25001 E20009 E70101 E20001 E250001 E20010 E25001 E20010 E25001 E20011 E25000 E20011 E25000 E20011 E25000 E20014 E18016 E20015 E20014 E20016 E20016 E20016 E20016 E20016 E20016 E20017 E26006 E21001 E26006 E21002 E27009 E21003 E21000	

LOGIC BOARD WIR	E WRAP (TB	304A)		WL DOCUMENT NO.	SHEET NO),	REV.
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	E21004 E21005 E21006 E21006 E21006 E21007 E21008 E21009 E21012 E21013 E21014 E22001 E22002 E22003 E22004 E22005 E22006 E22006 E22007 E22006 E22007 E22007 E22008 E22007 E22008 E22009 E22010 E22011 E22011 E22011 E22011 E22011 E22012 E23006 E23007 E23006 E23007 E23006 E23007	E27001 E21002 E23012 D09000 D250104 E21010 E22001 E14002 E23002 E	5 2 3 4 2 5 1 1 1 1 1 2 1 2 1 1 1 1 1 1 2 1 2 1		E23010 E23011 E23012 E23013 E23014 E23015 E24001 E24002 E24003 E24003 E24006 E24007 E25007 E25001 E25001 E25001 E25001 E25002 E25002 E25003 E25007 E25006 E25007	E28014 D07006 D21016 D21016 E22016 E23006 E23006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E24006 E25006 E26006	5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

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TITLE LOGIC BOARD WIRE	WRAP (TB	304A)		WL	DOCUMENT NO.	SHEET NO		REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	₹ LEVEL	0	IGNAL NAME R NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
	F02004 F02005 F02008 F02016 F03002 F03003 F03006 F03007 F03009 F03010 F03011 F03012 F03013 F03014 F03015 F04003 F04004 F04006 F04006 F04001 F04011 F04012 F04014 F04016 F04016 F04016 F04017 F04018 F04016 F04017 F04018 F04016 F05006 F05007 F05008 F05007 F05008 F05007 F05008 F05007 F05008 F05009 F05010 F05012 F05016 F05016 F05017 F05016 F05017 F05016 F05017 F05018 F05016 F05017 F05018	F0201 B1101 F0600 B12001 F0101 B2000 F01001 B2000 F01000 F01000 F01000 F01000 F01000 F01000 F01000 B1301 F01000 F01000 B1301 F01000 B1301 F0401 F0401 E1200 D1200 E1501 C0401 E1501 C0401 E1501 C0401 F0401	3 1 3 2 6 1 5 1 2 1 2 1 2 1 2 1 3 1 6 1 6 1 6 1 6 1 6 1 7 1 9 1 8 1 6 1 7 1 9 1 9 1 9 1 9 1 9 1 9 1 9 1 9			F06004 F06005 F06006 F06007 F06008 F06013 F06015 F08003 F08004 F08005 F08007 F08011 F08012 F08013 F08013 F08014 F08015 F08016 F09007 F08016 F09007 F09011 F09017 F09011 F09011 F09011 F09013 F09013 F09014 F09015 F09016 F09017 F0	F0401 F0401 F0301 F0200 F0200 F0200 F0400 F1401 E2700 F1001 E3000 D1401 C2601 F0801 F0800 F0801 F0800	3 1 1 4 2 3 1 5 1 1 1 2 1 1 1 2 2 1 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 1 2 2 1 1 2 2 1 1 1 2 2 1 1 2 1 1 2 2 1 1 2 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 2 1 1 1 2 1 1 2 1 1 2 1 1 1 2 1

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SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL	Ċ	GIGNAL NAME OR NUMBER DENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL
	F10013 F10014 F10015 F10016 F12001 F12010 F12010 F12010 F12010 F12010 F13000 F13000 F13000 F13000 F13001 F13011 F13011 F13011 F13011 F13010 F14000 F15000	E2401 F1001 F1001 F1001 F1001 F1001 F1001 F1001 F1201 F1200 F1301 F1200 F1301 F1200 F1301 F1401	4 1 6 1 5 1 9 1 5 2 7 1 6 1 5 1 9 1 5 2 7 1 6 1 5 1 3 2 3 1 1 1 7 2 0 1 1 3 1 3 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		PE54015	F15010 F15010 F15011 F15014 F15014 F15015 F15016 F17010 F17011 F17012 F17013 F17014 F17015 F18003 F18003 F18005 F180005 F180005 F180010 F18010 F18010 F18011 F18011 F18011 F18014 F19005 F19006 F19006 F19006 F19006 F19006 F19007 F19007 F19007 F19010 F19000	826006 F15011 F15010 E16013 F09002 F15016 E01003 F17014 F30010 F19006 F17011 F17016 E28006 F19000 F19006 F19000 F1	

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SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	EVE.
PE54018	F20006 F20007 F20010 F20010 F20011 F20013 F20014 F20015 F20016 F20016 F21005 F21005 F21007 F21009 F21010 F21011 F2	D29013 F10011 E07009 JE31044 C19003 C20012 B17003 F25002 F10006 F20015 F28002 F12010 F22002 F12011 A25013 F21001 E14012 C15006 F21010 F30014 F22002 F22001 F21007 D20013 F21006 F21010 F30014 F22002 F22001 F21007 D20013 F21006 F21010 F30014 F22002 F22001 F21007 D20013 F21006 F23011 E05013 C24005 E01001 E13014 D16002 F23016 F23016 F23016 F23016 F23016 F23016 F23017 JF31009 JC31032 E19017	1		F24011 F24013 F24014 F24015 F24016 F25003 F250004 F25005 F250007 F25007 F25007 F250101 F25011 F25012 F25013 F25014 F25015 F25016 F27001 F27001 F27009 F27009 F27009 F27009 F27009 F27010 F28001 F28001 F28001 F28001 F28001 F28001 F29000		2 1 2 1 1 1 1 1 2 1 1 1 1 2 1 1 1 1 2 1

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IDENTIFICATION	F29011 F2 F29011 F2 F29012 F2 F29013 D1 F29014 D1 F29014 C3 F29015 F2 F29016 F2 F30002 F3 F30003 F3 F30003 F3 F30005 F3 F30005 F3 F30006 D2 F30007 B2 F30007 B2 F30001 F3 F30010 F3 F30010 F3 F30010 F3 F30010 F3 F30010 F3 F30011 F3 F30011 F3 F30012 B2 F30013 B1 F30014 F3 F30015 F3 F31026 F3 F31027 F3 F31027 F3 F31028 F3 F31029 F3 F31029 F3 F31030 F3 F31030 F3 F31030 F3 F31031 F3 F31031 F3 F31032 F3 F31033 F3 F31033 F3 F31034 F3 F31035 F3 F31036 F3 F31037 F3 F31037 F3 F31038 F3	9008 1 9012 2 9011 2 0005 1 3011 2 0002 1 9016 1 9015 1 1002 1 0003 2 0002 2 3006 1 5009 1 0013 1 6002 2 0013 1 6002 2 0011 2 0013 1 6002 1 1010 1 0010 2 0010 2 0010 2 1010 1 1010 1 1010 2 1010 3 1 1010 4 1 1010 1 1010 2 1010 3 1 1010 1 1010 2 1010 3 1 1010 3 1 1010 4 1 1010 3 1 1010 4 1 10		DENTIFICATION	F31041 F31042 F31042 F31043 F31044 F31045 F31046 F31046 F31046 JA31001 JA31002 JA31009 JA31009 JA31009 JA31009 JA31010 JA31011 JA31011 JA31011 JA31017 JA31017 JA31017 JA31017 JA31017 JA31017 JA31017 JA31017 JA31017 JA31020 JA31020 JA31020 JA31020 JA31020 JA31020 JA31020 JA31020 JA31027 JA31027 JA31027 JA31027 JA31027 JA31037	F31040 F31042 F31043 F31043 F31044 F31044 F31044 F31045 F05008 B06014 A03001 B06009 A02004 A03007 B15010 A01001 JA31037 JA31037 JA31041 A02006 A02007 JA31037 JA31041 A05008 B06010 A02006 A03008 B15013 B15013 JA31013 JA31017 A01002 A02005 JA31017	1 2 2 1 1 1 2 2 1 1 1 1 1 1 1 1 1 1 1 1

LOGIC BOARD WIR	E WRAP (TB3	04A)		WL DOCUMENT NO.	SHEET N		REV.
SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	Z LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION	ORIGIN	DESTI- NATION	LEVE
SIGNAL NAME OR NUMBER	ORIGIN JA31041 JA31047 JA31044 JA31044 JA31044 JA31046 JB31001 JB31002 JB31003 JB31004 JB31007 JB31007 JB31010 JB31011 JB31012 JB31013 JB31014 JB31017 JB31017 JB31017 JB31017 JB31017 JB31017 JB31017 JB31017 JB31027 JB31021 JB31021 JB31022 JB31023 JB31024 JB31027 JB31027 JB31027 JB31027 JB31028 JB31029 JB31029 JB31030 JB31031 JB31032 JB31034 JB31035 JB31034	DESTI-NATION JA31020 A02008 A03005 A01003 A08003 B08014 B08009 B09014 A04007 B07014 B01010 B02009 B03014 B04009 B03014 B04009 B03014 B04009 B05014 B05009 A04008 B07013 B08010 A04008 B07013 B08010	LEVEL	SIGNAL NAME OR NUMBER IDENTIFICATION	JC31009 JC31010 JC31017 JC31018 JC31017 JC31018 JC31021 JC31022 JC31023 JC31025 JC31025 JC31025 JC31036 JC31037 JC31040 JC31041 JC31041 JC31043 JC31043 JC31043 JC31043 JC31043 JC31043 JC31043 JC31043 JC31041 JC31042 JC31043 JC31043 JC31043 JC31043 JC31041 JC31042 JC31043 JC31043 JC31043 JC31043 JC31043 JC31043 JC31041 JC31042 JC31043 JC310442 JC31044	C14005 E20006 B21004 F09003 D18012 E15002 E13006 B21002 C05002 B07006 B08002 E13006 B26002 F24005 F23013 JA31009 D22011 C25012 B08006 B09002 JC31042 E04009 D02009 C02011 C25012 B08006 B09006 E05001 C14013 D27005 C14011 C02014 C12012 B17013 E15011 B17014 B21006 D20014	Z LEVE
	JB31033 JB31034 JB31036 JB31037 JB31038 JB31040 JB31041 JB31041 JB31043 JR31044 JB31045 JB31046 JB31046 JB31046	A0400A B07013 B01010 B02013 B02010 B03013 B03010 B04010 B05010 A04005 E12006	1 1 1 1 1 1 1 1 1 1		JN3102n JN31021 JN31022 JN31023 JN31024 JN31025 JN31029 JN31029 JN31033 JD31035 JN31037 JN31040 JN31040 JN31044	E15011 B17014 B21006 D20014 D26003 D02007 E05015 D26003 D27003 D12006 F25006 B21011 B21010	1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
-	JC31002 JC31003 JC31007	C03006 F23005	_1		JD31045 JE31001 JE31002	Cn6n12 C18013 D01nns	1 1 1

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LOGIC BOARD WIRI		DESTI-	2	S	IGNAL NAME	32	DESTI-	A Z
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	JE31003 JE31004 JE31005 JE31006 JE31007 JE31010 JE31017 JE31017 JE31017 JE31017 JE31021 JE31022 JE31023 JE31024 JE31025 JE31026 JE31026 JE31027 JE31028 JE31028 JE31028 JE31029 JE31035 JE31035 JE31036 JE31036 JE31036 JE31036 JE31007 JE31007 JE31007 JE31007 JF31007 JF31007 JF31007 JF31007 JF31007 JF31007 JF31007 JF31010 JF31010 JF31010 JF31010 JF31010 JF31010 JF31010 JF31010 JF31010	A30007 E03001 E07011 D01007 A29009 D01009 A29007 E03009 E07012 D01011 E04015 D26011 D19012 E04010 D02005 C02013 C18005 D01003 A30009 E07005 C18007 E03010 E07004 C18003 D26009 A28009 E06005 D01013 A28007 D02003 A27009 F20010 D17012 F03012 B06006 F130105 F04010 B05006 F130105 F04010 B05006 F130105 F130106 F1301	1		-	JF31014 JF31015 JF31017 JF31019 JF31020 JF31023 JF31024 JF31025 JF31029	B05002 B04006 B04002 B23004 B02006 B01002 B02007 E28003 B14010 B15003	1 1 1 1 1 1 2

SECTION 7

PARTS DATA

INTRODUCTION

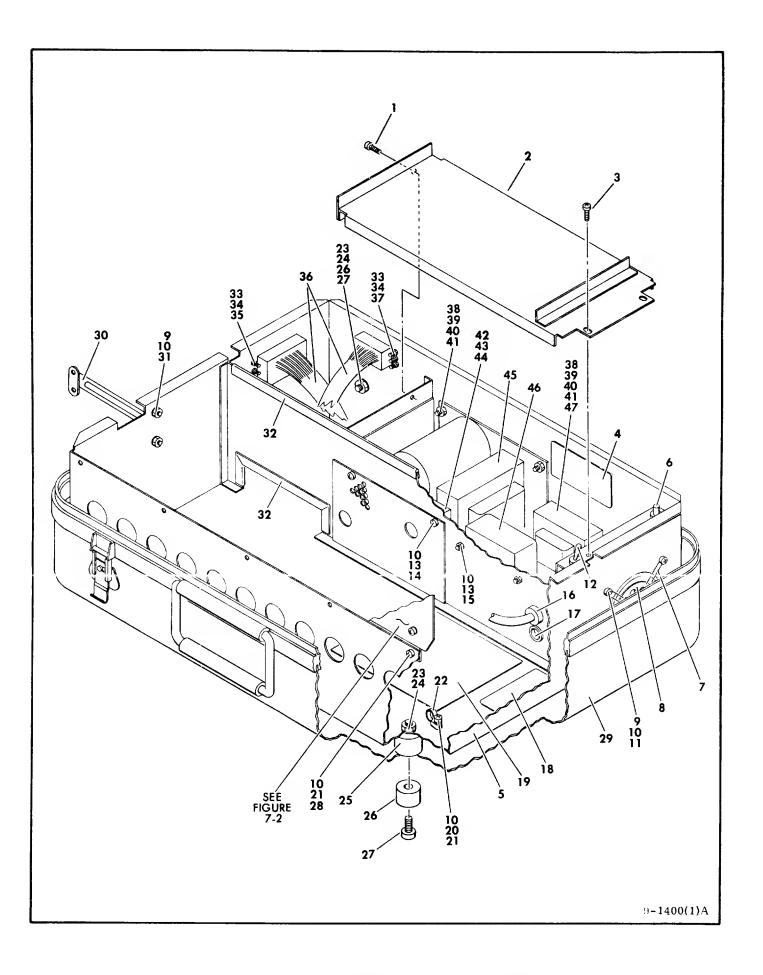
This section provides the information needed to order field replaceable parts for the TB304 Field Test Unit.

Information within this section is provided by representative illustrations and their companion parts lists. The parts shown on the illustrations are assigned index numbers. These numbers cross reference the illustrations to the associated parts lists.

The parts list associated with each illustration is organized in four columns:

- The Index Number column cross references the applicable entry to the associated illustration.
- The Part Number column provides the eight-digit number by which a part may be ordered.

- The Description column provides the part nomenclature. This column also provides information on the relationship of parts and assemblies. This is accomplished by means of indentation within the column. An indented item is part of a previous assembly which is indented to a lesser degree.
- The Notes column is used to show differences in configuration when more than one configuration of a machine is covered in the manual. This is shown by identifying a model level (Mod B), by identifying a machine series code and change order number (S/C10 with PE39289), or by identifying the last two digits of the eight-digit assembly part number to which the particular part applies (Tab 17).



INDEX NO.	PART NUMBER	PART DESCRIPTION	NOTES
1- 12345678910112131415161718922122234256278289331323333444223444444444444444444444444	77449300 17901509 77451400 17901508 46068603 77451500 94308204 94348701 94348701 94348700 10125803 10125105 92742179 92509009 10126401 10127113 92491013 15012410 10126600 72959300 10127114 10126103 94277421 10125301 10126404 92674005 94202502 10127154 10125301 10126404 92674005 94202502 10127154 10125301 10126404 92674005 94202502 10127154 10125301 10126404 92674005 94202502 10127154 10125106 10125106 10125714 94060001 93643004 93643005 93642007 10125106 10127123 10125606 10127123 10125606 10126402 93041010 93105110 931077002 94370600 94348402 94368600	FIELD TEST UNIT SCREW, THREAD ROLLING, PHILLIPS, 6-32 x 3/8 COVER, POWER SUPPLY SCREW, THREAD ROLLING, PHILLIPS, 6-32 x 1/4 PLATE, WARNING HOUSING, FTU CIRCUIT BREAKER GUARD, FAN, MINIATURE FAN, MINIATURE WASHER, SPRING LOCK, 6-TOOTH NUT, HEX, 6-32 SCREW, PAN HD, PHILLIPS, 6-32 x 1-7/8 SWITCH, TOGGLE, 2-POSITION WASHER, LOCK, EXT 6-TOOTH SCREW, PAN HD, 6-32 x 3/8 RELIEF, CORD BUSHING, SNAP-IN PLATE, EQUIP. IDENT. LABEL, FIELD CHANGE LOG SCREW, PAN HD, 6-32 x 1/2 WASHER, LOCK, INT 6-TOOTH STRAP, CABLE TIE NUT, HEX, 1/4-20 WASHER, LOCK, EXT TOOTH, 1/4 MOUNT, VIBRATION BUMPER, RUBBER SCREW, PAN HD, 6-32 x 1/4 CASE, FIBERGLASS SUPPORT, LID, FRICTION SCREW, FH, PHILLIPS, 6-32 x 1/4 CHANNEL, RUBBER, TYPE 1 CONNECTOR CORNER GUIDE PIN CONNECTOR, JACK SCREW, MALE I/O CABLE ASSY CONNECTOR, JACK SCREW, MALE NUT, HEX, 8-32 SCREW, PAN HD, 8-32 x 1/2 WASHER, LOCK, EXT 8-TOOTH STRIP, TERMINAL, BARRIER TYPE STRIP, MARKER, TYPE A-10 TERM. JUMPER, BARRIER STRIP POWER SUPPLY; -5V, 3A	PE54023
	54226509 77440300 83249600 77453400 83248700 83248900 83254300 92183001 92183003	ASSEMBLIES NOT SHOWN TYPE HFSV HD ALIGNMENT CARD ASSY HEAD ALIGNMENT CABLE ASSY A-CABLE ASSY (I/O) B-CABLE ASSY (I/O) I/O BYPASS (BEHIND THE I/O) CABLE ASSY ADAPTER CABLE ASSY, A-CABLE, 50-PIN ADAPTER CABLE ASSY, A-CABLE, 60-PIN ADAPTER CABLE ASSY, B-CABLE TEST LEAD, BLACK TEST LEAD, RED	TB304 A/B ONLY TB304 A/B ONLY TB304 A/B ONLY TB304 A/B ONLY TB304 A/B ONLY
		,	IBSG III B GITEL

83319600 D 7-3

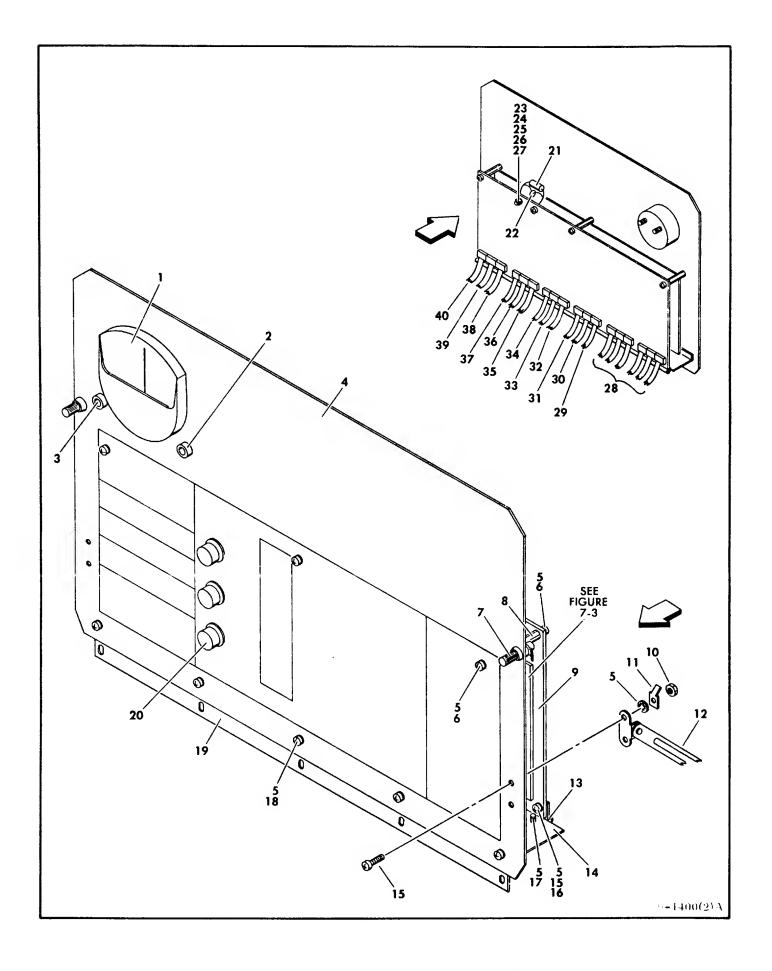
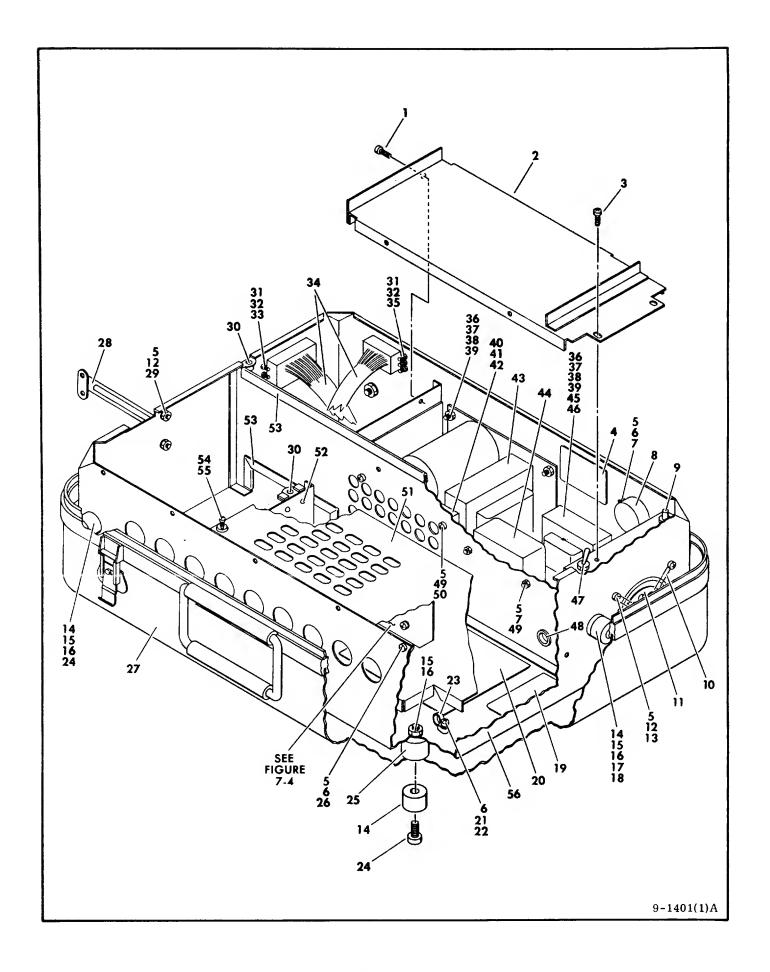


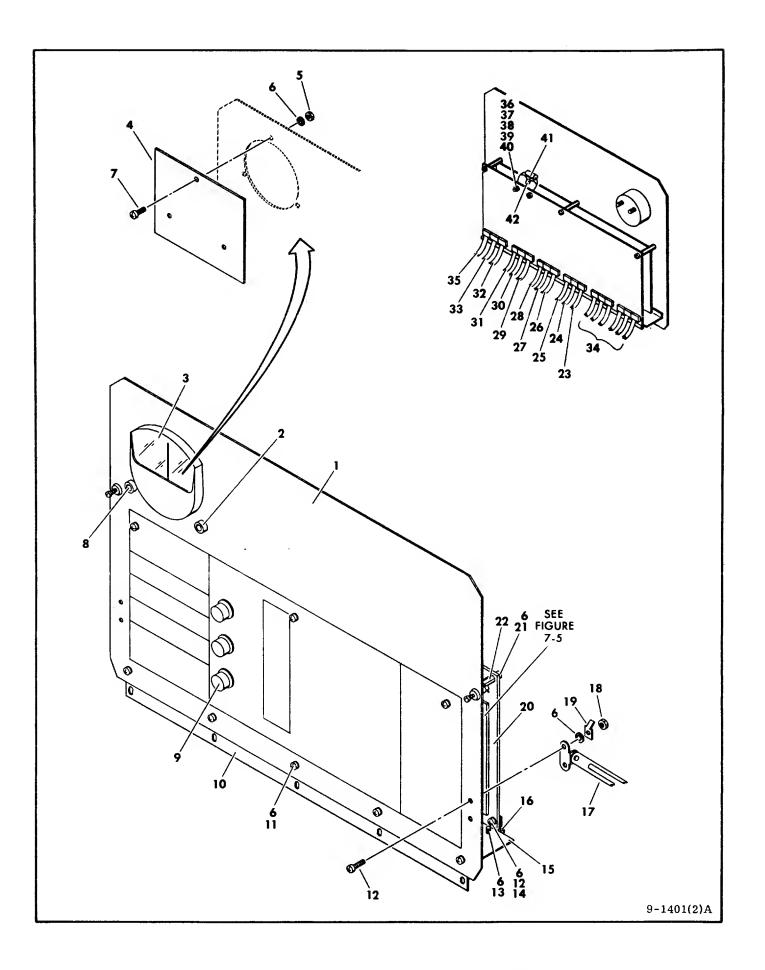
FIGURE 7-2. CONTROL PANEL, STANDARD PACKAGE

INDEX NO.	PART NUMBER	PART DESCRIPTION	NOTES
2- 123456789101123145678910122224456789333333333333333333333333333333333333	94559700 92183003 92183001 77451201 10126103 10127111 94358800 93114221 774516XX 10125105 94274106 94370400 76390800 77451300 10127113 95510026 17901508 17901509 76390700 93152009 76390700 93152009 76390700 93152009 76390700 93152009 76390700 93152009 7445100 74452103 74452106 74452105 74452105 74452105 74452100 74452100 74452100 74452100 74452100 74452100 74452100	FTU VOLTMETER, PANEL, DC JACK, BANANA, NYLON, BEDC JACK, BANANA, NYLON, BLACK PANEL, CONTROL WASHER, LOCK, INT 6-TOOTH SCREW, PAN HD, 6-32 x 1/4 LATCH, ADJUSTABLE GRIP STANDOFF, TAPPED POST, HEX, ALUM. PANEL, LOGIC, WIRE WRAP ASSY NUT, HEX, 6-32 TERMINAL, QUICK CONNECT, TYPE 3 SUPPORT, LID, FRICTION HINGE, IC BOARD BRACKET, BOARD MOUNTING SCREW, PAN HD, 6-32 x 3/8 NUT, HEX SCREW, THREAD ROLLING, PHILLIPS, 6-32 x 1/4 SCREW, THREAD ROLLING, PHILLIPS, 6-32 x 3/8 HINGE, CONTROL PANEL KNOB, SKIRTED CAPACITOR, ELECTRO; 6.8µF, 35V CAPACITOR, ELECTRO; 6.8µF, 35V CAPACITOR, 10,000PF, 25Y TERMINAL, RING TONGUE INS SCREW, PAN HD, 4-40 x 3/8 WASHER, NYLON WASHER, LOCK, EXT 4-TOOTH NUT, HEX, 4-40 I/O CABLE ASSY (SEE FIGURE 7-1) INTERCONNECT CABLE ASSY	PE54023

83319600 D 7-5

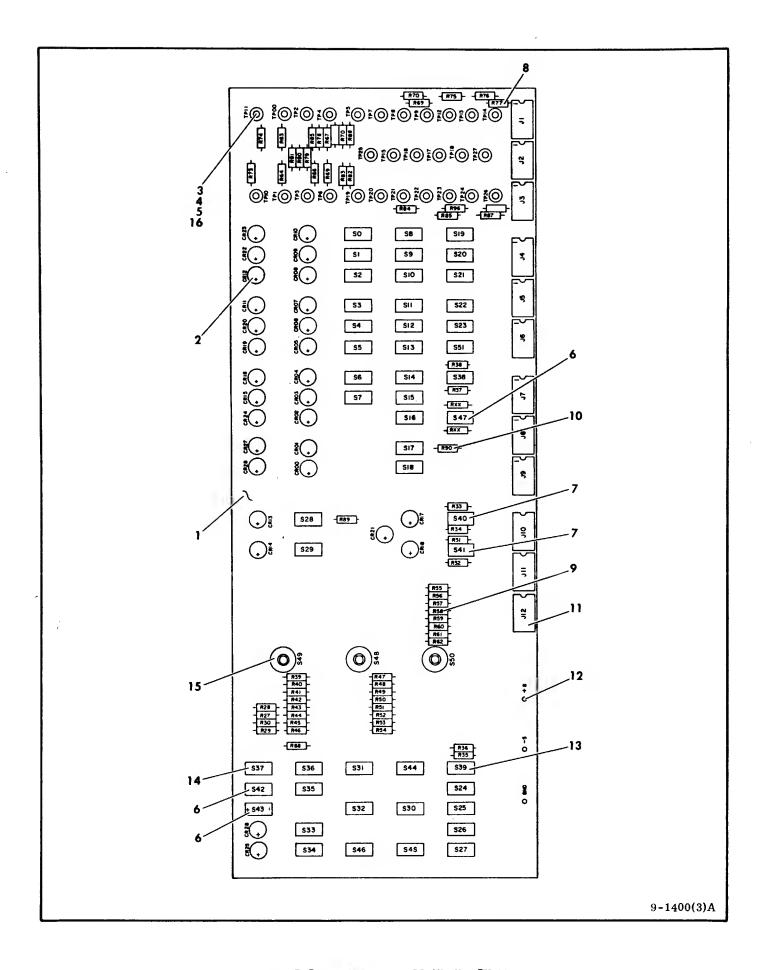


INDEX NO.	PART NUMBER	PART DESCRIPTION	NOTES
1- 1- 1- 1	77449300 77449301 77449302 17901509 83276500	FIELD TEST UNIT FIELD TEST UNIT FIELD TEST UNIT SCREW, THRD ROLLING, PHILLIPS, 6-32 x 3/8 COVER, POWER SUPPLY	TB304-A TB304-B TB304-C
3 4 5 6 7 8	17901508 46068603 10125105 10126103 10127113 24556501	SCREW, THRD ROLLING, PHILLIPS, 6-32 x 1/4 PLATE, WARNING NUT, HEX, MACHINE, 6-32 WASHER, LOCK, INT 6-TOOTH SCREW, PAN HD, PHILLIPS, 6-32 x 3/8 CONNECTOR	
9 10 11 12 13 14	94308204 94348701 94348700 10125803 92742179 94202502	CIRCUIT BREAKER GUARD, FAN, MINIATURE FAN, MINIATURE WASHER, SPRING LOCK, 6-TOOTH SCREW, PAN HD, PHILLIPS, 6-32 x 1 7/8 BUMPER, RUBBER	
15 16 17 18 19 20 21	10125301 10126404 94047086 10127351 10126600 72959300 10127114	NUT, HEX, 1/4-20 WASHER, LOCK, EXT TOOTH, 1/4 WASHER, SPECIAL SCREW, PAN HD, SLOT, 1/4-20 x 7/8 PLATE, EQUIP IDENT LABEL, FIELD CHANGE LOG SCREW, PAN HD, PHILLIPS, 6-32 x 1/2	
22 23 24 25 26 27	95510026 94277421 10127154 92674005 10127111 83271800	NUT, HEX, MACH, 6-32 STRAP, CABLE TIE SCREW, PAN HD, PHILLIPS, 1/4-20 x 5/8 MOUNT, VIBRATION SCREW, PAN HD, PHILLIPS, 6-32 x 1/4 CASE, FIBERGLASS	
28 29 30 31 32 33	94370400 10125714 93570009 93643004 93643005 93642006	SUPPORT, LID, FRICTION SCREW, FLT HD, CROSS-RECESSED, 6-32 x 3/8 RECEPTACLE CONNECTOR, CORNER GUIDE PIN CONNECTOR, CORNER GUIDE SOCKET CONNECTOR, JACK-SCREW, MALE	PE54023
34 35 36 37 38 39	77453600 93642007 10125106 10127123 10125606 10126402	I/O CABLE ASSY CONNECTOR, JACK-SCREW, FEMALE NUT, HEX, MACH, 8-32 SCREW, PAN HD, PHILLIPS, 8-32 x 1/2 WASHER, PLAIN, 8-TOOTH WASHER, LOCK, EXT 8-TOOTH	
40 41 42 43 44 44	93041010 93105110 77452100 94370600 94348402 94348403	STRIP, TERMINAL, BARRIER TYPE MARKER STRIP, TYPE A-16 TERM INTERCONNECTOR, CABLE ASSY POWER SUPPLY, 5V, 6A FILTER, R.F. FILTER, R.F.	SC 06 AND BELOW SC 07 AND ABOVE
45 46 47 48 49 50	94368600 83271300 92509009 15012410 10126401 10127115 83271500	POWER SUPPLY, 5V, 3A PLATE, BACK-UP SWITCH, TOGGLE BUSHING, SNAP-IN WASHER, LOCK, EXT 6-TOOTH SCREW, PAN HD, PHILLIPS, 6-32 x 5/8	
52 53 54 55 56	93994000 94060001 93573005 93988000 77459900 47455600	COVER, CABLE RIVET CHANNEL, RUBBER, TYPE 1 STUD ASSY WASHER, RETAINING HOUSING, FTU HOUSING, FTU	SC 06 AND BELOW
30	1,133000	FOR ASSEMBLIES NOT SHOWN, SEE PAGE 7-3.	SC 07 AND ABOVE



1 83276300 CONTROL PANEL, FTU 2 92183003 JACK, BANANA, NYLON, RED 3 94359700 VOLTMETER, PANEL, D.C. 4 77459800 COVER, NULL METER 5 10125105 NUT, HEX, MACH, 6-32 WASHER, LOCK, INT 6-TOOTH 6 10126103 WASHER, LOCK, INT 6-TOOTH 7 92748160 SCREW, PAN HD, PHILLIPS, 6-32 x 5/16 8 92183001 JACK, BANANA, NYLON, BLACK 9 93152009 KNOB, SKIRTED 10 83276200 HINGE, CONTROL PANEL 11 17901509 SCREW, THRD ROLLING, PHILLIPS, 6-32 x 3/8	NO. NUMBER PART DESCRIPTION	NOTES
12 10127113 SCREW, PAN HD, PHILLIPS, 6-32 x 3/8 13 17901508 SCREW, THER ROLLING, PHILLIPS, 6-32 x 1/4 14 95510026 NUT, HEX, MACH, 6-32 15 83276900 BRACKET, BOARD MOUNTING 16 76390800 HINGE, IC BOARD 17 94370400 SUPPORT, LID, PRICTION 18 10125105 NUT, HEX, MACH, 8-32 19 94274106 TERM., QUICK-CONNECT, TYPE 3 20 774516XX PANEL, LOGIC, WIRE WRAP ASSY 21 10127111 SCREW, PAN HD, PHILLIPS, 6-32 x 1/4 22 93114221 STANDOFP, TAPPED POST, HEX, ALUM. 23 77452101 INTERCONNECT CABLE ASSY 26 77452108 INTERCONNECT CABLE ASSY 27 77452106 INTERCONNECT CABLE ASSY 28 77452106 INTERCONNECT CABLE ASSY 30 77452104 INTERCONNECT CABLE ASSY 31 77452104 INTERCONNECT CABLE ASSY 32 77452102 INTERCONNECT CABLE ASSY 33 77452101 INTERCONNECT CABLE ASSY 34 77452101 INTERCONNECT CABLE ASSY 35 77452100 INTERCONNECT CABLE ASSY 36 77452100 INTERCONNECT CABLE ASSY 37 77452100 INTERCONNECT CABLE ASSY 37 77452100 INTERCONNECT CABLE ASSY 38 77452100 INTERCONNECT CABLE ASSY 39 77452101 INTERCONNECT CABLE ASSY 30 77452100 INTERCONNECT CABLE ASSY 31 77452100 INTERCONNECT CABLE ASSY 31 77452100 INTERCONNECT CABLE ASSY 32 77452100 INTERCONNECT CABLE ASSY 33 77452100 INTERCONNECT CABLE ASSY 34 77452100 INTERCONNECT CABLE ASSY 35 77452100 INTERCONNECT CABLE ASSY 36 77452100 INTERCONNECT CABLE ASSY 37 77452100 INTERCONNECT CABLE ASSY 38 77452100 INTERCONNECT CABLE ASSY 39 77452100 INTERCONNECT CABLE ASSY 30 77452100 INTERCONNECT CABLE ASSY 31 77452100 INTERCONNECT CABLE ASSY 32 77452100 INTERCONNECT CABLE ASSY 33 77452100 INTERCONNECT CABLE ASSY 34 77452101 INTERCONNECT CABLE ASSY 35 77452100 INTERCONNECT CABLE ASSY 36 77452100 INTERCONNECT CABLE ASSY 37 77452100 INTERCONNECT CABLE ASSY 38 77452100 INTERCONNECT CABLE ASSY 39 77452100 INTERCONNECT CABLE ASSY 30 77452100 INTERCONNECT CABLE ASSY 31 77452100 INTERCO	2 92183003 3 94359700 VOLTMETER, PANEL, D.C. 77459800 COVER, NULL METER 10125105 6 10125105 6 10126103 7 92748160 8 92183001 9 93152009 10 83276200 HINGE, CONTROL PANEL 11 17901509 12 10127113 13 17901508 14 95510026 15 83276900 16 76390800 17 94370400 18 10125105 19 94274106 20 774516XX 21 10127111 21 1072711 22 93114221 23 77452101 24 77452109 25 77452109 26 77452108 27 77452107 28 77452107 31 77452101 31 77452100 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452101 31 77452102 31 77452103 31 77452104 31 77452105 31 77452107 31 77452107 31 77452107 31 77452108 31 77452107 31 77452108 31 77452109 31 77452107 31 77452107 31 77452108 31 77452109 31 77452107 31 77452107 31 77452108 31 77452109 31 77452107 31 77452107 31 77452108 31 77452109 31 77452107 31 77452108 31 77452109 31 77452107 31 77452107 31 77452108 31 77452109 31 77452107 31 77452107 31 77452108 32 77452109 33 77452101 34 77452100 35 77452100 36 93541010 37 10127104 38 93564004 39 93564004 39 93564004 39 93564004 30 10125103 30 77452100 31 0126400 32 02PACITOR, ELECTRO, 55 V, 6.8 μF	TB304 A/B ONLY TB304 C ONLY TB304 A/B ONLY

83319600 D 7-9



INDEX NO.	PART NUMBER	PART DESCRIPTION	NOTES	
3- 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16	77449800 77449700 94367113 94363100 95644206 94390300 94263900 94263900 94263903 92512153 92512258 92512158 94260300 93640022 94263904 94263901 94370801 10125103	FTU TYPE 5VKN COMPONENT ASSY TYPE 5VKN BOARD, BLANK L.E.D. LENS, DIFFUSED STANDOFF, THREADED, SWAGED BUSHING, INSULATING CONDUCTOR, NON-INSULATED SWITCH, TOGGLE, 3-POSITION: LOCKING UP/ DOWN, CENTER OFF SWITCH, TOGGLE, 3-POS MOMENTARY, CENTER OFF RESISTOR, 1/4 W, 100 OHMS RESISTOR, 1/4 W, 3.9K OHMS RESISTOR, 1/4 W, 1K OHMS SOCKET, IC, 14-PIN STUD, SELF-CLINCHING SWITCH, TOGGLE, 3-POS: LOCKING UP, MOMENTARY DOWN, CENTER OFF SWITCH, TOGGLE, SPDT SWITCH, ROTARY, 10-POSITION NUT, HEX, MACHINE, 4-40	QTY: 3 QTY: 2 QTY: 27 (SEE NOTE 1) QTY: 36 (SEE NOTE 1) QTY: 2 (SEE NOTE 1) QTY: 43 (SEE NOTE 2) QTY: 3	
		NOTES 1. TO REPLACE RESISTORS (INDEX NO'S 8,9,10), SEE CR301 THROUGH CR305 IN DIAGRAMS SECTION FOR RESISTANCE VALUE AND R-NUMBER, THEN REFER TO FIGURE 7-5 FOR LOCATION. 2. ALL TOGGLE SWITCHES NOT IDENTIFIED BY INDEX NUMBERS ARE INDEX NO. 14.		

83319600 C

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